Unit: mm

TOSHIBA Transistor Silicon PNP Epitaxial Type

# 2SA2058

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain:  $h_{FE} = 200$  to 500 ( $I_{C} = -0.2$  A)
- Low collector-emitter saturation voltage:  $V_{CE (sat)} = -0.19 \text{ V (max)}$
- High-speed switching:  $t_f = 25$  ns (typ.)

#### Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	-20	V	
Collector-emitter voltage		$V_{CEO}$	-10	V	
Emitter-base voltage		V <sub>EBO</sub>	-7	V	
Collector current	DC	Ic	-1.5	A	
	Pulse	I <sub>CP</sub>	-2.5		
Base current		Ι <sub>Β</sub>	-150	mA	
Collector power dissipation	DC	PC	500	mW	
	t = 10 s	(Note)	750		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	–55 to 150	°C	

1. Base
2. Emitter
3. Collector

JEDEC —

JEITA —

TOSHIBA 2-3S1A

Weight: 0.01 g (typ.)

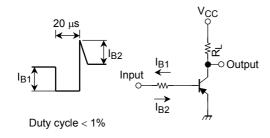
Note: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area:  $645 \text{ mm}^2$ )

### **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I <sub>CBO</sub>	$V_{CB} = -20 \text{ V}, I_E = 0$	_	_	-100	nA	
Emitter cut-off current		I <sub>EBO</sub>	$V_{EB} = -7 \text{ V}, I_{C} = 0$	_	_	-100	nA	
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = -10 \text{ mA}, I_B = 0$	-10	_	_	٧	
DC current gain		h <sub>FE</sub> (1)	$V_{CE} = -2 \text{ V}, I_{C} = -0.2 \text{ A}$	200	_	500		
		h <sub>FE</sub> (2)	$V_{CE} = -2 \text{ V}, I_{C} = -0.6 \text{ A}$	125	_	_		
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	$I_C = -0.6 \text{ A}, I_B = -20 \text{ mA}$	_	_	-0.19	٧	
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	$I_C = -0.6 \text{ A}, I_B = -20 \text{ mA}$	_	_	-1.10	V	
Collector output capacitance		C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	12	_	pF	
Switching time	Rise time	t <sub>r</sub>	See Figure 1 circuit diagram.	_	50	_	ns	
	Storage time	t <sub>stg</sub>	$V_{CC} \simeq -6 \text{ V}, R_L = 10 \Omega$	_	115	_		
	Fall time	t <sub>f</sub>	-l <sub>B1</sub> = l <sub>B2</sub> = −20 mA	_	25	_		

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## Marking



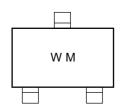
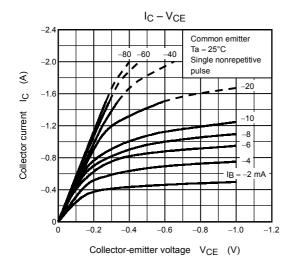
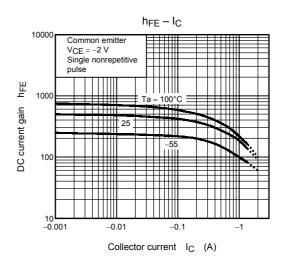
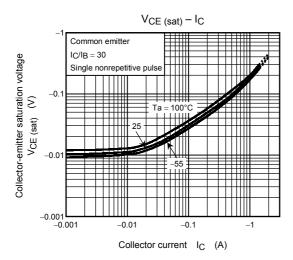
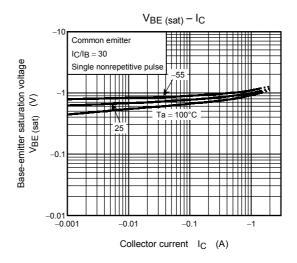


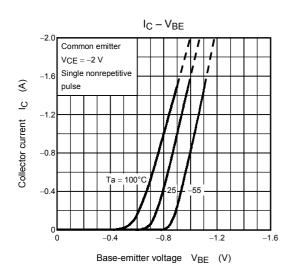
Figure 1 Switching Time Test Circuit & Timing Chart

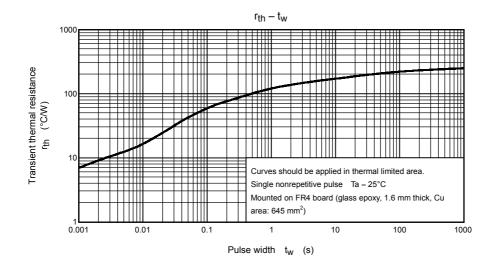


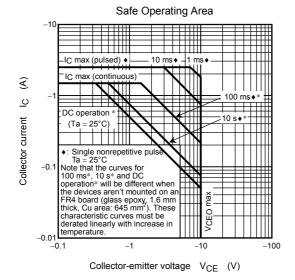












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