

STRUCTURE TYPE

Silicon Monolithic Integrated Circuit
Flexible Step-Down Switching Regulator

PRODUCT SERIES

BD9001F

FEATURES

- Wide input Range:7~48V
- High Precision(Reference Voltage) : $\pm 2\%$

- Integrated 2A P-ch Power MOS FET
- Adjustable Frequency:50~300KH z

○ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply Voltage	V_{IN}	50	V
SW Pin Voltage	V_{SW}	V_{IN}	V
Output SW Current	I_{SW}	2 ⁽¹⁾	A
EN Pin Voltage	V_{EN}	V_{IN}	V
RT, FB, INV Pin Voltage	V_{RT}, V_{FB}, V_{INV}	7	V
Power Dissipation	P_d	0.69 ⁽²⁾	W
Operating Temperature Range	T_{opr}	-40~+95	°C
Storage Temperature Range	T_{stg}	-55~+150	°C
Maximum Junction Temperature	T_{jmax}	150	°C

(1) Do not however exceed P_d .

(2) P_d derated at 5.52mW/°C for temperature above Ta=25°C, Mounted on a double layer PCB 70mm×70mm×1.6mm.

○ OPERATING CONDITIONS

Parameter	Symbol	Min.	Max.	Unit
Recommend Supply Voltage	V_{IN}	7	48	V
Output Switch Current	I_{SW}	-	2	A
Oscillator Frequency	F_{OSC}	50	300	kHz
Oscillator Timing Resistance	RT	100	800	kΩ
Output Voltage	V_o	1 or $V_{IN} \times 6\%$	V_{IN}	V

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* The product is not designed for protection against radioactive rays.

* Status of this document

The Japanese version of this document is the formal specification.

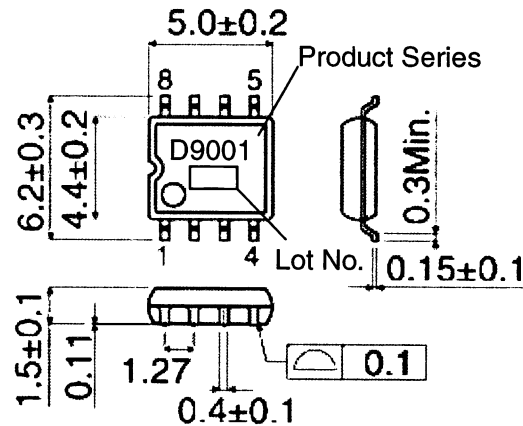
A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document, formal version takes priority.

○ ELECTRICAL CHARACTERISTICS (Unless otherwise specified, $T_a = -40 \sim 95^\circ\text{C}$, $V_{IN} = 13.2\text{V}$, $V_{EN} = 5\text{V}$)

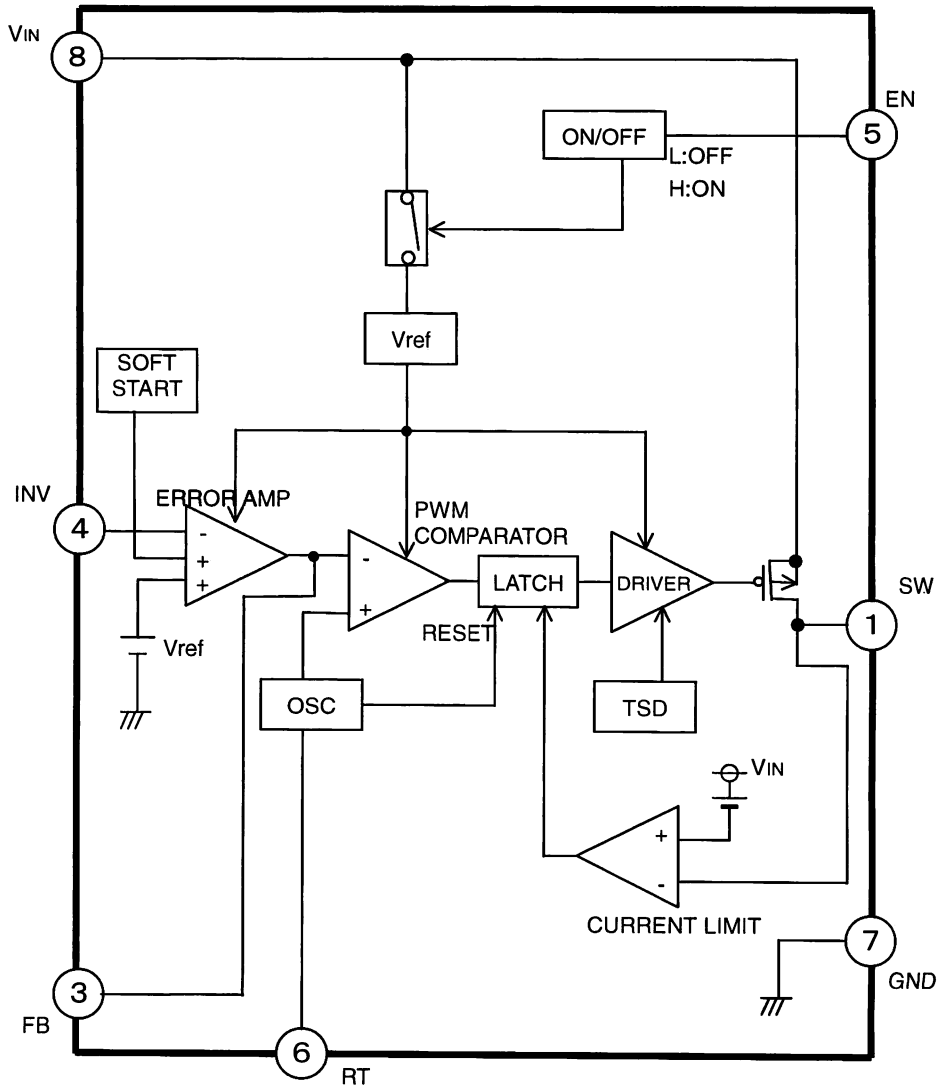
Parameter	Symbol	Limits			Unit	Condition
		Min.	Typ.	Max.		
[Entire Device]						
Stand-by Current	I _{STB}	-	4	10	μA	V _{EN} =0V, Ta=25℃
Quiescent Current	I _Q	-	3	4.2	mA	I _O =0A
[Switch]						
Switch On Resistance	R _{ON}	-	0.53	0.9	Ω	I _{SW} =50mA
Leakage Current	I _{OLEAK}	-	0	30	μA	V _{IN} =48V, V _{EN} =0V
[Error Amplifier]						
Reference Voltage1	V _{REF1}	0.98	1.00	1.02	V	V _{FB} =V _{INV} , Ta=25℃
Reference Voltage2	V _{REF2}	0.96	1.00	1.04	V	V _{FB} =V _{INV}
Input Bias Current	I _B	-1	-	-	μA	V _{INV} =1.1V
Maximum FB Voltage	V _{FBH}	2.4	2.5	-	V	V _{INV} =0.5V
Minimum FB Voltage	V _{FBL}	-	0.05	0.10	V	V _{INV} =1.5V
FB sink current	I _{FBSINK}	-5.0	-3.0	-0.5	mA	V _{FB} =1.5V, V _{INV} =1.5V
FB source current	I _{FBSOURCE}	70	120	170	μA	V _{FB} =1.5V, V _{INV} =0.5V
[Oscillator Section]						
Switching Frequency	F _{OSC}	82	102	122	kHz	RT=390kΩ
[Enable]						
Threshold Voltage	V _{EN}	0.8	1.7	2.6	V	
Sense Current	I _{EN}	-	13	50	μA	V _{EN} =5V

○ PHYSICAL DIMENSIONS • MARKING



SOP8 (Unit: mm)

○ BLOCK DIAGRAM



※Refer to the Technical Note about the details of the application.

○ Pin No. • Pin Name

Pin No.	Pin Name
1	SW
2	N.C.
3	FB
4	INV
5	EN
6	RT
7	GND
8	VIN

NOTES FOR USE

1. Absolute maximum range

Absolute Maximum Ratings are those values beyond which the life of a device may be destroyed we cannot be defined the failure mode, such as short mode or open mode.

Therefore physical security countermeasure, like fuse, is to be given when a specific mode to be beyond absolute maximum ratings is considered.

2. Operation supply voltage range

The circuit functionality is guaranteed within operation of ambient temperature range, as long as it is within operation supply voltage range. The standard electrical characteristic values are guaranteed at the test circuit voltage of $V_{IN}=13.2V$. They cannot be guaranteed at other voltages in the operating range of 7V-48V. However, the variation will be small.

3. Grounding

It is recommended that every capacitor (bypass and another capacitors) is grounded to PIN7 using single-point connections.

4. Input supply voltage

Input supply pattern layout should be as short as possible.

5. VIN Terminal

For reduce the influence of switching noise, bypass capacitor is connected between VIN and GND.

6. FB Terminal

The FB terminal is for phase margin of the DC/DC system. A capacitor and a resistor or an only capacitor placed between the FB terminal and the INV terminal. The values of the capacitor and the resistor shall be adjusted according to the output current and the output capacitor value. The output may be oscillating if the value of capacitor is not sufficient, also the transient response may become insufficient if the value is too large. Therefore, the value of the capacitor and the resistor shall be adequately set up based on the condition of the temperature, and so on. Since the FB terminal also detects output short condition compulsorily applying an external voltage onto the FB terminal must not be performed because it may activate the timer latch protection circuit.

7. Electromagnetic Fields

The IC is susceptible to strong electromagnetic fields and may cause malfunction. Therefore, caution should be used when placing it on the PCB.

8. Application Design

When designing the external circuit, included adequate margins, including not only steady state but also transient characteristics.

9. Over Output Current Protection

SW Output terminal has over current protection circuit of 4A, with prevents IC from being damage by short circuit at over current.

10. Thermal Shut Down Circuit

A temperature control is built in the IC to prevent the damage due to overheat. Therefore, the output is turned off when the thermal circuit works and are turned on when the temperature goes down to the specified level.

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Beijing	TEL : +86(10)8525-2483	FAX : +86(10)8525-2489
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Singapore	TEL : +65-6332-2322	FAX : +65-6332-5662
Malaysia / Kuala Lumpur	TEL : +60(3)7958-8355	FAX : +60(3)7958-8377
Philippines / Manila	TEL : +63(2)807-6872	FAX : +63(2)809-1422
Thailand / Bangkok	TEL : +66(2)254-4890	FAX : +66(2)256-6334

Japan /
(Internal Sales)

Tokyo	2-1-1, Yaesu, Chuo-ku, Tokyo 104-0082	
	TEL : +81(3)5203-0321	FAX : +81(3)5203-0300
Yokohama	2-4-8, Shin Yokohama, Kohoku-ku, Yokohama, Kanagawa 222-8575	
	TEL : +81(45)476-2131	FAX : +81(45)476-2128
Nagoya	Dainagayo Building 9F 3-28-12, Meieki, Nakamura-ku, Nagoya, Aichi 450-0002	
	TEL : +81(52)581-8521	FAX : +81(52)561-2173
Kyoto	579-32 Higashi Shiokouji-cho, Karasuma Nishi-iru, Shiokoujidori, Shimogyo-ku, Kyoto 600-8216	
	TEL : +81(75)311-2121	FAX : +81(75)314-6559

(Contact address for overseas customers in Japan)

Yokohama	TEL : +81(45)476-9270	FAX : +81(045)476-9271
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