

To our customers,

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HZM6.8Z4MFA

Silicon Planar Zener Diode for Surge Absorb

REJ03G0203-0100Z

Rev.1.00

Mar.29.2004

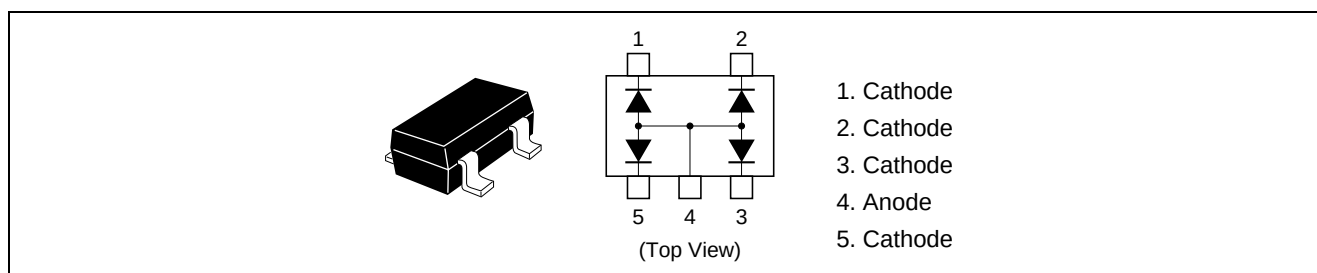
Features

- HZM6.8Z4MFA has four devices in a monolithic, and can absorb surge.
- Low capacitance ($C = 4.0 \text{ pF Typ} / 4.5 \text{ pF max}$) and can protect ESD of signal line.
- MPAK-5 Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM6.8Z4MFA	N2	MPAK-5

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Four device total, See Fig.2.

Electrical Characteristics *¹

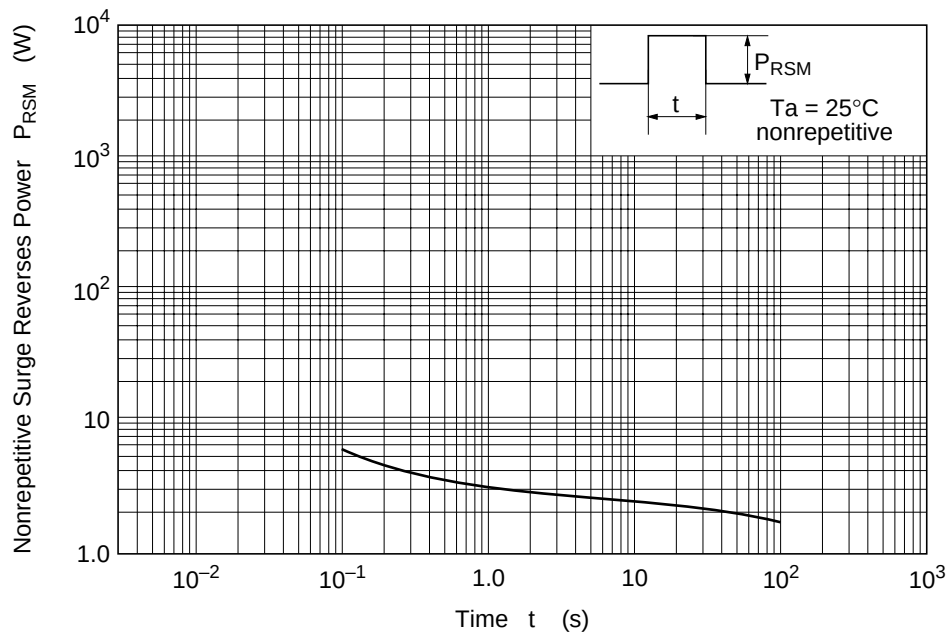
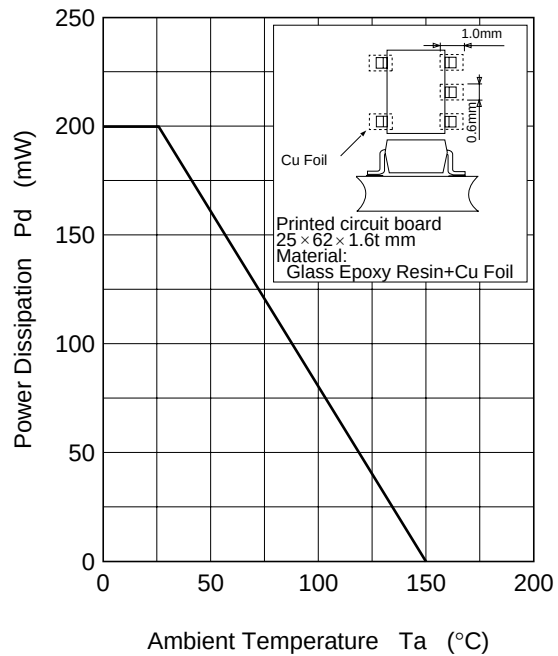
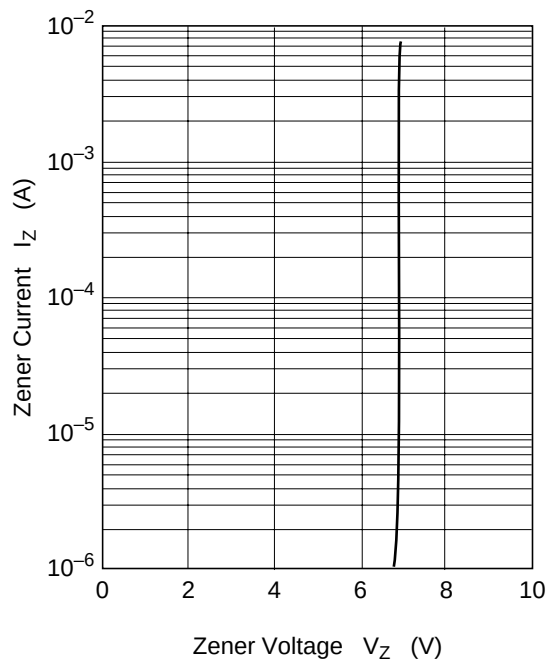
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _Z	6.47	—	7.00	V	I _Z = 5 mA, 40 ms pulse
Reverse current	I _R	—	—	2	μA	V _R = 3.5 V
Capacitance	C	—	4.0	4.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	—	—	30	Ω	I _Z = 5 mA
ESD-Capability * ²	—	8	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ; I_R > 2 μA at V_R = 3.5 V.

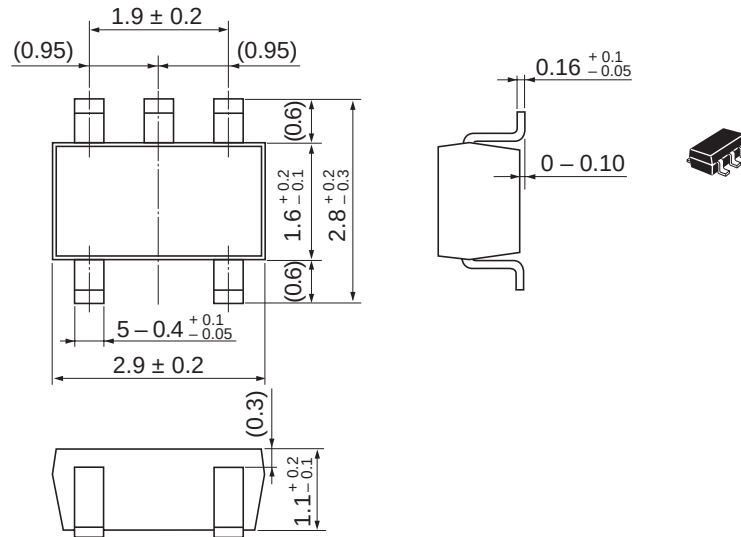
Main Characteristic



Package Dimensions

As of January, 2003

Unit: mm



Package Code	MPAK-5
JEDEC	—
JEITA	—
Mass (reference value)	0.013 g

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