#### TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7SB66FU

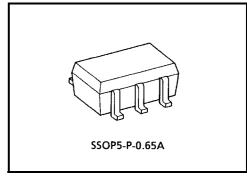
#### Single Bus Switch

The TC7SB66FU is a low on-resistance, high-speed CMOS 1-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

P-MOS and N-MOS channel block means the device is suitable for analog signal transmission.

All inputs are equipped with protector circuits to protect the device from static discharge.



Weight: 0.006 g (typ.)

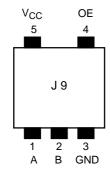
#### **Features**

- Operating voltage:  $V_{CC} = 2 \sim 5.5 \text{ V}$
- High speed operation:  $t_{pd} = 0.25 \text{ ns (max)}$
- Ultra-low on resistance:  $RON = 5 \Omega$  (typ.)

±2000 V or more (MIL)

- High noise margin:  $V_{NIL} = V_{NIH} = 28\% V_{CC}$  (min)
- Power-down protection for inputs (control inputs only)
- · Package: USV

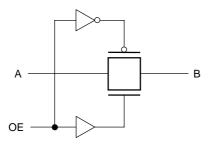
#### Pin Assignment (top view)



### **Truth Table**

| Inputs | Function        |
|--------|-----------------|
| OE     | Tanction        |
| Н      | A port = B port |
| L      | Disconnect      |

# **System Diagram**



# **Maximum Ratings**

| Chara                           | cteristics        | Symbol                            | Rating                     | Unit |  |
|---------------------------------|-------------------|-----------------------------------|----------------------------|------|--|
| Power supply volt               | age               | V <sub>CC</sub>                   | -0.5~7.0                   | V    |  |
| Control pin input v             | voltage           | V <sub>IN</sub>                   | -0.5~7.0                   | V    |  |
| Switch terminal I/O voltage     |                   | Vs                                | -0.5~V <sub>CC</sub> + 0.5 | V    |  |
| Clump diode current             | Control input pin | luz                               | -50                        | mA   |  |
|                                 | Switch terminal   | lık                               | ±50                        | IIIA |  |
| Switch I/O current              |                   | IS                                | 128                        | mA   |  |
| Power dissipation               |                   | PD                                | 200                        | mW   |  |
| DC V <sub>CC</sub> /GND current |                   | I <sub>CC</sub> /I <sub>GND</sub> | ±100                       | mA   |  |
| Storage temperature             |                   | T <sub>stg</sub>                  | -65~150                    | °C   |  |

# **Recommended Operating Conditions**

| Characteristics                  | Symbol           | Rating            | Unit |
|----------------------------------|------------------|-------------------|------|
| Power supply voltage             | V <sub>CC</sub>  | 2.0~5.5           | V    |
| Control pin input voltage        | V <sub>IN</sub>  | 0~5.5             | V    |
| Switch I/O voltage               | Vs               | 0~V <sub>CC</sub> | V    |
| Operating temperature            | T <sub>opr</sub> | -40~85            | °C   |
| Control pin input rise/fall time | dt/dv            | 0~10              | ns/V |

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#### **Electrical Characteristics**

### DC Characteristics ( $Ta = -40 \sim 85$ °C)

| Characteristics                   |  | Symbol          | ol Test Condition                                |                     | Min                         | Typ.                         | Max  | Unit |
|-----------------------------------|--|-----------------|--|---------------------|-----------------------------|------------------------------|------|------|
|                                   |  |                 |  | V <sub>CC</sub> (V) |                             | (Note 1)                     |      |      |
| Control pin input                 | "H" level                                      | $V_{IH}$        | _  | 2.0~5.5             | $^{0.7\times}_{\text{VCC}}$ | _                            | _    | V    |
| voltage "L" level V <sub>IL</sub> |  | _               | 2.0~5.5  |                     | _                           | $^{\rm 0.3\times}_{\rm VCC}$ | V    |      |
| Control pin input l               | eakage   | I <sub>IN</sub> | V <sub>IN</sub> = 0~5.5 V                        | 2.0~5.5             | _                           | _                            | ±1.0 | μΑ   |
| Off-state leakage                 | current  | I <sub>SZ</sub> | A, B = 0~V <sub>CC</sub> , OE = GND              | 2.0~5.5             | _                           | _                            | ±1.0 | μА   |
| (switch off)                      |  |                 | 1,, 2 0 1,00, 02 0.12                            |                     |                             |                              |      | μ    |
|                                   |  | R <sub>ON</sub> | $V_{IS} = 0 \text{ V}, I_{IS} = 30 \text{ mA}$   | 4.5                 |                             | 3                            | 7    |      |
|                                   |  |                 | $V_{IS} = 4.5 \text{ V}, I_{IS} = 30 \text{ mA}$ | 4.5                 |                             | 5                            | 15   |      |
|                                   |  |                 | $V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$ | 4.5                 | _                           | 6                            | 12   |      |
| ON resistance (Note 2)            | V <sub>IS</sub> = 0 V, I <sub>IS</sub> = 24 mA |                 | 3.0  | _                   | 4                           | 9                            | Ω    |      |
|                                   | V <sub>IS</sub> = 3 V, I <sub>IS</sub> = 24 mA |                 | 3.0  | _                   | 7                           | 20                           |      |      |
|                                   | V <sub>IS</sub> = 0 V, I <sub>IS</sub> = 8 mA  |                 | 2.0  | _                   | 6                           | 12                           |      |      |
|                                   |  |                 | V <sub>IS</sub> = 2 V, I <sub>IS</sub> = 8 mA    | 2.0                 | _                           | 10                           | 30   |      |
| Quiescent supply                  | current  | I <sub>CC</sub> | $V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$          | 5.5                 |                             | _                            | 10   | μΑ   |

Note 1: The typical values are at Ta = 25°C.

Note 2: Apply the specified current to the switch, then measure the voltages on pins A and B. The on-resistance is the lower of the two.

### AC Characteristics ( $Ta = -40 \sim 85$ °C)

| Characteristics                 | Symbol                  | Test Condition              | V <sub>CC</sub> (V) | Min | Max  | Unit |
|---------------------------------|-------------------------|-----------------------------|---------------------|-----|------|------|
| Propagation delay time          | <b></b>                 |                             | 2.0                 | _   | 0.5  |      |
| (bus to bus)                    | t <sub>pLH</sub>        | Figure 1, Figure 2 (Note 3) | $3.3 \pm 0.3$       |     | 0.35 | ns   |
| (bus to bus)                    | t <sub>pHL</sub>        |                             | $5.0\pm0.5$         | _   | 0.25 |      |
|                                 | Output enable time tpZH | Figure 1, Figure 3          | 2.0                 |     | 8    |      |
| Output enable time              |                         |                             | $3.3 \pm 0.3$       | _   | 5    | ns   |
|                                 |                         |                             | $5.0 \pm 0.5$       |     | 4.5  |      |
| Output disable time   tpLZ tpHZ | <b>.</b>                | Figure 1, Figure 3          | 2.0                 | _   | 8    |      |
|                                 | -                       |                             | $3.3 \pm 0.3$       | _   | 6.5  | ns   |
|                                 | чрНΖ                    |                             | $5.0 \pm 0.5$       | _   | 5    |      |

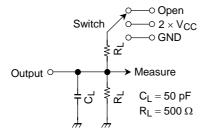
Note 3: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

### **Capacitive Characteristics (Ta = 25°C)**

| Characteristics               | Symbol           | Test Condition |          | V <sub>CC</sub> (V) | Тур. | Unit |
|-------------------------------|------------------|----------------|----------|---------------------|------|------|
| Control pin input capacitance | C <sub>IN</sub>  |                | (Note 4) | 5.0                 | 3    | pF   |
| Switch terminal capacitance   | C <sub>I/O</sub> | OE = GND       | (Note 4) | 5.0                 | 10   | pF   |

Note 4: Guaranteed by design.

#### **AC Test Circuit**



| Parameter                           | Switch            |  |  |
|-------------------------------------|-------------------|--|--|
| t <sub>pLH</sub> , t <sub>pHL</sub> | Open              |  |  |
| t <sub>pLZ</sub> , t <sub>pZL</sub> | $2 \times V_{CC}$ |  |  |
| t <sub>pHZ</sub> , t <sub>pZH</sub> | GND               |  |  |

Figure 1

#### **AC Waveform**

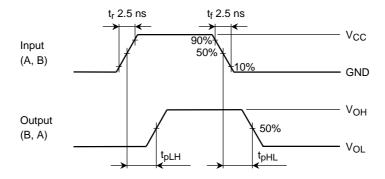
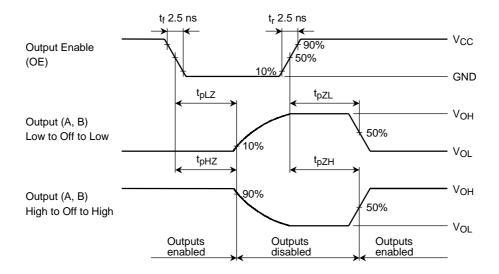


Figure 2 tpLH, tpHL

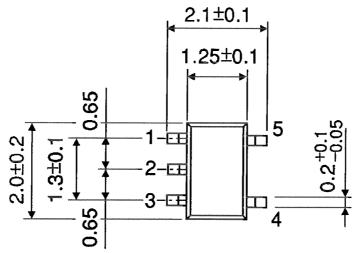


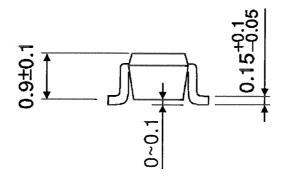
 $Figure \ 3 \quad t_{pLZ}, \, t_{pHZ}, \, t_{pZL}, \, t_{pZH}$ 

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# **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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