

2SK3419

Silicon N Channel MOS FET
High Speed Power Switching

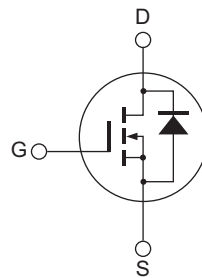
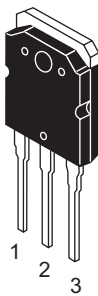
REJ03G1099-0200
(Previous: ADE-208-942)
Rev.2.00
Sep 07, 2005

Features

- Low on-resistance
 $R_{DS(on)} = 4.3 \text{ m}\Omega$ typ.
- 4 V gate drive device
- High speed switching

Outline

RENESAS Package code: PRSS0004ZE-A
(Package name: TO-3P)



1. Gate
2. Drain (Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DS}	60	V
Gate to source voltage	V_{GS}	±20	V
Drain current	I_D	90	A
Drain peak current	$I_{D(pulse)}$ ^{Note 1}	360	A
Body-drain diode reverse drain current	I_{DR}	90	A
Avalanche current	I_{AP} ^{Note 3}	65	A
Avalanche energy	E_{AR} ^{Note 3}	362	mJ
Channel dissipation	P_{ch} ^{Note 2}	150	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$ 2. Value at $T_c = 25^\circ C$ 3. Value at $T_{ch} = 25^\circ C$, $R_g \geq 50 \Omega$

Electrical Characteristics

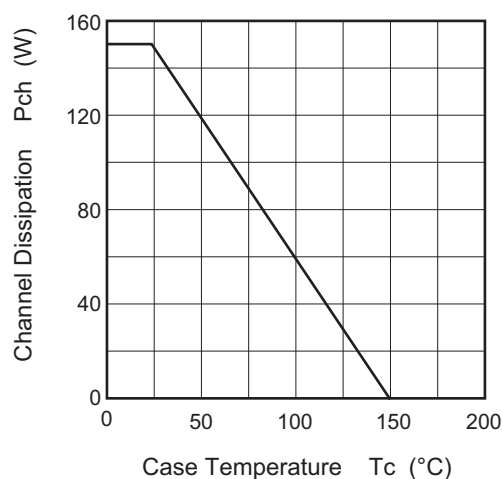
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 60 \text{ V}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	±0.1	μA	$V_{GS} = \pm 20 \text{ V}$, $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$ ^{Note 4}
Forward transfer admittance	$ y_{fs} $	55	90	—	S	$I_D = 45 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note 4}
Static drain to source on state resistance	$R_{DS(on)}$	—	4.3	5.5	m Ω	$I_D = 45 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note 4}
	$R_{DS(on)}$	—	6.0	9.0	m Ω	$I_D = 45 \text{ A}$, $V_{GS} = 4 \text{ V}$ ^{Note 4}
Input capacitance	C_{iss}	—	9770	—	pF	$V_{DS} = 10 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	1340	—	pF	
Reverse transfer capacitance	C_{rss}	—	470	—	pF	
Total gate charge	Q_g	—	180	—	nC	$V_{DD} = 50 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 90 \text{ A}$
Gate to source charge	Q_{gs}	—	32	—	nC	
Gate to drain charge	Q_{gd}	—	36	—	nC	
Turn-on delay time	$t_{d(on)}$	—	53	—	ns	$V_{GS} = 10 \text{ V}$ $I_D = 45 \text{ A}$ $R_L = 0.67 \Omega$
Rise time	t_r	—	320	—	ns	
Turn-off delay time	$t_{d(off)}$	—	700	—	ns	
Fall time	t_f	—	380	—	ns	
Body-drain diode forward voltage	V_{DF}	—	1.0	—	V	$I_F = 90 \text{ A}$, $V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	75	—	ns	$I_F = 90 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 50 \text{ A}/\mu s$

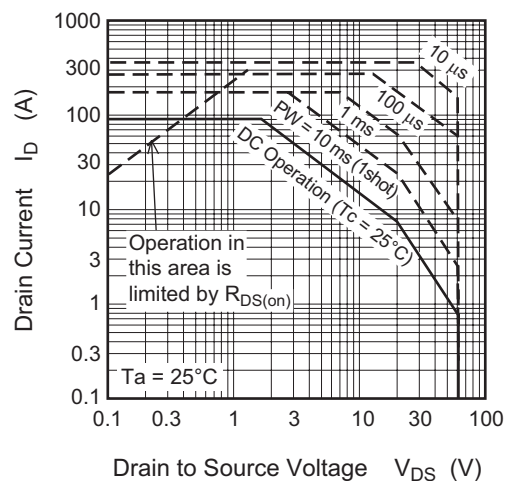
Note: 4. Pulse test

Main Characteristics

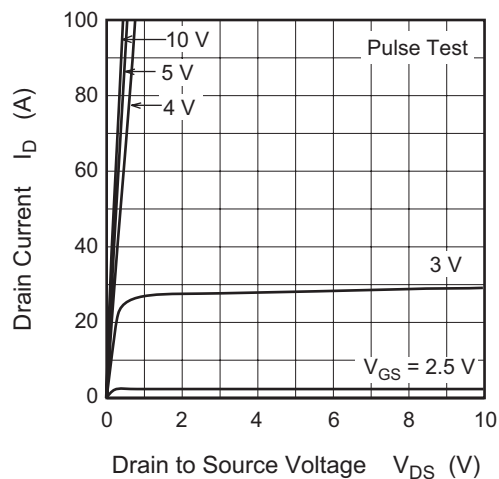
Power vs. Temperature Derating



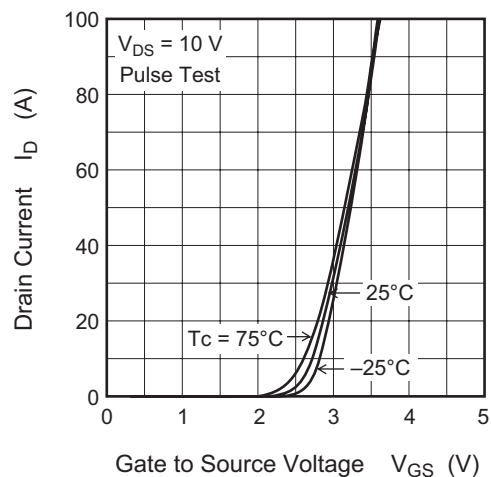
Maximum Safe Operation Area



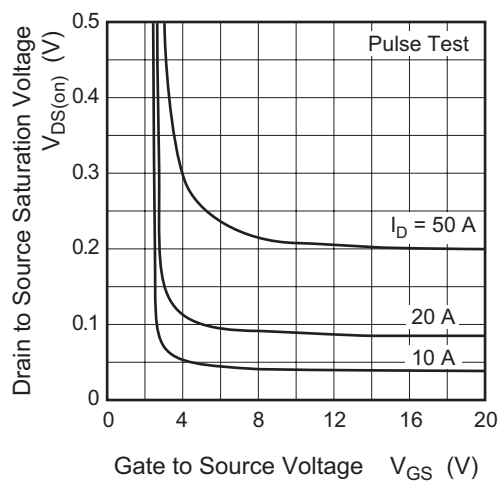
Typical Output Characteristics



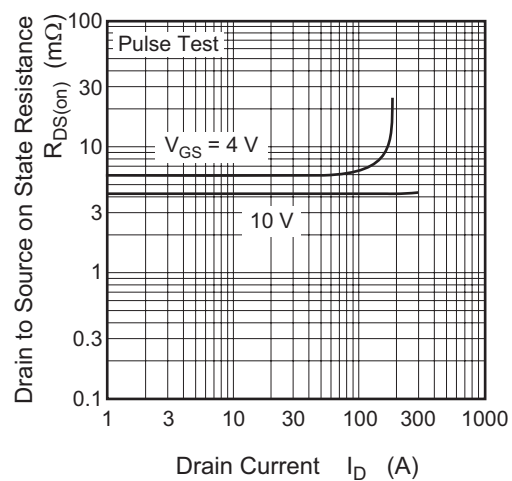
Typical Transfer Characteristics

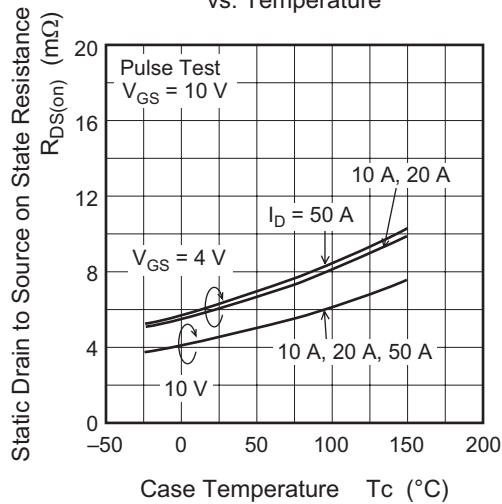
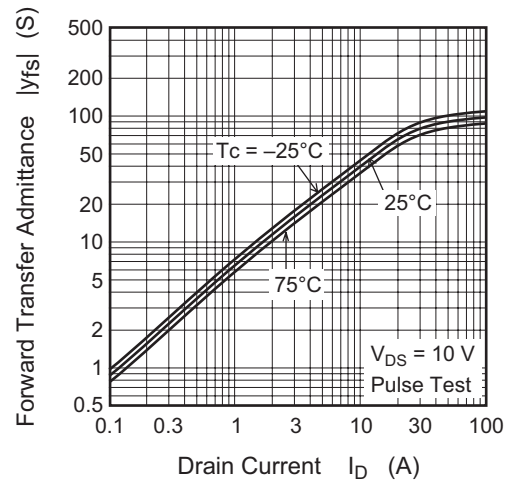
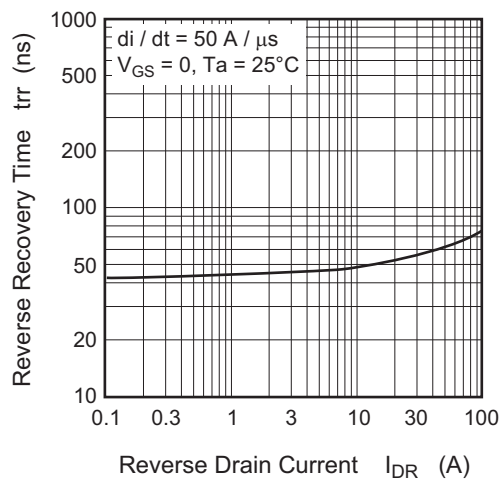
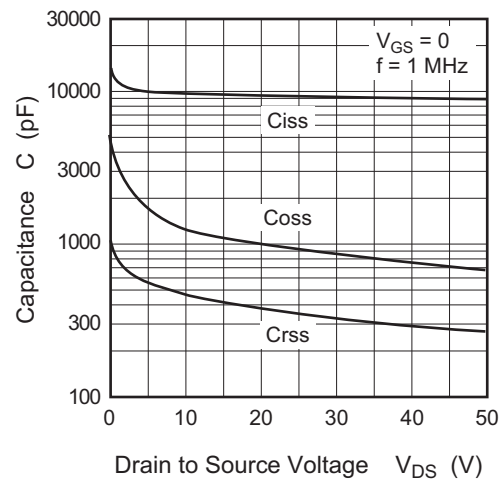


Drain to Source Saturation Voltage vs. Gate to Source Voltage

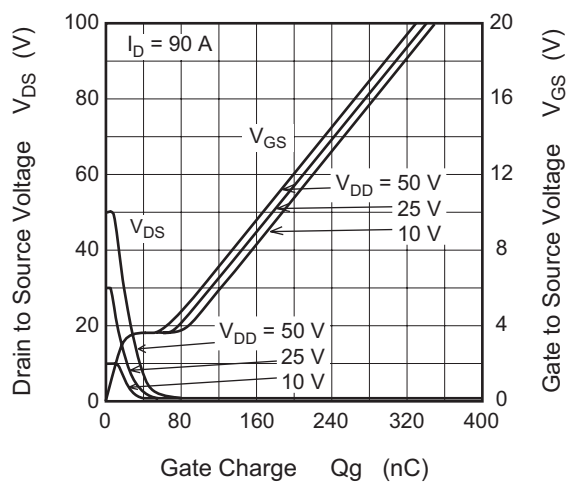


Static Drain to Source on State Resistance vs. Drain Current

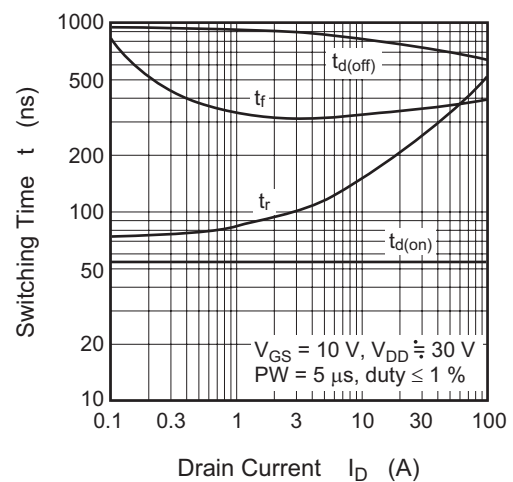


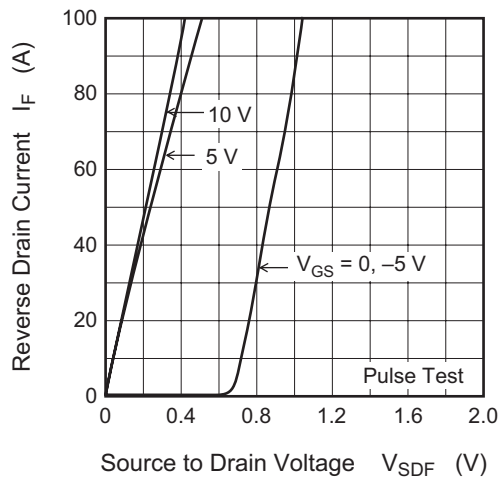
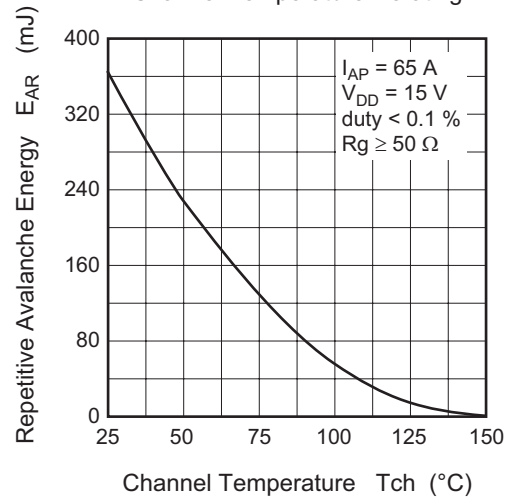
Static Drain to Source on State Resistance
vs. TemperatureForward Transfer Admittance vs.
Drain CurrentBody-Drain Diode Reverse
Recovery TimeTypical Capacitance vs.
Drain to Source Voltage

Dynamic Input Characteristics

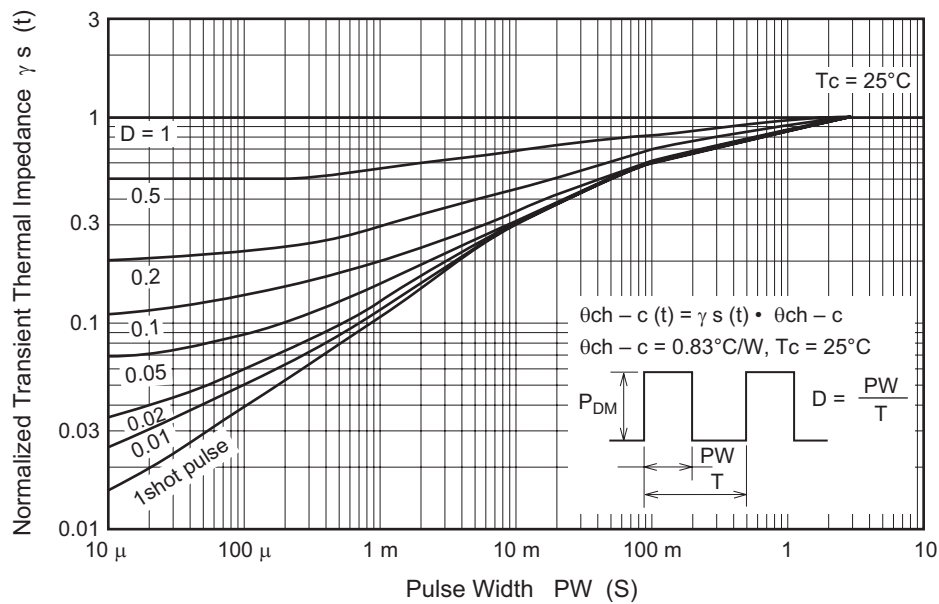


Switching Characteristics

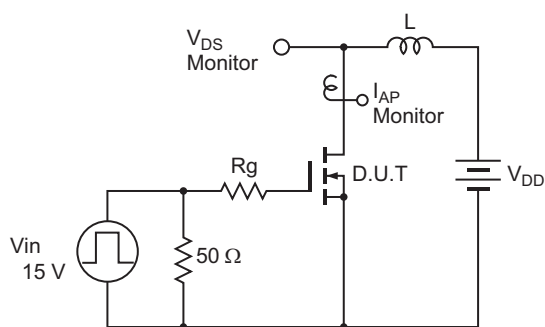


Reverse Drain Current vs.
Source to Drain VoltageMaximum Avalanche Energy vs.
Channel Temperature Derating

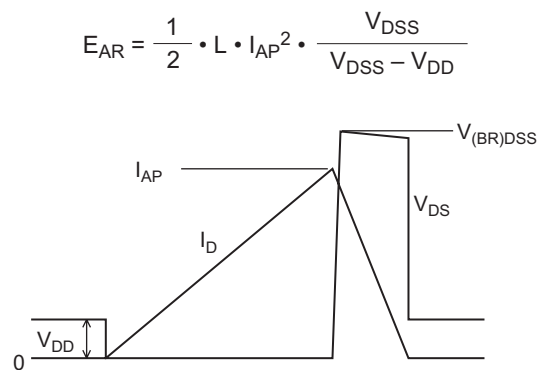
Normalized Transient Thermal Impedance vs. Pulse Width



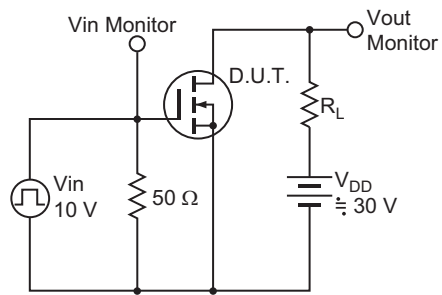
Avalanche Test Circuit



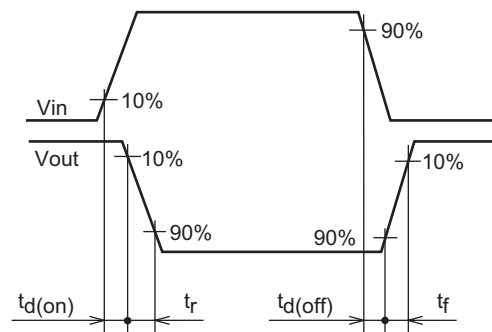
Avalanche Waveform



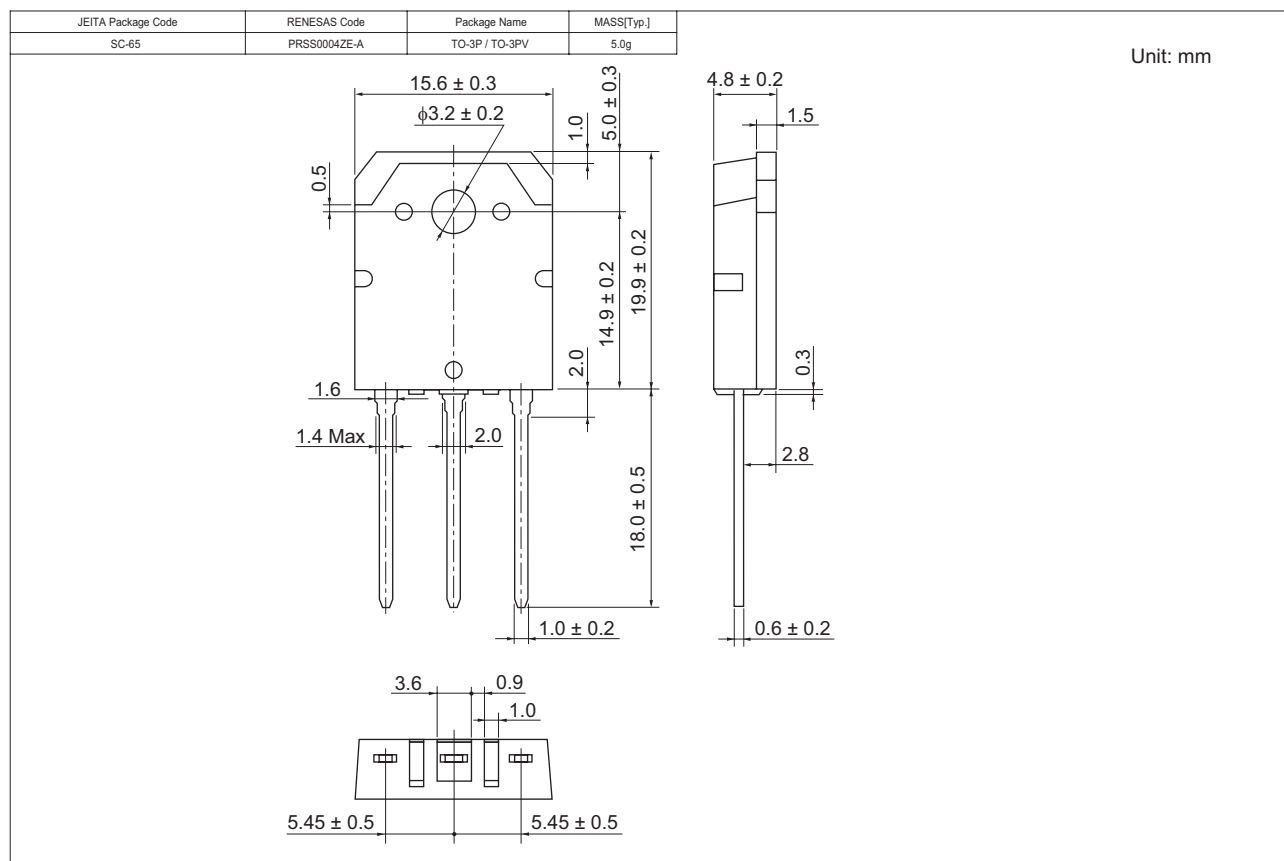
Switching Time Test Circuit



Switching Time Waveform



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK3419-E	30 pcs	Plastic magazine

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