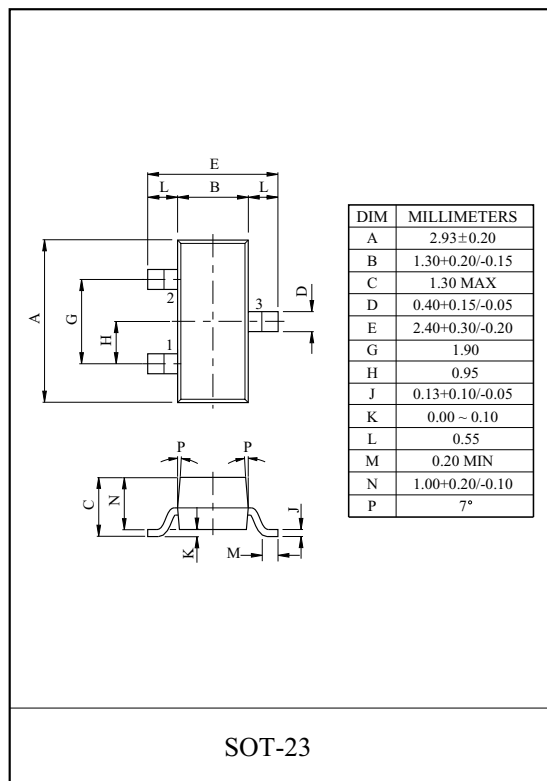


### General Description

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for portable equipment and SMPS.

### FEATURES

- $V_{DS}=60V$ ,  $I_D=2A$
- Drain-Source ON Resistance  
 $R_{DS(ON)}=160m\ \Omega$  (Max.) @  $V_{GS}=10V$   
 $R_{DS(ON)}=220m\ \Omega$  (Max.) @  $V_{GS}=4.5V$
- Super Hige Dense Cell Design

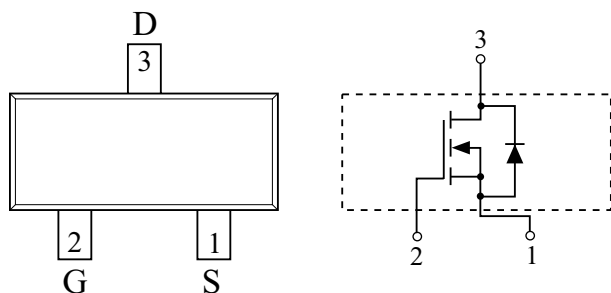


### MAXIMUM RATING (Ta=25 °C)

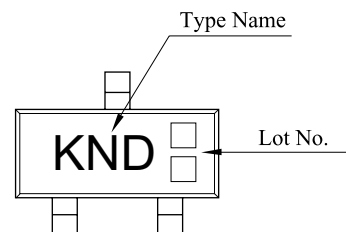
| CHARACTERISTIC                          |                          | SYMBOL     | N-Ch      | UNIT |
|-----------------------------------------|--------------------------|------------|-----------|------|
| Drain-Source Voltage                    |                          | $V_{DSS}$  | 60        | V    |
| Gate-Source Voltage                     |                          | $V_{GSS}$  | ±20       | V    |
| Drain Current                           | DC@T <sub>A</sub> =25 °C | $I_D$      | 2.0       | A    |
|                                         | DC@T <sub>A</sub> =70 °C |            | 1.6       |      |
|                                         | Pulsed                   | $I_{DP}$   | 10        |      |
| Drain-Source-Diode Forward Current      |                          | $I_S$      | 1.0       | A    |
| Drain Power Dissipation                 | T <sub>A</sub> =25 °C    | $P_D$      | 1.25      | W    |
|                                         | T <sub>A</sub> =70 °C    |            | 0.8       |      |
| Maximum Junction Temperature            |                          | $T_j$      | 150       | °C   |
| Storage Temperature Range               |                          | $T_{stg}$  | -55 ~ 150 | °C   |
| Thermal Resistance, Junction to Ambient |                          | $R_{thJA}$ | 100       | °C/W |

Note : Package Mounted on 99.5% Alumina (10 × 8 × 0.6mm)

### PIN CONNECTION (TOP VIEW)



### Marking



# KMB2D0N60SA

## ELECTRICAL CHARACTERISTICS (Ta=25℃)

| CHARACTERISTIC                                                      | SYMBOL         | TEST CONDITION                                                             | MIN. | TYP. | MAX.      | UNIT       |
|---------------------------------------------------------------------|----------------|----------------------------------------------------------------------------|------|------|-----------|------------|
| Static                                                              |                |                                                                            |      |      |           |            |
| Drain-Source Breakdown Voltage                                      | $BV_{DSS}$     | $I_{DS}=250\mu A$ , $V_{GS}=0V$ ,                                          | 60   | -    | -         | V          |
| Drain Cut-off Current                                               | $I_{DSS}$      | $V_{GS}=0V$ , $V_{DS}=60V$                                                 | -    | -    | 0.5       | $\mu A$    |
|                                                                     |                | $V_{GS}=0V$ , $V_{DS}=60V$ , $T_j=55\text{ }^{\circ}C$                     | -    | -    | 10        |            |
| Gate Leakage Current                                                | $I_{GSS}$      | $V_{GS}=\pm 20V$ , $V_{DS}=0V$                                             | -    | -    | $\pm 100$ | nA         |
| Gate Threshold Voltage                                              | $V_{th}$       | $V_{DS}=V_{GS}$ , $I_D=250\mu A$                                           | 1.5  | -    | -         | V          |
| 2Drain-Source ON Resistance                                         | $R_{DS(ON)}^*$ | $V_{GS}=10V$ , $I_D=2A$                                                    | -    | 125  | 160       | m $\Omega$ |
|                                                                     |                | $V_{GS}=4.5V$ , $I_D=1.7A$                                                 | -    | 155  | 220       |            |
| On-State Drain Current                                              | $I_{D(ON)}^*$  | $V_{GS}\geq 10V$ , $V_{DS}=4.5V$                                           | 6    | -    | -         |            |
|                                                                     |                | $V_{GS}\geq 4.5V$ , $V_{DS}=4.5V$                                          | 4    | -    | -         |            |
| Forward Transconductance                                            | $g_{fs}^*$     | $V_{DS}=4.5V$ , $I_D=2.0A$                                                 | -    | 4.6  | -         | S          |
| Dynamic                                                             |                |                                                                            |      |      |           |            |
| Input Capacitance                                                   | $C_{iss}$      | $V_{DS}=25V$ , $f=1MHz$ , $V_{GS}=0V$                                      | -    | 240  | -         | pF         |
| Ouput Capacitance                                                   | $C_{oss}$      |                                                                            | -    | 50   | -         |            |
| Reverse Transfer Capacitance                                        | $C_{rss}$      |                                                                            | -    | 15   | -         |            |
| Total Gate Charge                                                   | $Q_g^*$        | $V_{DS}=30V$ , $V_{GS}=10V$ , $I_D=2A$                                     | -    | 4.8  | 10        | nC         |
| Gate-Source Charge                                                  | $Q_{gs}^*$     |                                                                            | -    | 0.8  | -         |            |
| Gate-Drain Charge                                                   | $Q_{gd}^*$     |                                                                            | -    | 1.0  | -         |            |
| Turn-On Delat Time                                                  | $t_{d(on)}^*$  | $V_{DD}=30V$ , $V_{GS}=4.5V$<br>$I_D=-1A$ , $R_G=6\text{ }\Omega$ (NOTE 1) | -    | 7    | 15        | ns         |
| Turn-On Rise Time                                                   | $t_r^*$        |                                                                            | -    | 10   | 20        |            |
| Turn-On Deley Time                                                  | $t_{d(off)}^*$ |                                                                            | -    | 17   | 35        |            |
| Turn-On Fall Time                                                   | $t_f^*$        |                                                                            | -    | 6    | 15        |            |
| Source-Drain Diode Ratings                                          |                |                                                                            |      |      |           |            |
| Source-Drain Forward Voltage                                        | $V_{SDF}^*$    | $V_{GS}=0V$ , $I_{DR}=1A$                                                  | -    | 0.77 | 1.2       | V          |
| NOTE 1> * : Pulse Test : Pulse width <300 $\mu s$ , Duty cycle < 2% |                |                                                                            |      |      |           |            |

# KMB2D0N60SA

Fig1.  $I_D$  -  $V_{DS}$

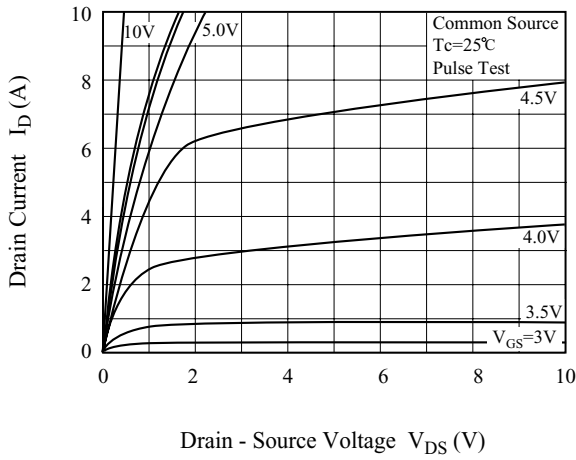


Fig2.  $R_{DS(ON)}$  -  $I_D$

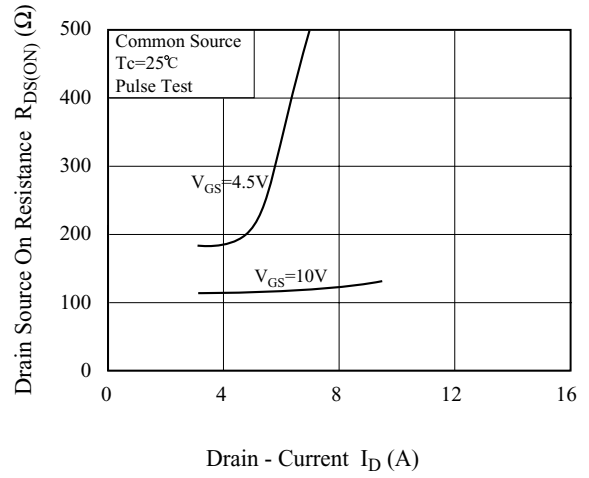


Fig3.  $I_D$  -  $V_{GS}$

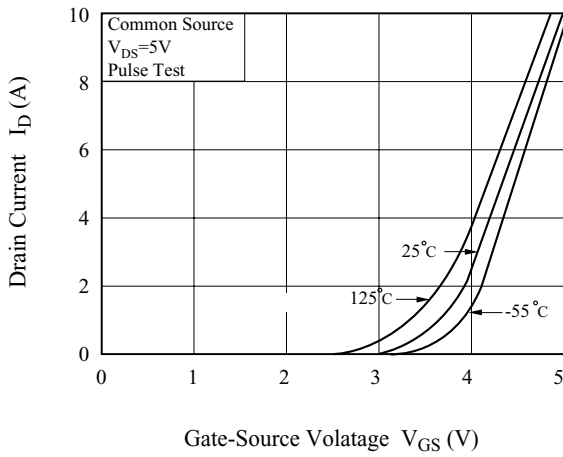


Fig4.  $R_{DS(ON)}$  -  $T_j$

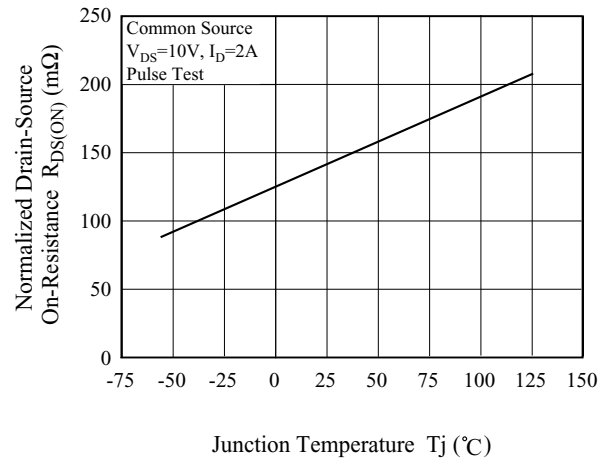


Fig5.  $V_{th}$  -  $T_j$

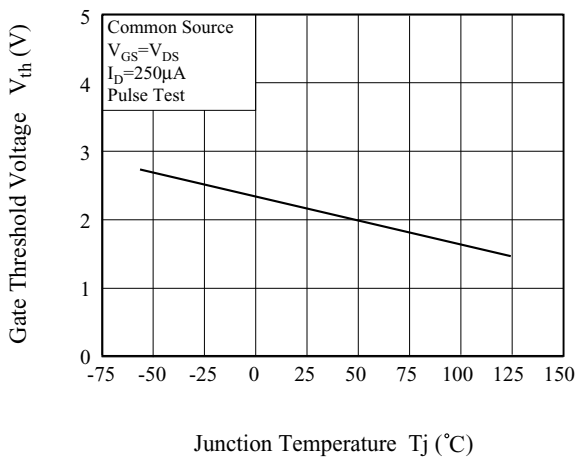


Fig6.  $I_S$ - $V_{SDF}$

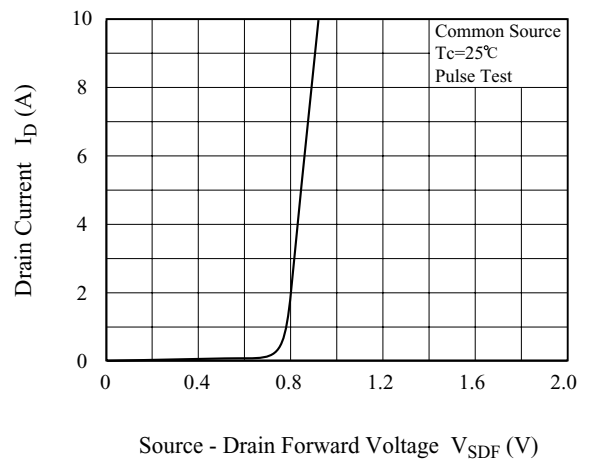


Fig7. Transient Thermal Response Curve

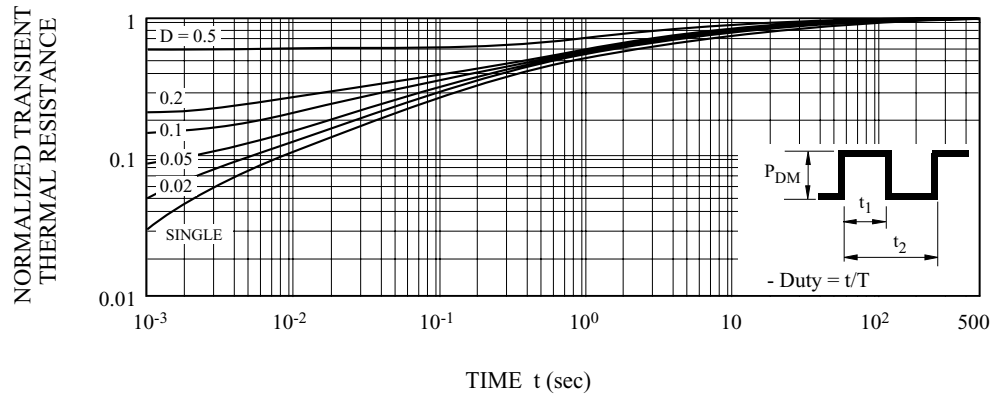


Fig8. Gate Charge Circuit and Wave Form

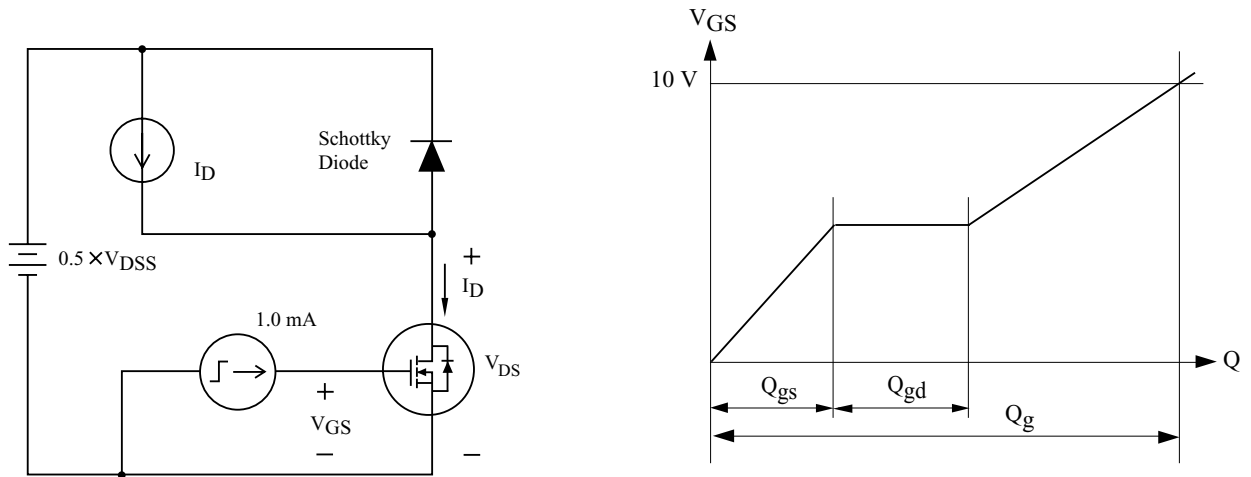


Fig9. Resistive Load Switching

