

August 30, 2010

Silicon Errata for the CY4672 Reference Design Kit

This document describes the errata for the CY4672 reference design kit (RDK). Details include errata trigger conditions, scope of impact, available workarounds, and silicon revision applicability. Compare this document to the device's datasheet for a complete functional description. While evaluating this kit, if you discover issues additional to those that are not listed in this document, use the contact information provided on this kit's cover letter to contact Cypress.

Contact your local Cypress sales representative if you have questions.

Part Numbers Affected

Part Number	Kit Revision
CY4672	B

CY4672 Reference Design Kit Qualification Status

In Production

CY4672 Errata Summary

This table defines the errata applicability to available CY4672 family of devices.

Items	[Part Number]	Silicon Revision	Fix Status
[1] The RDK is susceptible to zero length packets.	[X]	*B	Use workaround
[2] TMRCLKCR configuration for the CYRF69213 does not match the setting in the Device Editor view.	[X]	*B	Use workaround
[3] CY4672 RDK can be programmed only with the MiniProg, not with the ICE-Cube	[X]	*B	Use workaround
[4] Watchdog timer resets the controller before watchdog timer is cleared by application / protocol firmware.	[X]	*B	Use workaround

1. The RDK is susceptible to zero length packets

- **PROBLEM DEFINITION**
The RDK is susceptible to zero length packets.
- **PARAMETERS AFFECTED**
NA
- **TRIGGER CONDITION(S)**
When a zero length packet is used in an application, there is potential for the receive process to malfunction, causing the watchdog timer to reset the processor.
- **SCOPE OF IMPACT**
NA
- **WORKAROUND**
Keep the watchdog timer enabled in the event that an erroneous zero length packet is received.
- **FIX STATUS**
None. Use workaround.

2. TMRCLKCR configuration for the CYRF69213 does not match the setting in the Device Editor view.

- **PROBLEM DEFINITION**
In PSoC Designer™, the TMRCLKCR configuration for the CYRF69213 does not match the setting in the 'Device Editor' view.
- **PARAMETERS AFFECTED**
NA
- **TRIGGER CONDITION(S)**
NA
- **SCOPE OF IMPACT**
NA
- **WORKAROUND**
The TMRCLKCR should be configured in the application code.
- **FIX STATUS**
None. Use workaround.

3. CY4672 RDK can be programmed only with the MiniProg, not with the ICE-Cube.

- **PROBLEM DEFINITION**
Because of a known issue with the ICE-Cube, the CY4672 RDK can be programmed only with the MiniProg, not with the ICE-Cube. In the debug mode, the ICE-Cube works fine with the CY4672 RDK.
- **PARAMETERS AFFECTED**
NA
- **TRIGGER CONDITION(S)**
NA
- **SCOPE OF IMPACT**
NA
- **WORKAROUND**
Use MiniProg for programming
- **FIX STATUS**
None. Use workaround.

4. Watchdog timer resets the controller before the watchdog timer is cleared by application/protocol firmware.

▪ **PROBLEM DEFINITION**

The LPOSCTR register is initialized in 'TimerInit' function in the *timer.c* file of mouse and keyboard firmware. This initialization is not required as it is initialized during manufacturing settings.

▪ **PARAMETERS AFFECTED**

NA

▪ **TRIGGER CONDITION(S)**

NA

▪ **SCOPE OF IMPACT**

NA

▪ **WORKAROUND**

The LPOSCTR register is initialized in the 'TimerInit' function in the *timer.c* file of the keyboard and mouse firmware. Remove the initialization code prior to building the project. The *timer.c* file is present in the following directories:

- CY4672 RDK v1.0\firmware\Source Code\Keyboard
- CY4672 RDK v1.0\firmware\Source Code\Mouse

▪ **FIX STATUS**

None. Use workaround.

Document History Page

Document Title: Silicon Errata for the CY4672 Reference Design Kit Document Number: 001-63873			
Rev.	ECN No.	Orig. of Change	Description of Change
**	3018453	DATT	New errata for the CY4672 kit..

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