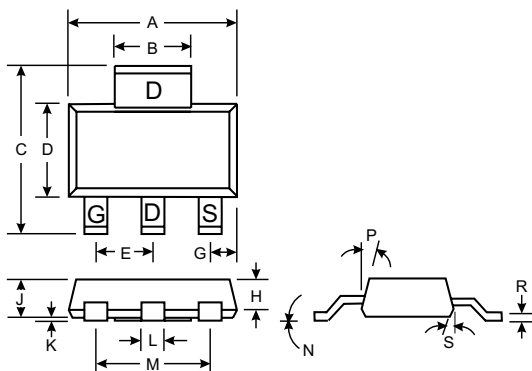


**P-CHANNEL ENHANCEMENT MODE
FIELD EFFECT TRANSISTOR**
Features

- High Cell Density DMOS Technology
- Low On-State Resistance
- High Power and Current Capability
- Fast Switching Speed
- High Transient Tolerance



SOT-223		
Dim	Min	Max
A	6.30	6.71
B	2.90	3.10
C	6.71	7.29
D	3.30	3.71
E	2.22	2.35
G	0.92	1.00
H	1.10	1.30
J	1.55	1.80
K	0.025	0.102
L	0.66	0.79
M	4.55	4.70
N	—	10°
P	10°	16°
R	0.254	0.356
S	10°	16°
All Dimensions in mm		

Mechanical Data

- SOT-223 Plastic Case
- Terminal Connections: See Outline Drawing and Internal Circuit Diagram Above

Maximum Ratings 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current	I_D	-2.5	A
Note 1a Continuous Pulsed		-15	
Maximum Power Dissipation	P_d	3.0	W
Note 1a		1.3	
Note 1b		1.1	
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +150	°C

Thermal Characteristics

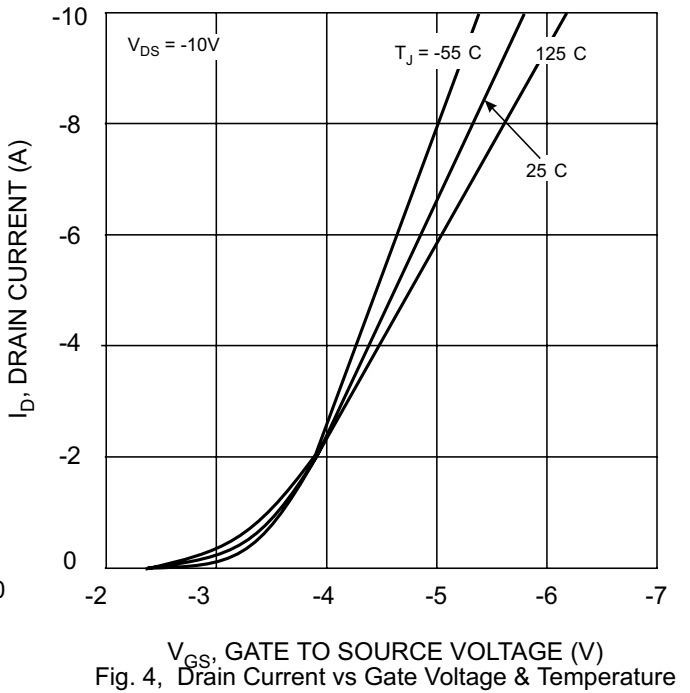
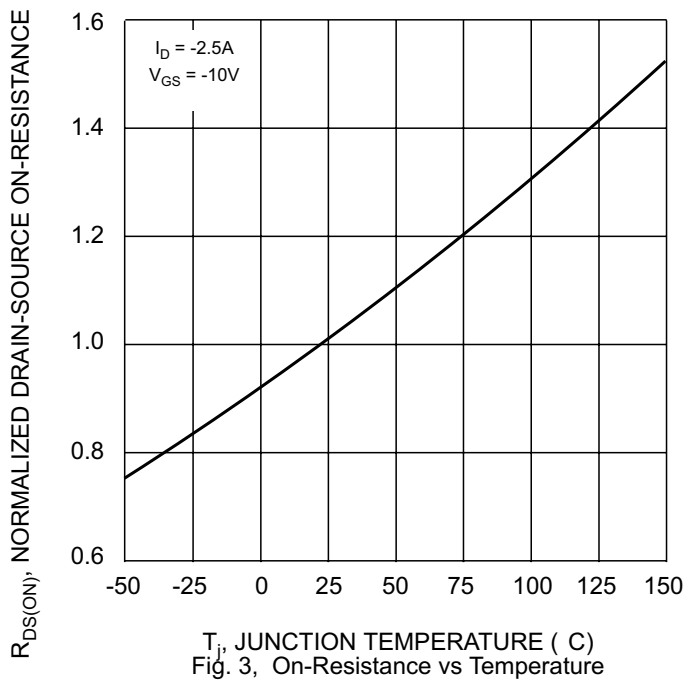
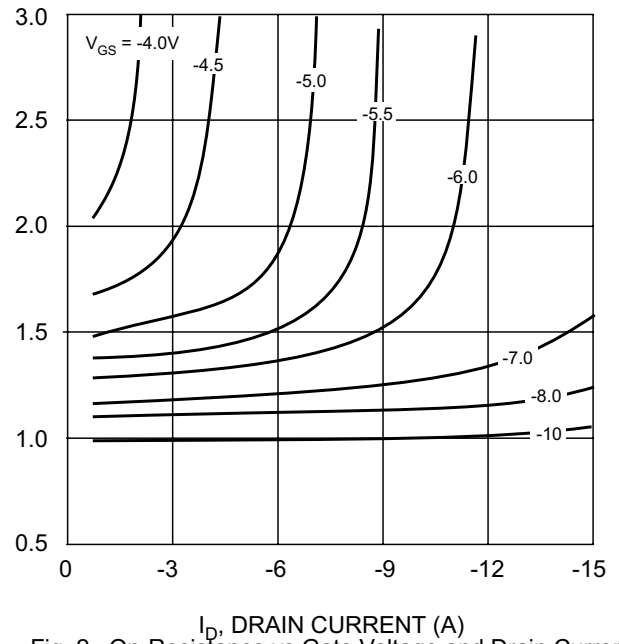
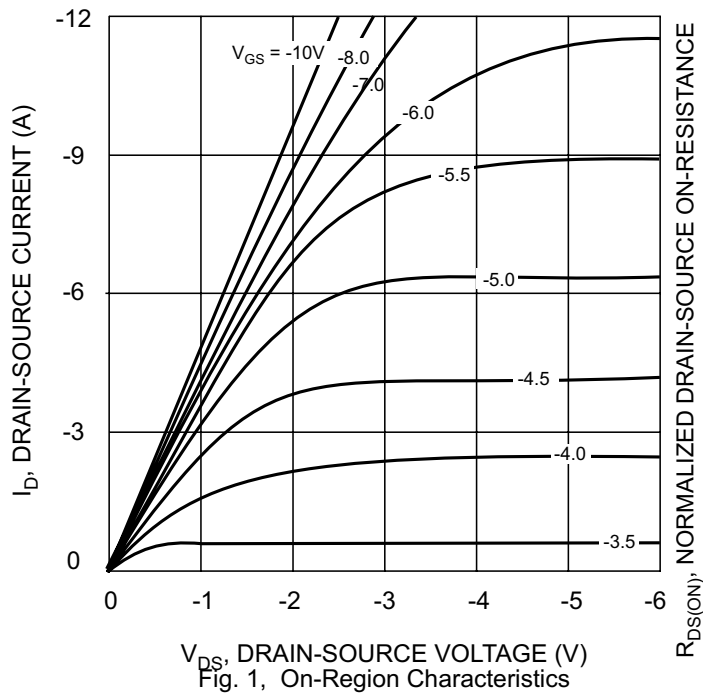
Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	42	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	12	°C/W

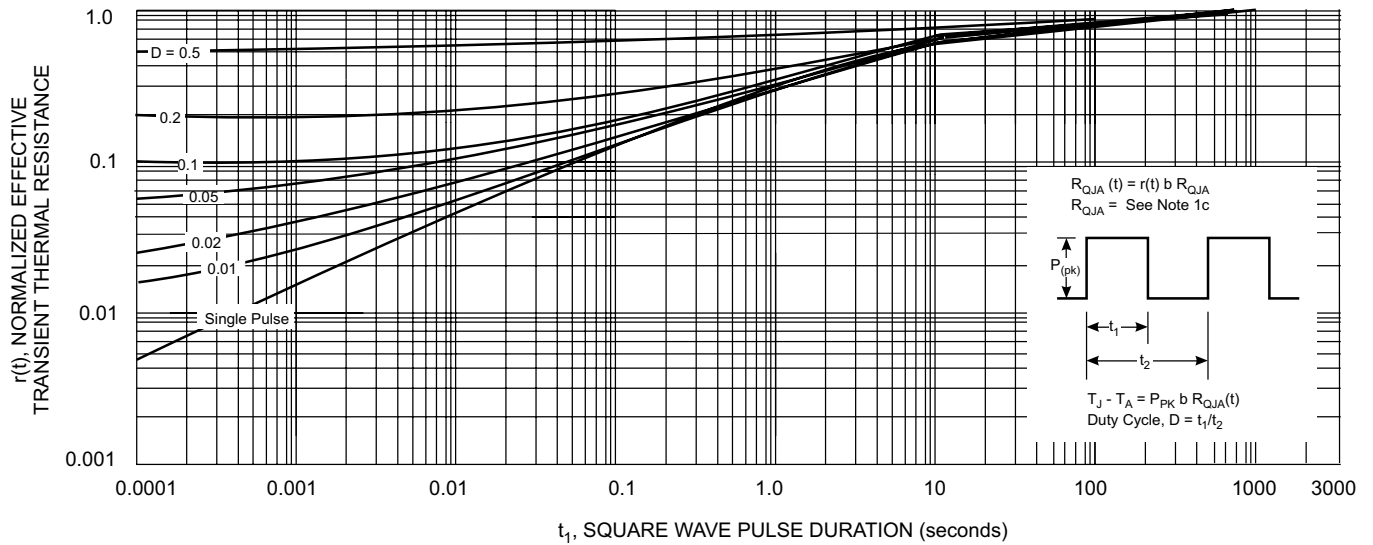
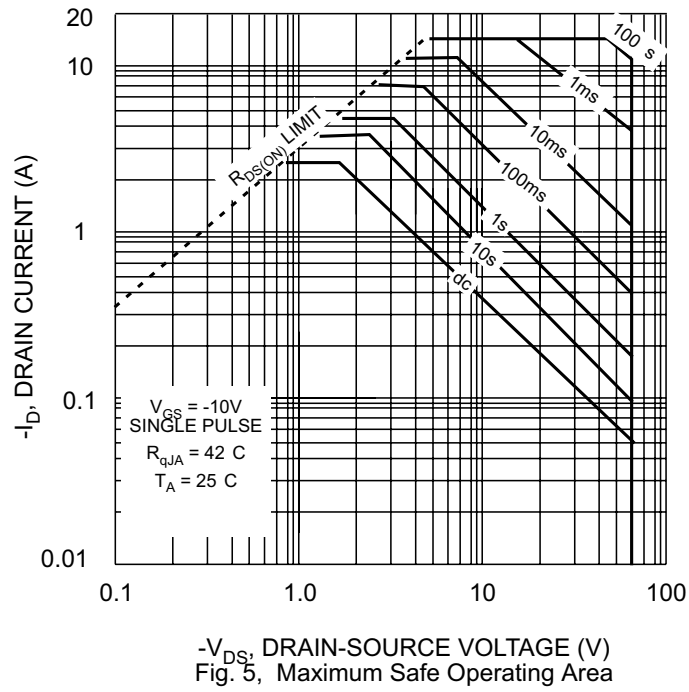
- Notes: 1. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.
- 1a. With 1 in² oz 2 oz. copper mounting pad $R_{\theta JA} = 42^\circ\text{C/W}$.
- 1b. With 0.0066 in² oz 2 oz. copper mounting pad $R_{\theta JA} = 95^\circ\text{C/W}$.
- 1c. With 0.0123 in² oz 2 oz. copper mounting pad $R_{\theta JA} = 110^\circ\text{C/W}$.

Electrical Characteristics^{25°C unless otherwise specified}

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	-60	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _j = 125°C	I _{DSS}	—	—	-10 -100	μA	V _{DS} = -60V, V _{GS} = 0V
Gate-Body Leakage, Forward	I _{GSSF}	—	—	100	nA	V _{GS} = 20V, V _{DS} = 0V
Gate-Body Leakage, Reverse	I _{GSSR}	—	—	-100	nA	V _{GS} = -20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 2)						
Gate Threshold Voltage T _j = 125°C	V _{GS(th)}	-2.0 -0.8	-2.4 -2.0	-4.0 -2.6	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance T _j = 125°C	R _{DS (ON)}	—	0.21 0.3 0.36	0.30 0.45 0.50	Ω	V _{GS} = -10V, I _D = -2.5A V _{GS} = -10V, I _D = -2.5A V _{GS} = -4.5V, I _D = -2.0A
On-State Drain Current	I _{D(ON)}	-12	—	—	A	V _{GS} = -10V, V _{DS} = -5.0V
Forward Transconductance	g _{FS}	—	3.5	—	m	V _{DS} = -10V, I _D = -2.5A
DYNAMIC CHACTERISTICS						
Input Capacitance	C _{iss}	—	570	—	pF	V _{DS} = -25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	140	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	40	—	pF	
SWITCHING CHARACTERISTICS (Note 2)						
Turn-On Delay Time	t _{D(ON)}	—	8.0	15	ns	V _{DD} = -30V, I _D = -1.0A V _{GEN} = -10V, R _{GEN} = 6.0Ω
Turn-On Rise Time	t _r	—	20	40	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	20	40	ns	
Turn-Off Fall Time	t _f	—	5.0	20	ns	
Total Gate Charge	Q _g	—	16	25	nC	V _{DS} = -30V, I _D = -2.5A. V _{GS} = -10V
Gate-Source Charge	Q _{gs}	—	2.0	5.0	nC	
Gate-Drain Charge	Q _{gd}	—	4.0	8.0	nC	
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Max Continuous Drain-Source Diode Forward Current	I _S	—	—	-2.3	A	
Drain-Source Diode Forward Voltage	V _{SD}	—	—	-1.3	V	V _{GS} = 0V, I _S = -2.5A (Note 2)

Notes: 2. Pulse Test: Pulse width $\leq 300\mu s$, duty cycle $\leq 2.0\%$.





Remark: Thermal characterization performed under conditions described in note 1c. Transient thermal response will change depending on the circuit board design.