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iCEblink40LP1K Evaluation Kit

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A low-cost platform for evaluating and developing with the low-power iCE40LP1K FPGA. The board provides access to general-purpose I/O and includes capacitive-touch buttons and LEDs. The board is powered and programmed via USB. An on-board microcontroller enables two-way communication with the iCE40LP1K FPGA.

The free iCEcube2 development tool controls programming, accesses virtual I/O functions, and runs the included demos. The tools can be downloaded from the Lattice website.

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Features

- Low power, small footprint iCE40LP1K FPGA in an 84-pin QFNS package
- USB programming, debugging, virtual I/O functions and power supply
- Four user LEDs
- Four capacitive-touch buttons
- 3.3 MHz clock source
- 1 Mbit SPI serial configuration PROM
- Supported by iCEcube2 design software
- 63 LVCMOS/LVTTL (3.3V) digital I/O connections on 0.1" through-hole connections
- Supports third-party I/O expansion boards and modules, including 3.3V Arduino Shield boards (requires additional sockets, not supplied)

[Kit Contents](#) [Board Photos](#) [Ordering Information](#) [Documentation](#) [Support](#)

Kit Contents

- iCEblink40 LP1K Board
- USB Mini Cable for power & programming
- QuickStart Guide

Board Photos



Side View

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Ordering Information

- Ordering Part Number: ICE40LP1K-BLINK-EVN
- [Click here](#) to find an authorized Lattice distributor near you
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Documentation

Quick Reference		Downloads					
User Manual		Design File					
		TITLE ▾	NUMBER ▾	VERSION ▾	DATE ▾	FORMAT ▾	SIZE
		CapSense Demo for iCEblink40 Evaluation Kits - User's Guide 	UG64	1.0	10/17/2012	PDF	2.1 MB
		iCEblink40-LP1K Evaluation Kit User's Guide  Includes schematics in .pdf format	EB75	01.1	9/18/2012	PDF	3.3 MB
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