

HD74LS07

Hex Buffers / Drivers (With Open Collector High-Voltage Outputs)

> REJ03D0393-0200 Rev.2.00 Feb.18.2005

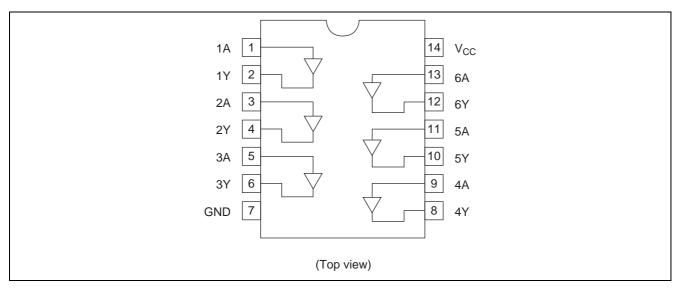
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS07P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS07FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC} Note	7	V
Input voltage	V _{IN}	7	V
Output voltage	V _{OUT}	30	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output voltage	V _{OH}	_	_	30	V
Output current	I _{OL}	_	_	48	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition	
Input voltage	V_{IH}	2.0	_	_	V		
input voitage	V_{IL}	_	_	0.8	V		
Output voltage	V _{OL}	_		0.4	V	$I_{OL} = 24 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{II} = 0.8 \text{ V}$	
		_		0.5		$I_{OL} = 48 \text{ mA}$	
	I _{IH}	_		20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$	
Input current	I _{IL}	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 0.4 \text{ V}$	
	I _I	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 7 \text{ V}$	
Output current	I _{OH}	_	_	250	μΑ	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{OH} = 30 \text{ V}$	
Supply current	I _{CCH}	_	22	41	mA	V _{CC} = 5.25 V	
	I _{CCL}	_	17	30	mA	V _{CC} = 5.25 V	
Input clamp voltage	V_{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$	

Note: ${}^*V_{CC} = 5 \text{ V}, \text{ Ta} = 25 {}^{\circ}\text{C}$

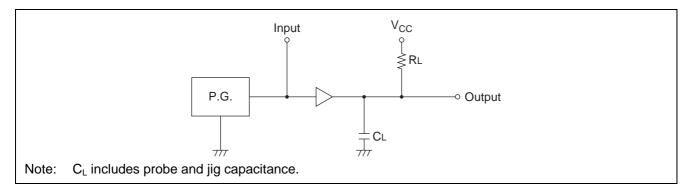
Switching Characteristics

 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

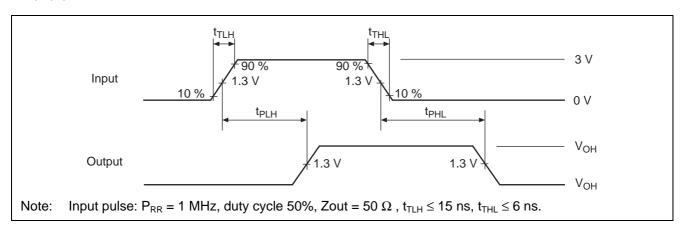
						(66 / /
Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	_	10	15	ns	C: - 15 pE P: - 100 C
	t _{PHL}	_	20	30	ns	$C_L = 15 \text{ pF}, R_L = 100 \Omega$

Testing Method

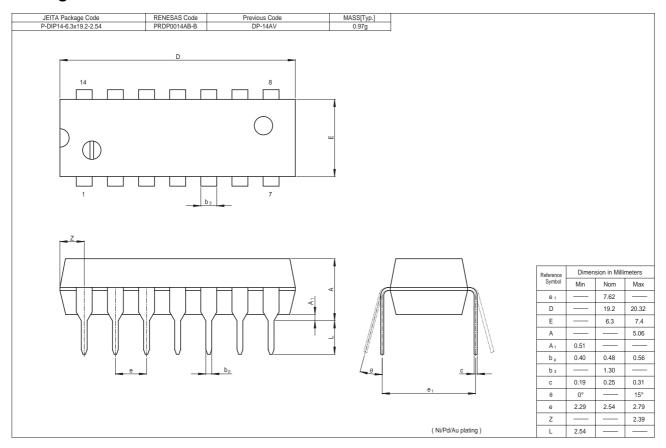
Test Circuit

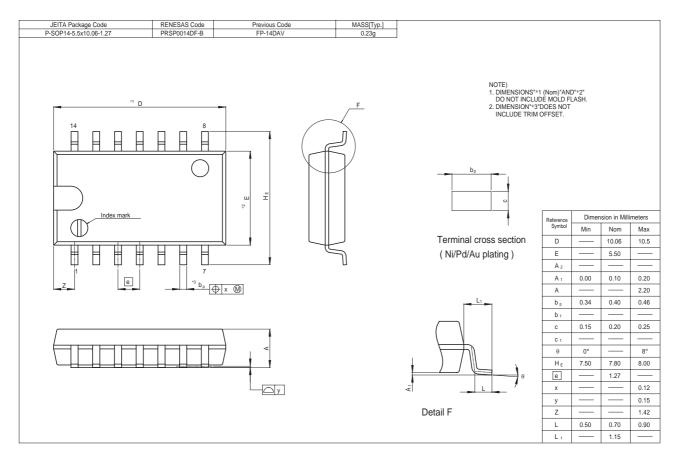


Waveform



Package Dimensions





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