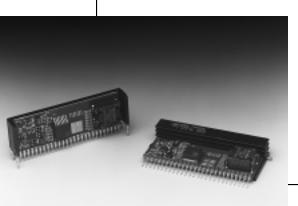
PT7600

Series

10 AMP PROGRAMMABLE INTEGRATED **SWITCHING REGULATOR**

Revised 5/15/98



Features

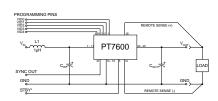
- Single-Device: +5V input
- 5-bit Programmable: 1.3V to 3.5V@10A
- High Efficiency
- Input Voltage Range: 4.5V to 5.5V
- Differential Remote Sense
- 27-pin SIP Package

The PT7600 is a new series of highperformance, 10 Amp Integrated Switching Regulators (ISRs) housed in a 27-pin SIP package. The 10A capability allows easy integration of the latest high-speed, low-voltage µPs and bus drivers into existing 5V systems.

The output voltage of the PT7600 is easily programmed with a 5 bit input compatible with Intel's Pentium® II Processor from 1.3V to 3.5V. A differential remote sense is also provided which automatically compensates for any voltage drop from the ISR to the load.

1200µF of output capacitance are required for proper operation.

Standard Application



 $\begin{array}{ll} C_{in} &= Required\ 1200\mu F\ electrolytic \\ C_{out} &= Required\ 1200\mu F\ electrolytic \\ L1 &= Optional\ l\mu H\ input\ choke \end{array}$

Pin-Out Information

Pin	Function	Pin	Function
1	VID0	10	V_{in}
2	VID1	11	V_{in}
3	VID2	12	Remote Sense Gr
4	VID3	13	GND
5	STBY* - Stand-by	14	GND
6	VID4	15	GND
7	V_{in}	16	GND
8	V_{in}	17	GND
9	Vin	18	GND

Pin	Function
19	GND
20	Vout
21	V_{out}
22	V_{out}
23	V_{out}
24	V_{out}
25	V_{out}
26	Remote Sense Vout
27	Do not connect

For STBY* pin; open = output enabled; ground = output

Specifications

Characteristics			PT7600 S	PT7600 SERIES		
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units
Output Current	I_{o}	$T_a = +60$ °C, 200 LFM, pkg N $T_a = +25$ °C, natural convection	0.1* 0.1*	=	10 10	A A
Input Voltage Range	$ m V_{in}$	$0.1A \le I_o \le 10A$	4.5**	_	5.5	V
Output Voltage Tolerance	ΔV_{o}	V_{in} = +5V, I_{o} = 10A 0°C \leq T_{a} \leq +55°C	Vo-0.03	-	Vo+0.03	V
Line Regulation	Reg _{line}	$4.5V \le V_{\rm in} \le 5.5V$, $I_{\rm o} = 10A$	_	±10	_	mV
Load Regulation	Reg _{load}	$V_{\rm in} = +5V, 0.1 \le I_{\rm o} \le 10A$	_	±10	_	mV
Vo Ripple/Noise pk-pk	V_n	$V_{in} = +5V, I_o = 10A$	_	50	_	mV
Transient Response with C _{out} = 1200μF	$egin{array}{c} t_{tr} \ V_{os} \end{array}$	I _o step between 5A and 10A V _o over/undershoot	_	100 200	_	μSec mV
Efficiency	η	$\begin{array}{c} V_{in} = +5 V, I_o = 10 A & V_o = 3.3 \\ V_o = 2.5 \\ V_o = 2.5 \\ V_o = 1.8 \\ V_o = 1.5 \end{array}$	V — V — V —	80 78 75 69 65	= = =	% % % %
Switching Frequency	f_{o}	$4.5V \le V_{in} \le 5.5V$ $0.1A \le I_0 \le 10A$	650	700	750	kHz
Absolute Maximum Operating Temperature Range	T_a		0	-	+85	°C
Recommended Operating Temperature Range	T_a	Forced Air Flow = 200 LFM Over V _{in and} I _o Ranges	0	_	+65***	°C
Storage Temperature	T_s	_	-40		+125	°C
Mechanical Shock		Per Mil-STD-883D, Method 2002.3 1 msec, Half Sine, mounted to a fixture	_	500	_	G's
Mechanical Vibration		Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, Soldered in a PC board	_	10	_	G's
Weight	_	Vertical/Horizontal		31/41	_	gram

^{*} ISR-will operate down to no load with reduced specifications. Please note that this product is not short-circuit protected. ** The minimum input voltage is $4.5 \mathrm{V}$ or $\mathrm{V}_{\mathrm{out}}$ + $1.2 \mathrm{V}$, whichever is greater. *** See SOA curves.

 $\textbf{Output Capacitors:} \ \ \underline{\textit{The PT7600 series requires a minimum output capacitance of 1200 \mu F for proper operation.} \ \ Do \ not \ use \ Oscon \ type \ capacitors. The \ maximum \ allowable}$ output capacitance is 7,500µF. See Capacitor Application Note.

Input Filter: An input filter is optional for most applications. The input inductor must be sized to handle 10ADC with a typical value of 1µH. The input capacitance must be rated for a minimum of 1.0 Arms of ripple current. For transient or dynamic load applications, additional capacitance may be required.

SHEETS

PT7600

Series

Programming Information

VID3	VID2	VID1	VIDO	VID4=1 Vout	VID4=0 Vout
1	1	1	1	2.0V	1.30V
1	1	1	0	2.1V	1.35V
1	1	0	1	2.2V	1.40V
1	1	0	0	2.3V	1.45V
1	0	1	1	2.4V	1.50V
1	0	1	0	2.5V	1.55V
1	0	0	1	2.6V	1.60V
1	0	0	0	2.7V	1.65V
0	1	1	1	2.8V	1.70V
0	1	1	0	2.9V	1.75V
0	1	0	1	3.0V	1.80V
0	1	0	0	3.1V	1.85V
0	0	1	1	3.2V	1.90V
0	0	1	0	3.3V	1.95V
0	0	0	1	3.4V	2.00V
0	0	0	0	3.5V	2.05V

Logic 0 = Pin 12 (remote sense gnd) potential Logic 1 = Open circuit (no pull-up resistors)

Ordering Information

PT7601□ = 1.3 to 3.5 Volts

(For dimensions and PC board layout, see Package Styles 800 & 810.)

PT Series Suffix (PT1234X)

Case/Pin
Configuration

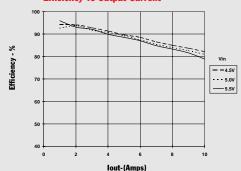
Configuration	
Vertical Through-Hole	N
Horizontal Through-Hole	A
Horizontal Surface Mount	C

CHARACTERISTIC DATA

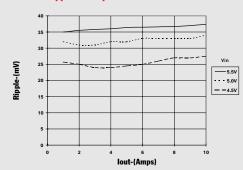
Safe Operating Area Curve (@V_{in}=+5V) Airflow Nat Conv - 60LPM - 100LPM 20 0 2 4 6 8 10

PT7601, 3.3 VDC See Note 1)

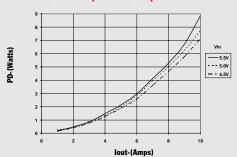
Efficiency vs Output Current



Ripple vs Output Current



Power Dissipation vs Output Current



Note 1: SOA curves represent operating conditions at which internal components are at or below manufacturer's maximum rated operating temperatures.

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