

CUS01

Portable Equipment Battery Applications

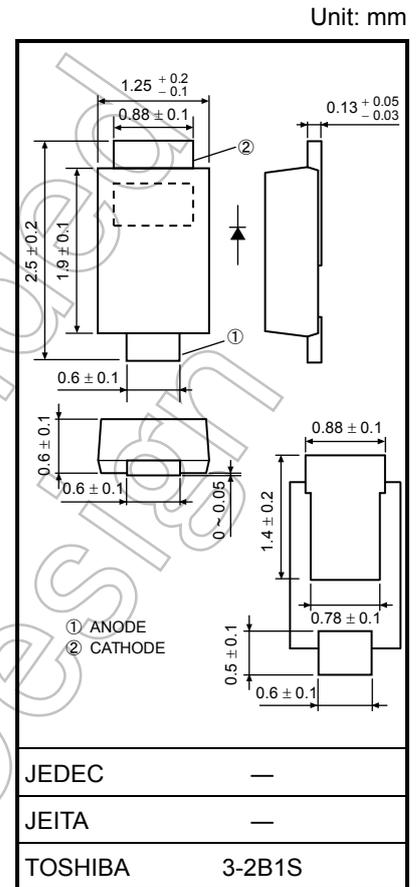
- Repetitive peak reverse voltage : $V_{RRM} = 30\text{ V}$
- Average forward current : $I_F (AV) = 1.0\text{ A}$
- Peak forward voltage : $V_{FM} = 0.37\text{ V (max) @} I_F = 0.7\text{ A}$
- Suitable for high-density board assembly due to the use of a small Toshiba Nickname: US-FLAT™

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Average forward current	$I_F (AV)$	1.0 (Note 1)	A
Non-repetitive peak forward surge current	I_{FSM}	20 (50 Hz)	A
Junction temperature	T_j	-40 to 125	°C
Storage temperature range	T_{stg}	-40 to 150	°C

Note 1: $T_l = 86^\circ\text{C}$: Rectangular waveform ($\alpha = 180^\circ$), $V_R = 15\text{ V}$

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.004 g (typ.)

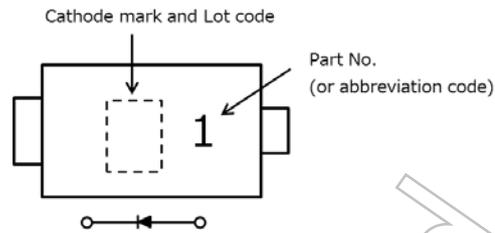
Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Peak forward voltage	$V_{FM} (1)$	$I_{FM} = 0.1\text{ A (pulse test)}$	—	0.25	—	V
	$V_{FM} (2)$	$I_{FM} = 0.7\text{ A (pulse test)}$	—	0.33	0.37	
	$V_{FM} (3)$	$I_{FM} = 1.0\text{ A (pulse test)}$	—	0.39	—	
Repetitive peak reverse current	$I_{RRM} (1)$	$V_{RRM} = 5\text{ V (pulse test)}$	—	50	—	μA
	$I_{RRM} (2)$	$V_{RRM} = 30\text{ V (pulse test)}$	—	0.5	1.5	mA
Junction capacitance	C_j	$V_R = 10\text{ V, } f = 1.0\text{ MHz}$	—	40	—	pF
Thermal resistance (junction to ambient)	$R_{th} (j-a)$	Device mounted on a ceramic board board size 50 mm × 50 mm soldering land size 2 mm × 2 mm board thickness 0.64 mm	—	—	75	°C/W
		Device mounted on a glass-epoxy board board size 50 mm × 50 mm soldering land size 6 mm × 6 mm board thickness 1.6 mm	—	—	150	
Thermal resistance (junction to lead)	$R_{th} (j-l)$	Junction to lead of cathode side	—	—	30	°C/W

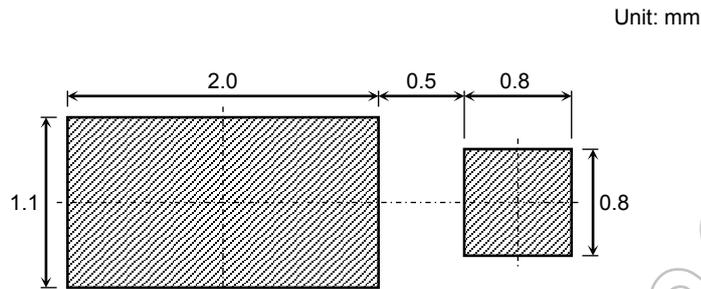
Start of commercial production
2001-12

Marking

Abbreviation Code	Part No.
1	CUS01

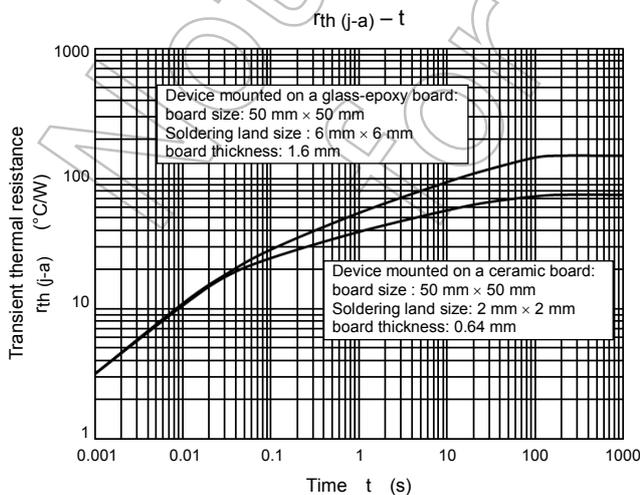
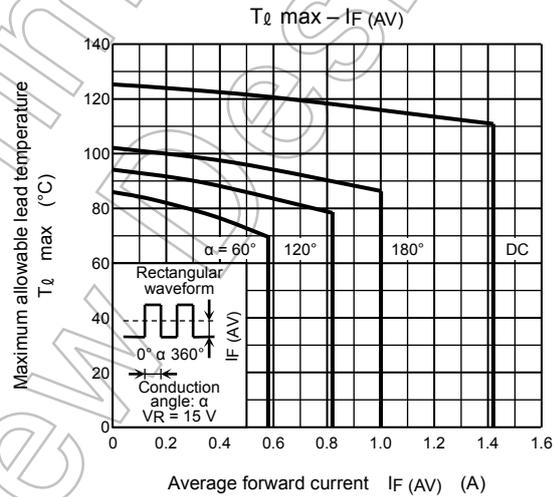
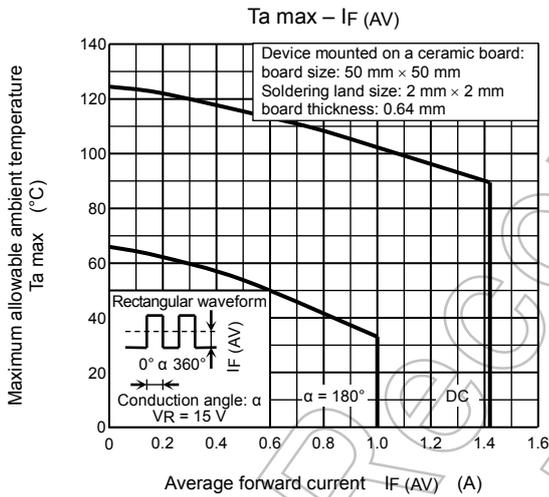
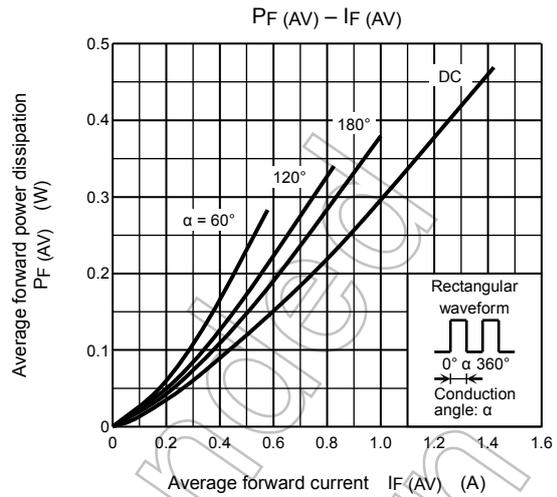
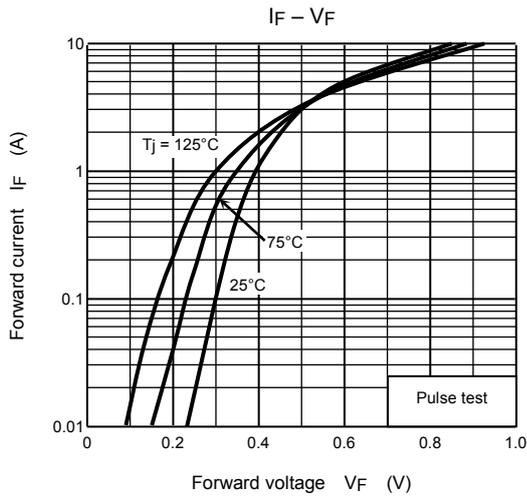


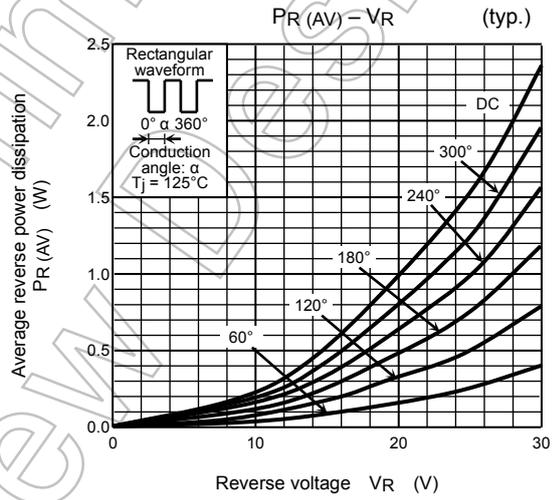
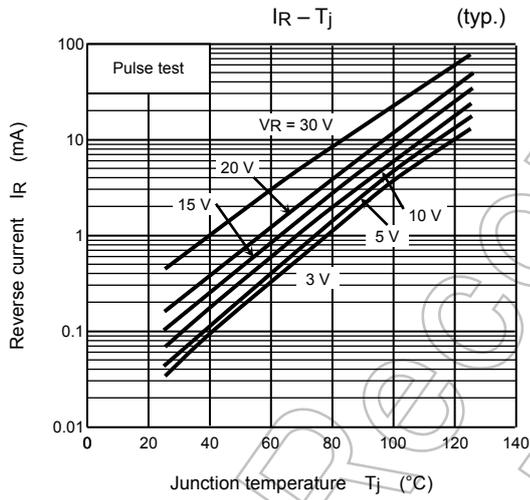
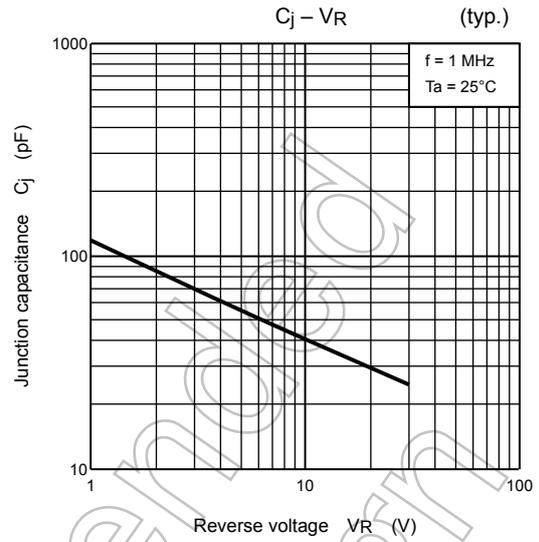
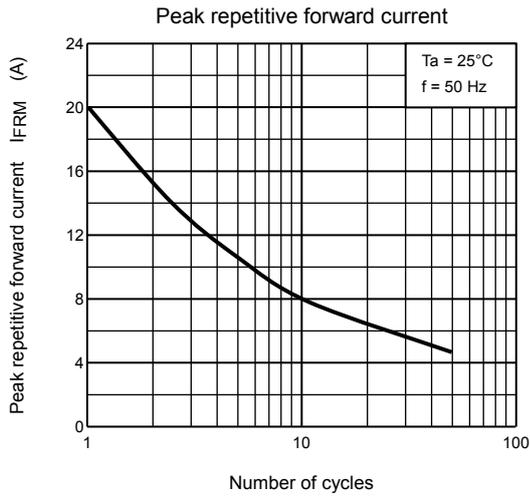
Land pattern dimensions for reference only



Handling Precaution

- 1) Schottky barrier diodes have reverse current characteristic compared to the other diodes. There is a possibility SBD may cause thermal runaway when it is used under high temperature or high voltage. This device is VF-Irrm trade-off type, lower VF higher Irrm; therefore, thermal runaway might occur when voltage is applied. Please take forward and reverse loss into consideration during design.
- 2) The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend for designing a circuit using this device.
 - VRRM: Use this rating with reference to the above. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.
 - IF(AV): We recommend that the worst-case current be no greater than 80% of the absolute maximum rating of IF(AV) and Tj be below 100°C. When using this device, take the margin into consideration by using an allowable Ta max-IF(AV) curve.
 - IFSM: This rating specifies the non-repetitive peak current. This is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.
 - Tj: Derate this rating when using a device in order to ensure high reliability. We recommend that the device be used at Tj below 100°C.
- 3) Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, please design a circuit board and a soldering land size to match the appropriate thermal resistance value.
- 4) For other design considerations, see the Toshiba website.





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