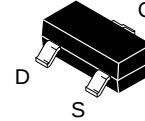


N-Channel RF Amplifier

MMBF5484, MMBF5485, MMBF5486

This device is designed primarily for electronic switching applications such as low On Resistance analog switching. Sourced from Process 50.



NOTE: Source & Drain
are interchangeable

**SOT-23
CASE 318**

ABSOLUTE MAXIMUM RATINGS* ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Rating	Value	Unit
V_{DG}	Drain-Gate Voltage	25	V
V_{GS}	Gate-Source Voltage	-25	V
I_{GF}	Forward Gate Current	10	mA
T_J, T_{stg}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*These rating are limiting values above which the serviceability of any semiconductor device may be impaired.

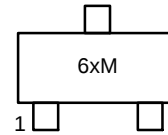
1. These rating are based on a maximum junction temperature of 150°C .
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

THERMAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Characteristic	Max	Unit
		*MMBF5484-5486	
P_D	Total Device Dissipation Derate above 25°C	225 1.8	mW mW/ $^\circ\text{C}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	-	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	556	$^\circ\text{C}/\text{W}$

*Device mounted on FR-4 PCB 1.6" x 1.6" x 0.06".

MARKING DIAGRAM



6x = Device Code (x = B, M, H)
M = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBF5484	SOT-23 (Pb-Free)	3000 Tape & Reel
MMBF5485		
MMBF5486		

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBF5484, MMBF5485, MMBF5486

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
--------	-----------	----------------	-----	-----	-----	------

OFF CHARACTERISTICS

V _{(BR)GSS}	Gate-Source Breakdown Voltage	I _G = -1.0 μA, V _{DS} = 0		-25	-	-	V
I _{GSS}	Gate Reverse Current	V _{GS} = -20 V, V _{DS} = 0 V _{GS} = -20 V, V _{DS} = 0, T _A = 100°C		-	-	-1.0 -0.2	nA μA
V _{GS(off)}	Gate-Source Cutoff Voltage	V _{DS} = 15 V, I _D = 10 nA	5484 5485 5486	-0.3 -0.5 -2.0	- - -	-3.0 -4.0 -6.0	V V V

ON CHARACTERISTICS

I_{DSS}	Zero-Gate Voltage Drain Current*	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$	5484 5485 5486	1.0 4.0 8.0	-	5.0 10 20	mA mA mA
-----------	----------------------------------	--	----------------------	-------------------	---	-----------------	----------------

SMALL SIGNAL CHARACTERISTICS

g_{fs}	Forward Transfer Conductance	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 1.0\ \text{kHz}$	5484 5485 5486	3000 3500 4000	-	6000 7000 8000	μmhos μmhos μmhos
$Re(y_{is})$	Input Conductance	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 100\ \text{MHz}$	5484	-	-	100	μmhos
		$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 400\ \text{kHz}$	5485 / 5486	-	-	1000	μmhos
g_{os}	Output Conductance	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 1.0\ \text{kHz}$	5484 5485 5486	- - -	- - -	50 60 75	μmhos μmhos μmhos
		$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 100\ \text{MHz}$	5484	-	-	75	μmhos
		$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 400\ \text{MHz}$	5485 / 5486	-	-	100	μmhos
$Re(y_{fs})$	Forward Transconductance	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 100\ \text{MHz}$	5484	2500	-	-	μmhos
		$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 400\ \text{MHz}$	5485 5486	3000 3500	- -	- -	μmhos μmhos
C_{iss}	Input Capacitance	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 1.0\ \text{MHz}$	-	-	-	5.0	pF
C_{rss}	Reverse Transfer Capacitance	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 1.0\ \text{MHz}$	-	-	-	1.0	pF
C_{oss}	Output Capacitance	$V_{DS} = 15\ \text{V}$, $V_{GS} = 0$, $f = 1.0\ \text{MHz}$	-	-	-	2.0	pF
NF	Noise Figure	$V_{DS} = 15\ \text{V}$, $R_G = 1.0\ \text{k}\Omega$, $f = 100\ \text{MHz}$	5484	-	-	3.0	dB
		$V_{DS} = 15\ \text{V}$, $R_G = 1.0\ \text{k}\Omega$, $f = 400\ \text{MHz}$	5484	-	4.0	-	dB
		$V_{DS} = 15\ \text{V}$, $R_G = 1.0\ \text{k}\Omega$, $f = 100\ \text{MHz}$	5485 / 5486	-	-	2.0	dB
		$V_{DS} = 15\ \text{V}$, $R_G = 1.0\ \text{k}\Omega$, $f = 400\ \text{MHz}$	5485 / 5486	-	-	4.0	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

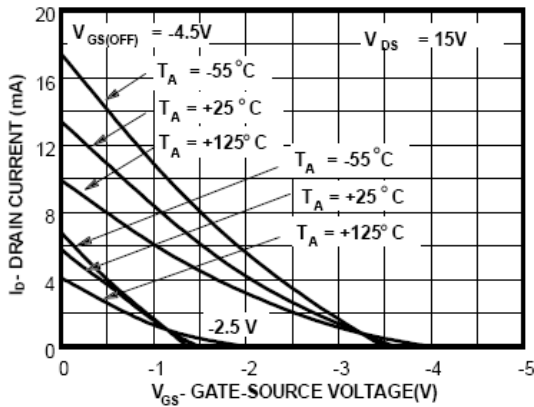


Figure 1. Transfer Characteristics

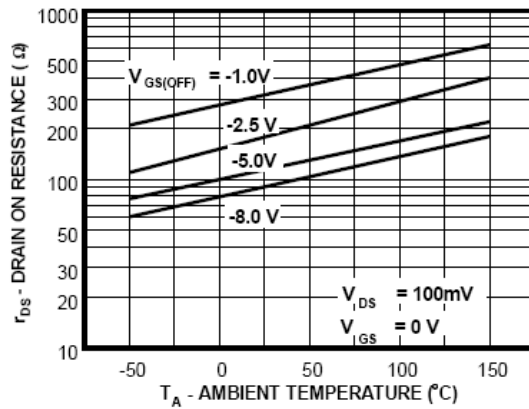


Figure 2. Channel Resistance vs. Temperature

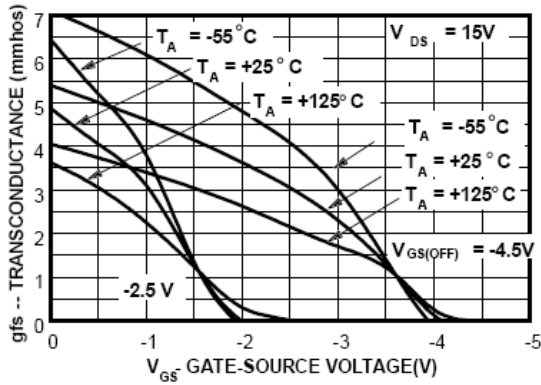


Figure 3. Transconductance Characteristics

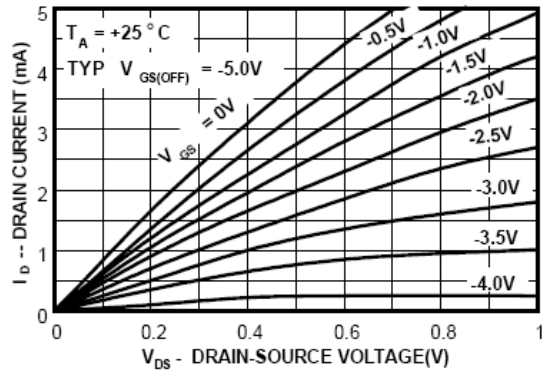


Figure 4. Common Drain-Source Characteristics

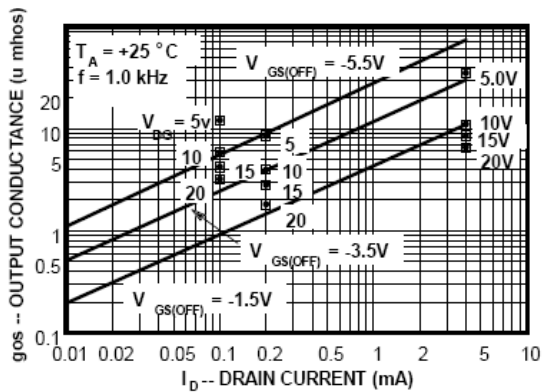


Figure 5. Output Conductance vs. Drain Current

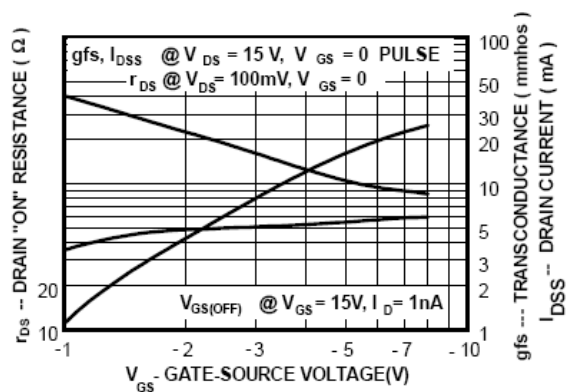


Figure 6. Transconductance Parameter Interactions

TYPICAL CHARACTERISTICS (CONTINUED)

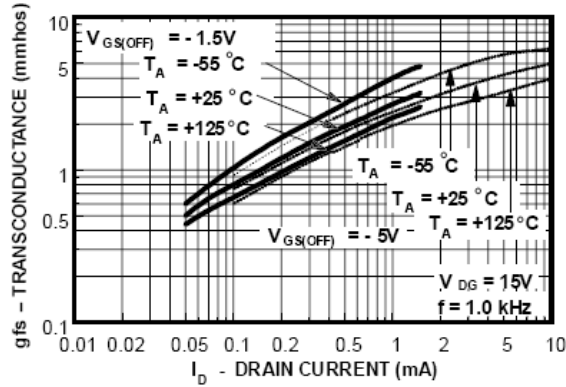


Figure 7. Transconductance vs. Drain Current

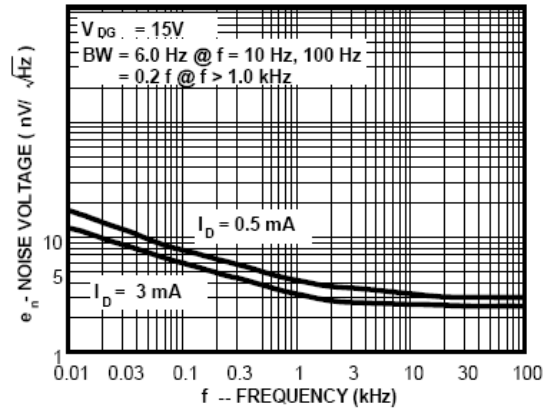


Figure 8. Noise Voltage vs. Frequency

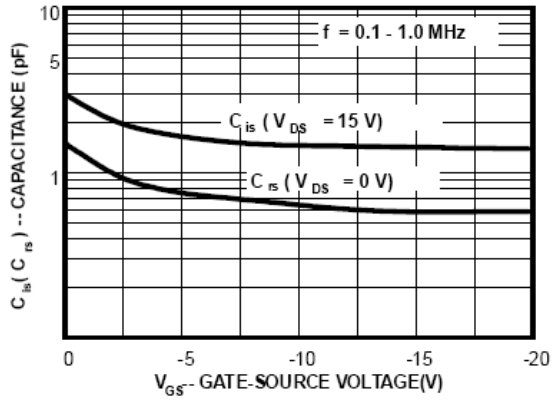


Figure 9. Capacitance vs. Voltage

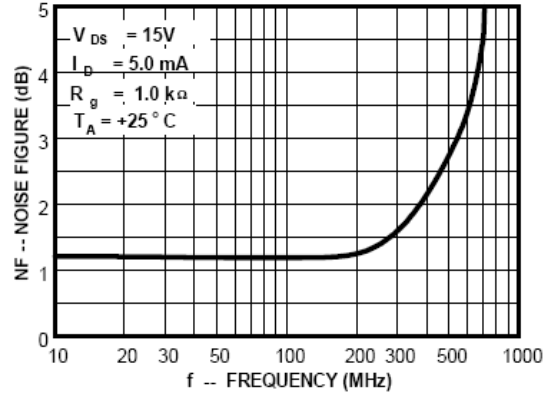


Figure 10. Noise Figure Frequency

COMMON SOURCE CHARACTERISTICS

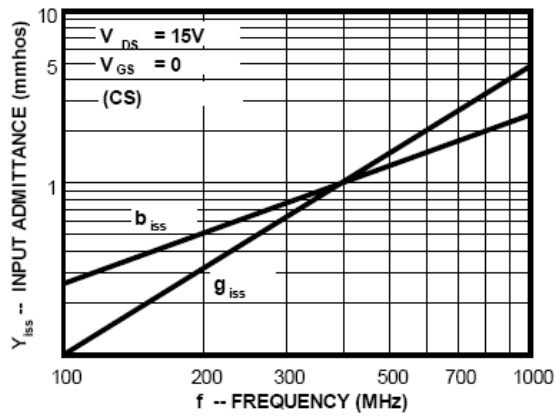


Figure 11. Input Admittance

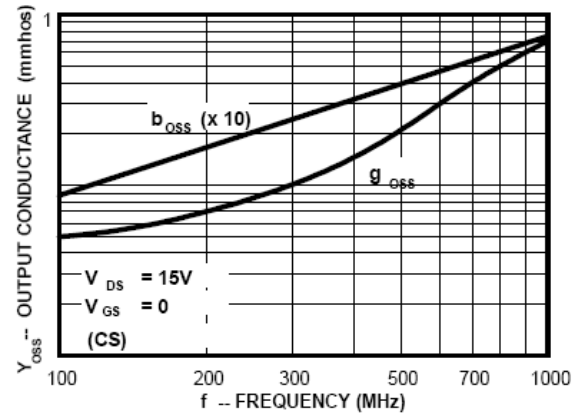


Figure 12. Output Admittance

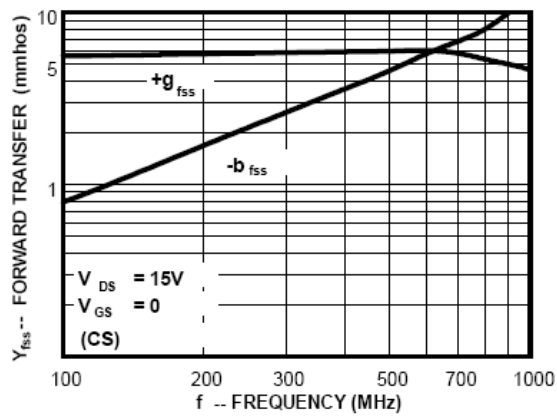


Figure 13. Forward Transadmittance

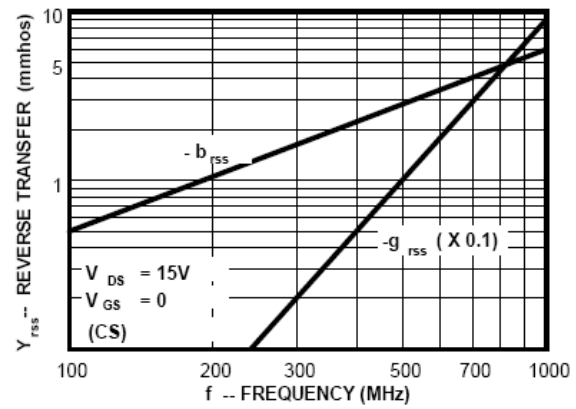


Figure 14. Reverse Transadmittance

COMMON GATE CHARACTERISTICS

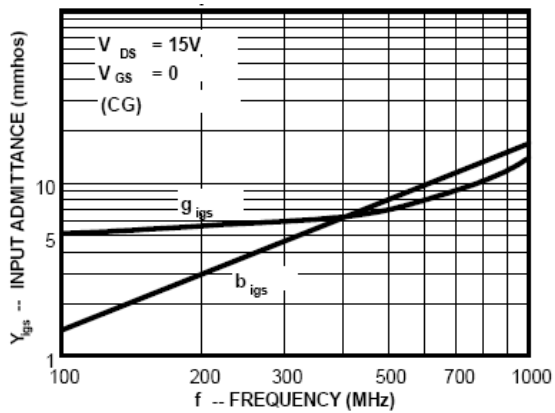


Figure 15. Input Admittance

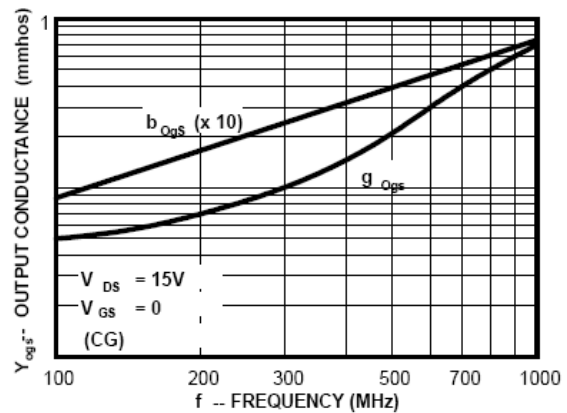


Figure 16. Output Admittance

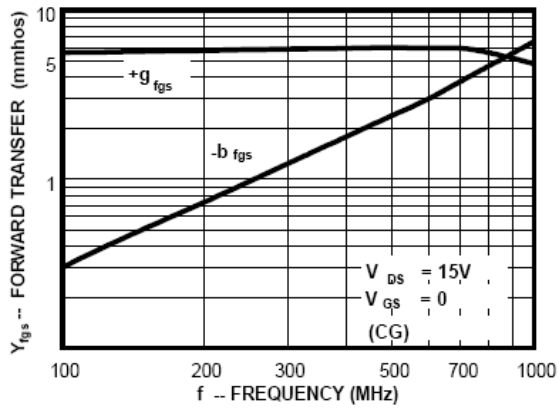


Figure 17. Forward Transadmittance

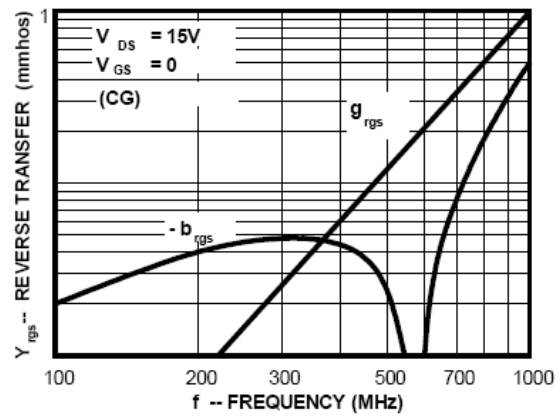


Figure 18. Reverse Transadmittance



SCALE 4:1

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P
CASE 318
ISSUE AU

DATE 14 AUG 2024

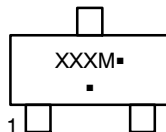


MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.89	1.00	1.11
A1	0.01	0.06	0.10
b	0.37	0.44	0.50
c	0.08	0.14	0.20
D	2.80	2.90	3.04
E	1.20	1.30	1.40
e	1.78	1.90	2.04
L	0.30	0.43	0.55
L1	0.35	0.54	0.69
HE	2.10	2.40	2.64
T	0°	---	10°

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC
MARKING DIAGRAM*



XXX = Specific Device Code
M = Date Code
▪ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED
MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

STYLES ON PAGE 2

DOCUMENT NUMBER:	98ASB42226B	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	SOT-23 (TO-236) 2.90x1.30x1.00 1.90P	PAGE 1 OF 2

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P
CASE 318
ISSUE AU

DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE		
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: PIN 1. ANODE 2. CATHODE 3. CATHODE-ANODE	STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 13: PIN 1. SOURCE 2. DRAIN 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	STYLE 17: PIN 1. NO CONNECTION 2. ANODE 3. CATHODE	STYLE 18: PIN 1. NO CONNECTION 2. CATHODE 3. ANODE	STYLE 19: PIN 1. CATHODE 2. ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 24: PIN 1. GATE 2. DRAIN 3. SOURCE	STYLE 25: PIN 1. ANODE 2. CATHODE 3. GATE	STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

DOCUMENT NUMBER:	98ASB42226B	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	SOT-23 (TO-236) 2.90x1.30x1.00 1.90P	PAGE 2 OF 2

onsemi and **onsemi** are trademarks of Semiconductor Components Industries, LLC dba **onsemi** or its subsidiaries in the United States and/or other countries. **onsemi** reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at
www.onsemi.com/support/sales