

SPECIFICATION

- Supplier : Samsung electro-mechanics
- Product : Multi-layer Ceramic Capacitor

- Samsung P/N : **CL10X226MR8NUNE**
- Description : **CAP, 22 μ F, 4V, $\pm$ 20%, X6S, 0603**

A. Samsung Part Number

CL **10** **X** **226** **M** **R** **8** **N** **U** **N** **E**
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

① Series	Samsung Multi-layer Ceramic Capacitor					
② Size	0603 (inch code)	L: 1.6 ± 0.25	mm	W: 0.8 ± 0.25	mm	
③ Dielectric	X6S	⑧ Inner electrode	Ni			
④ Capacitance	22 μF	Termination	Cu			
⑤ Capacitance tolerance	±20 %	Plating	Sn 100% (Pb Free)			
⑥ Rated Voltage	4 V	⑨ Product	0603 Size dimension spec			
⑦ Thickness	0.8 ± 0.25 mm	⑩ Special	Reserved for future use			
		⑪ Packaging	Embossed Type, 7" reel			

B. Samsung Reliability Test and Judgement condition

	Performance	Test condition
Capacitance	Within specified tolerance	120Hz $\pm$ 20% 0.5 $\pm$ 0.1Vrms
Tan δ (DF)	0.1 max.	
Insulation Resistance	10,000Mohm or 50Mohm $\cdot\mu$ F Whichever is Smaller	Rated Voltage 60~120 sec.
Appearance	No abnormal exterior appearance	Microscope ($\times$ 10)
Withstanding Voltage	No dielectric breakdown or mechanical breakdown	250% of the rated voltage
Temperature Characteristics	X6S (From -55 $^{\circ}$ C to 105 $^{\circ}$ C, Capacitance change should be within $\pm$ 22%)	
Adhesive Strength of Termination	No peeling shall be occur on the terminal electrode	500g-F, for 10 $\pm$ 1 sec.
Bending Strength	Capacitance change : within $\pm$ 12.5%	Bending to the limit (1mm) with 1.0mm/sec.
Solderability	More than 75% of terminal surface is to be soldered newly	SnAg3.0Cu0.5 solder 245 $\pm$ 5 $^{\circ}$ C, 3 $\pm$ 0.3sec. (preheating : 80~120 $^{\circ}$ C for 10~30sec.)
Resistance to Soldering heat	Capacitance change : within $\pm$ 7.5% Tan δ , IR : initial spec.	Solder pot : 270 $\pm$ 5 $^{\circ}$ C, 10 $\pm$ 1sec.

	Performance	Test condition
Vibration Test	Capacitance change : within $\pm 5\%$ Tan δ , IR : initial spec.	Amplitude : 1.5mm From 10Hz to 55Hz (return : 1min.) 2hours $\times$ 3 direction (x, y, z)
Moisture Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.25 max IR : 8.8M $\Omega \cdot \mu F$ or Over	With rated voltage 40 $\pm 2^\circ C$, 90~95%RH, 500+12/-0hrs
High Temperature Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.25 max IR : 17.7M $\Omega \cdot \mu F$ or Over	With 100% of the rated voltage Max. operating temperature 1000+48/-0hrs
Temperature Cycling	Capacitance change : within $\pm 10\%$ Tan δ , IR : initial spec.	1 cycle condition Min. operating temperature $\rightarrow 25^\circ C$ $\rightarrow$ Max. operating temperature $\rightarrow 25^\circ C$ 5 cycle test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^\circ C$, 10sec. Max)

* For the more detail Specification, Please refer to the Samsung MLCC catalogue.