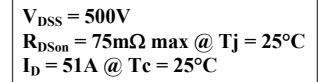
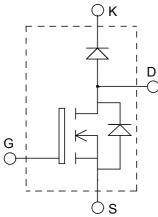


ISOTOP® Boost chopper MOSFET Power Module





Application

- AC and DC motor control
- Switched Mode Power Supplies
- Power Factor Correction
- Brake switch

Features

- Power MOS 7[®] MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Fast intrinsic reverse diode
 - Avalanche energy rated
 - Very rugged
- ISOTOP® Package (SOT-227)
- Very low stray inductance
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Very rugged
- Low profile
- **RoHS Compliant**



Absolute maximum ratings

Symbol	Parameter			Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage			500	V
Ţ	Continuous Drain Current $ \frac{T_c = 25^{\circ}C}{T_c = 80^{\circ}C} $		51		
I_{D}			$T_c = 80$ °C	39	A
I_{DM}	Pulsed Drain current	204			
V_{GS}	Gate - Source Voltage			±30	V
R_{DSon}	Drain - Source ON Resistance	75	$m\Omega$		
P_{D}	Maximum Power Dissipation		$T_c = 25$ °C	290	W
I_{AR}	Avalanche current (repetitive and non repetitive)			51	A
E_{AR}	Repetitive Avalanche Energy			50	mJ
E_{AS}	Single Pulse Avalanche Energy			2500	1113
IF_{AV}	Maximum Average Forward Current	Duty cycle=0.5	$Tc = 80^{\circ}C$	30	A
IF_{RMS}	RMS Forward Current (Square wave, 5	50% duty)		39	A

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.



All ratings @ $T_j = 25$ °C unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$ $T_j = 25^{\circ}C$			100	μА
		$V_{GS} = 0V, V_{DS} = 400V$ $T_j = 125^{\circ}$	C		500	
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 25.5A$			75	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{mA}$	3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		5590		
C_{oss}	Output Capacitance	$V_{DS} = 25V$		1180		pF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		85		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		123		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 250V$		33		nC
Q_{gd}	Gate – Drain Charge	$I_D = 51A$		65		
$T_{d(on)}$	Turn-on Delay Time	Resistive Switching		10		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$ $V_{Bus} = 250V$		20		na
$T_{d(off)}$	Turn-off Delay Time	$I_{\rm D} = 51A$		21		ns
T_{f}	Fall Time	$R_G = 0.6\Omega$		5		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		755		I
Eoff	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 330V$ $I_D = 51A, R_G = 5\Omega$		726		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1241		
Eoff	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 330V$ $I_D = 51A, R_G = 5\Omega$		846		μJ



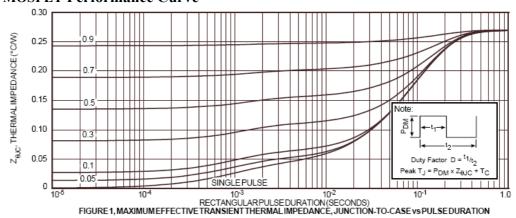
Chopper diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{F}	Diode Forward Voltage	$I_F = 30A$			1.6	1.8	
		$I_F = 60A$			1.9		V
		$I_F = 30A$	$T_i = 125$ °C		1.4		
I_{RM}	Maximum Reverse Leakage Current	$V_{R} = 600V$	$T_j = 25^{\circ}C$			250	۸
1RM	Waximum Reverse Leakage Current	$V_{R} = 600V$	$T_j = 125$ °C			500	μA
C_{T}	Junction Capacitance	$V_{R} = 200V$			44		pF
	Reverse Recovery Time	$I_F=1A, V_R=30V$ $di/dt = 100A/\mu s$	$T_j = 25$ °C		23		
t_{rr}	Reverse Recovery Time	$I_F = 30A$ $T_i = T_i = T_i$	$T_i = 25^{\circ}C$		85		ns
			$T_{i} = 125^{\circ}C$		160		
I_{RRM}	Maximum Reverse Recovery Current		$T_j = 25$ °C		4		Α
1RRM	Widainidin Reverse Recovery Current	$V_R = 400V$	$T_{i} = 125^{\circ}C$		8		А
0	Reverse Recovery Charge	$di/dt = 200A/\mu s$	$T_j = 25$ °C		130		nC
Q_{rr}			$T_j = 125$ °C		700		IIC
t _{rr}	Reverse Recovery Time	$I_F = 30A$ $V_R = 400V$			70		ns
Qrr	Reverse Recovery Charge		$T_j = 125$ °C		1300		nC
I_{RRM}	Maximum Reverse Recovery Current	$di/dt = 1000A/\mu s$			30		A

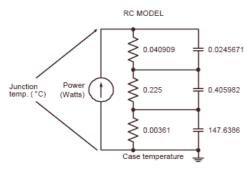
Thermal and package characteristics

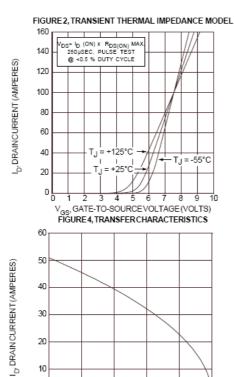
	Characteristic		Min	Тур	Max	Unit
R_{thJC}	Junction to Case Thermal Resistance	MOSFET			0.27	
		Diode			1.21	°C/W
R_{thJA}	Junction to Ambient (IGBT & Diode)				20	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz		2500			V
T_{J}, T_{STG}	Storage Temperature Range		-55		150	°C
$T_{ m L}$	Max Lead Temp for Soldering:0.063" from case for 10 sec				300	
Torque	Mounting torque (Mounting = 8-32 or 4mm Machine and terminals = 4mm Machine)				1.5	N.m
Wt	Package Weight			29.2		g

Typical MOSFET Performance Curve





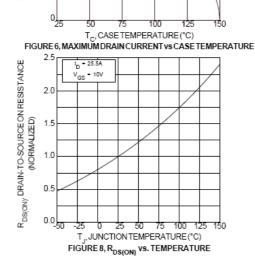


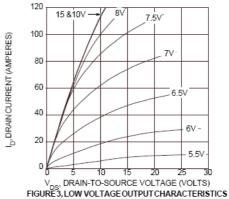


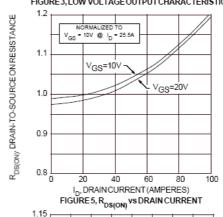
40

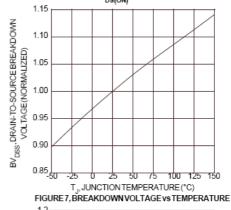
30

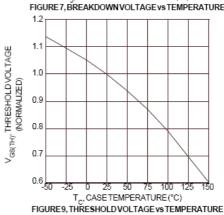
10



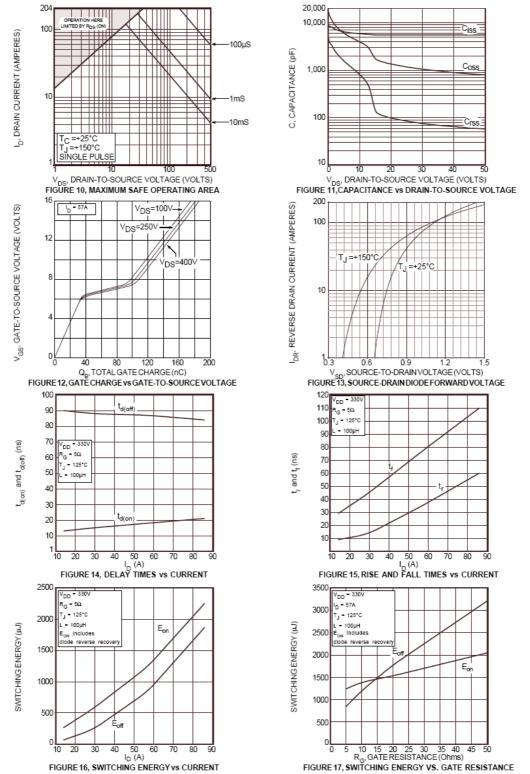














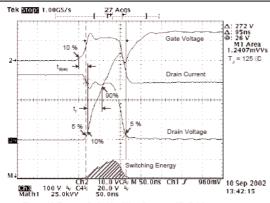


Figure 18, Turn-on Switching Waveforms and Definitions

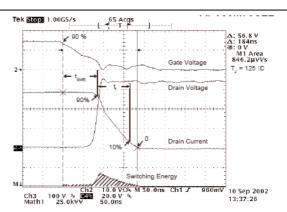


Figure 19, Turn-off Switching Waveforms and Definitions

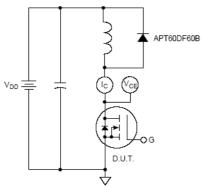
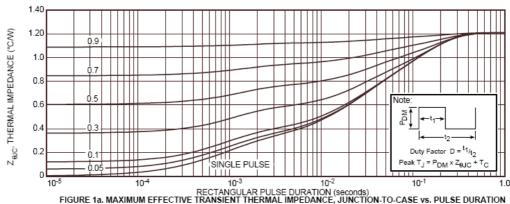


Figure 20, Inductive Switching Test Circuit

Typical Diode Performance Curve



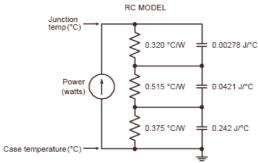


FIGURE 1b, TRANSIENT THERMAL IMPEDANCE MODEL



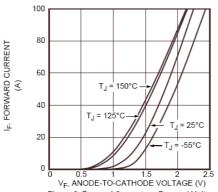


Figure 2. Forward Current vs. Forward Voltage

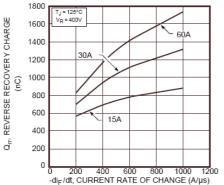


Figure 4. Reverse Recovery Charge vs. Current Rate of Change

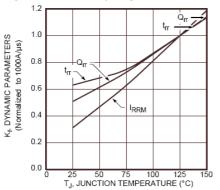


Figure 6. Dynamic Parameters vs. Junction Temperature

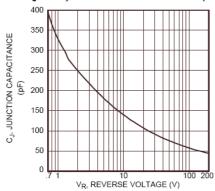


Figure 8. Junction Capacitance vs. Reverse Voltage

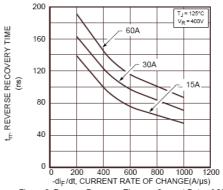


Figure 3. Reverse Recovery Time vs. Current Rate of Change

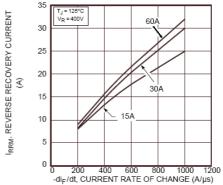


Figure 5. Reverse Recovery Current vs. Current Rate of Change

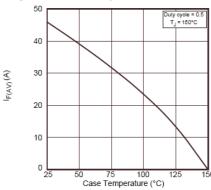


Figure 7. Maximum Average Forward Current vs. CaseTemperature



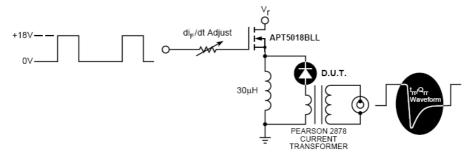
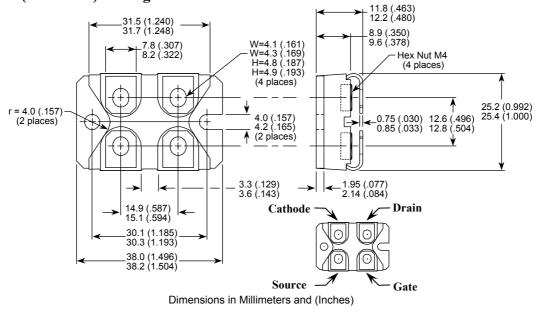


Figure 9. Diode Test Circuit

- 1 I_F Forward Conduction Current
 2 di_F/dt Rate of Diode Current Change Through Zero Crossing.
 3 I_{RRM} Maximum Reverse Recovery Current.
 4 t_{rr} Reverse Recovery Time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through I_{RRM} and 0.25 I_{RRM} passes through zero.
 5 Q_{rr} Area Under the Curve Defined by I_{RRM} and t_{rr}.
- The a olider the ourve belined by IRRM and trr.

Figure 10, Diode Reverse Recovery Waveform and Definitions

SOT-227 (ISOTOP®) Package Outline



ISOTOP® is a registered trademark of ST Microelectronics NV



DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.