

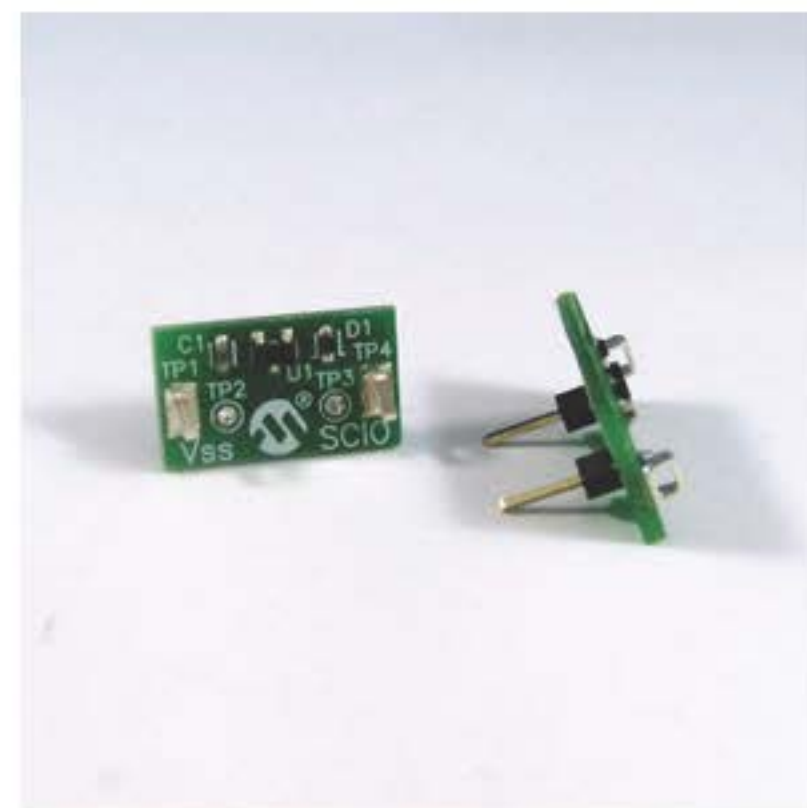
UNI/O Bus Parasitic Power Demo Board [Buy Now](#)

Part Number: AC243004



The UNI/O® Bus Parasitic Power Demo Board is designed to illustrate how a standard half-wave rectifier and capacitor circuit can be used to parasitically extract power for a UNI/O device from the SCIO signal as described in application note, AN1213 "Powering a UNI/O® Bus Device Through SCIO". This reduces the number of connections necessary for adding a UNI/O device to your application down to two: SCIO and Vss.

The board is designed to interface with MPLAB® Serial Memory Starter Kit, but the included test points allow you to interface the board with any application through the use of test leads (not included).



Features

Package Contents

- 11AA160, 16 Kbit UNI/O Serial EEPROM, featuring 1.8V to 5.5V operation
- Schottky diode and 4.7 μ F capacitor for extracting power parasitically
- Test points for application or oscilloscope connections
- Headers for interfacing with standard .300"-wide DIP sockets

Documentation & Software

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