54LS/74LS247

BCD TO 7-SEGMENT DECODER/DRIVER

(With Open-Collector Outputs)

PINOUT A

A1 1 16 Vcc

A2 2 15 f

LT 3 14 g

BI/RBO 4 13 a

RBI 5 12 b

A3 6 11 c

A0 7 10 d

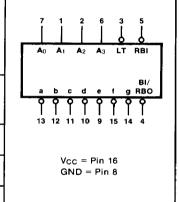
GND 8 9 e

CONNECTION DIAGRAM

DESCRIPTION — The 'LS247 has active LOW open-collector outputs guaranteed to sink 12 mA (Military) or 24 mA (Commercial). It has the same electrical characteristics and pin connections as the 'LS47. The only difference is that the 'LS247 will light the top bar (segment a) for numeral 6 and the bottom bar (segment d) for numeral 9. For detailed description and specifications please refer to the 'LS47 data sheet.

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0 \text{ V } \pm 5\%,$ $T_A = 0^{\circ} \text{ C to } +70^{\circ} \text{ C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%,$ $T_A = -55^{\circ}\text{ C to} + 125^{\circ}\text{ C}$	
Plastic DIP (P)	A	74LS247PC		9B
Ceramic DIP (D)	Α	74LS247DC	54LS247DM	6B
Flatpak (F)	А	74LS247FC	54LS247FM	4L



LOGIC SYMBOL

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PIN NAMES	DESCRIPTION	54/74L\$ (U.L. HIGH/LOW
10 — Аз	BCD Inputs	0.5/0.25
A ₀ — A3 RBI	Ripple Blanking Input (Active LOW)	0.5/0.25
LT.	Lamp Test Input (Active LOW)	0.5/0.25
BI/RBO	Blanking Input (Active LOW) or	0.5/0.25
	Ripple Blanking Output (Active LOW)	1.25/2.0
		(1.0)
ā−ÿ	Segment Outputs (Active LOW)	OC*/15
		(7.5)

*OC - Open Collector