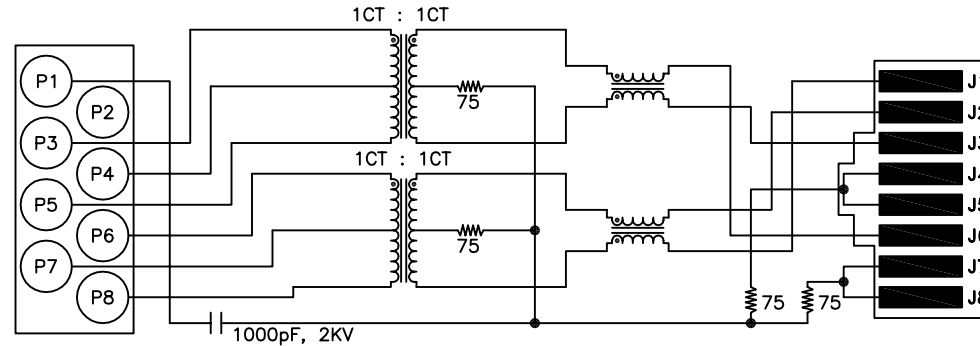




ELECTRICAL SPECIFICATIONS:

- 1.0 TURNS RATIO: (P5-P4-P3) : (J3-J6) : 1CT : 1CT $\pm$ 3%
(P8-P7-P6) : (J1-J2) : 1CT : 1CT $\pm$ 3%
- 2.0 INDUCTANCE: (P3-P5) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
(P8-P6) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
- 3.0 LEAKAGE INDUCTANCE: P3-P5 (WITH J6 AND J3 SHORT) : 0.3 MAX. @ 1MHz
P6-P8 (WITH J2 AND J1 SHORT) : 0.3 MAX. @ 1MHz
- 4.0 INTERWINDING CAPACITANCE: (P3,P5,P4) TO (J6,J3) : 30pf MAX @ 1MHz
(P8,P7,P6) TO (J2,J1) : 30pf MAX. @ 1MHZ
- 5.0 DC RESISTANCE: (J6-J3)=(J2-J1) : 1.3 ohms Max.



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RECEIVE

6.0 RETURN LOSS: (P8-P6)=100 OHMS AND (P5-P3)=100 OHM REF.

1MHz TO 30MHz : 18dB MIN.
30MHz TO 80MHz : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J3-J6).

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P8, P6) : 1500 VAC
(J3, J6) TO (P3, P5) : 1500 VAC

8.0 INSERTION LOSS: RS=RL=100 ohms
100KHz TO 100MHz : 1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS
OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX
PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1-100 MHz : 30 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : 35dB TYP

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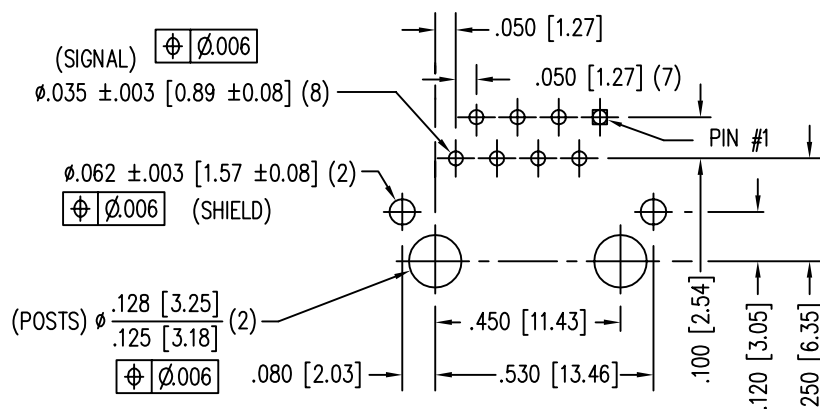
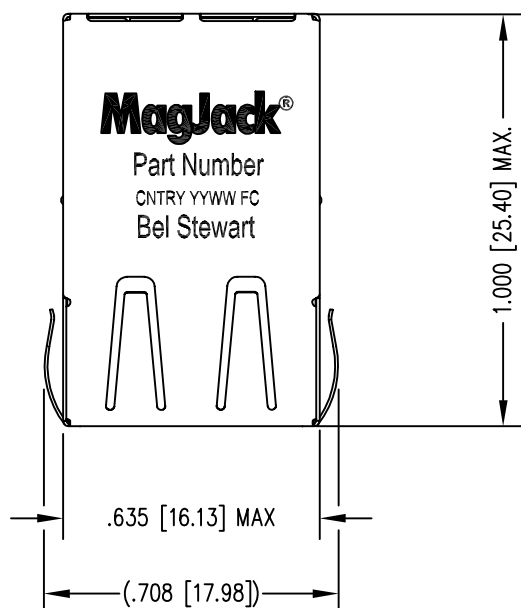
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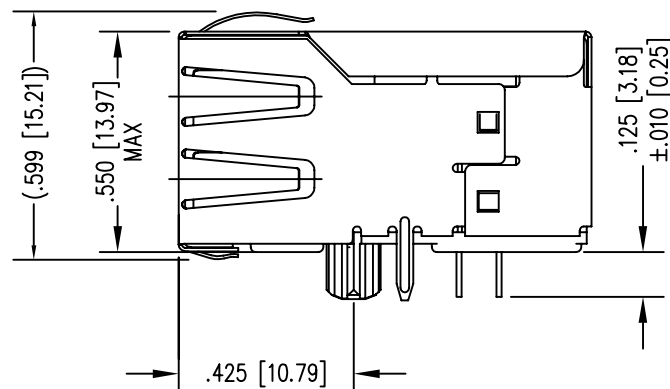
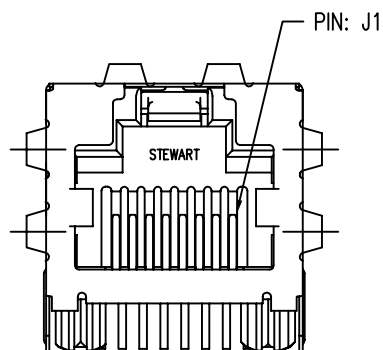
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P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.



NOTES:

- CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]
- WAVE SOLDER COMPATIBLE - PREHEAT 125°C/90SECS.

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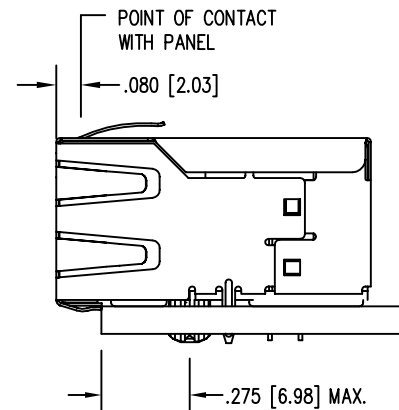
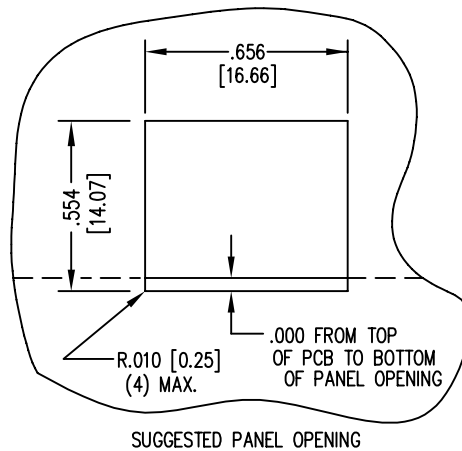
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1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ± 0.005 [0.13]

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