

PFE500-48 Application Board Test Data

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Begin Report

Program Name: PFE50048_PROMOTION BOARD

Version: 1.80

Date: 10/10/2007

Serial Number: EBHAL021689P1_2

Time: 8:57:32 AM

Operator: admin

TEST ROUTINE	Minimum	Reading	Maximum	Pass/Fail
Inrush Test				
Peak Inrush (Amps)	0.000	27.450	40.000	Pass
Delay after Inrush test				
Delay 5000 ms				
Input Current Test @ 100VAC				
Input current (AC Amps)	0.200	6.105	6.200	Pass
Efficiency @ 100VAC FL				
Efficiency (%)	84.000	85.054	100.000	Pass
Output Power (Watts)		503.690		
Input Power (Watts)		592.200		
Power Factor (Watts/VA)		0.972		
Power Factor Test @100VAC FL				
Power Factor	0.950	0.970	1.000	Pass
Peak-Peak ripple test FL 85VAC				
+48VDC(PkPk mV)	0.000	4.247	480.000	Pass
Load Reg. Low Line 85vac				
+48VDC(Delta mV)	-96.000	-30.365	96.000	Pass
Line Reg. Full Load				
+48VDC(Delta mV)	-96.000	-0.126	96.000	Pass
Peak-Peak ripple test FL 265VAC				
+48VDC(PkPk mV)	0.000	3.700	480.000	Pass
Load Reg. High Line @265VAC				
+48VDC(Delta mV)	-96.000	31.364	96.000	Pass
Line Reg No Load				
+48VDC(Delta mV)	-100.000	0.126	100.000	Pass
Undervoltage test				
UUT Lock Out Volts	65.000	70.000	84.000	Pass
Turn On UUT Volts	65.000	81.667	84.000	Pass
Delay before hold up test				
Delay 2000 ms				
Hold up time @100vac Full Power				
Event Time (mSec)	10.000	43.910	100.000	Pass
Delay after hold up				
Output Voltage Check				
+48VDC(DcV)	47.040	48.006	48.960	Pass
The End				
Delay 500 ms				

PFE50048-EB NEW HAL021689P1 BOARD

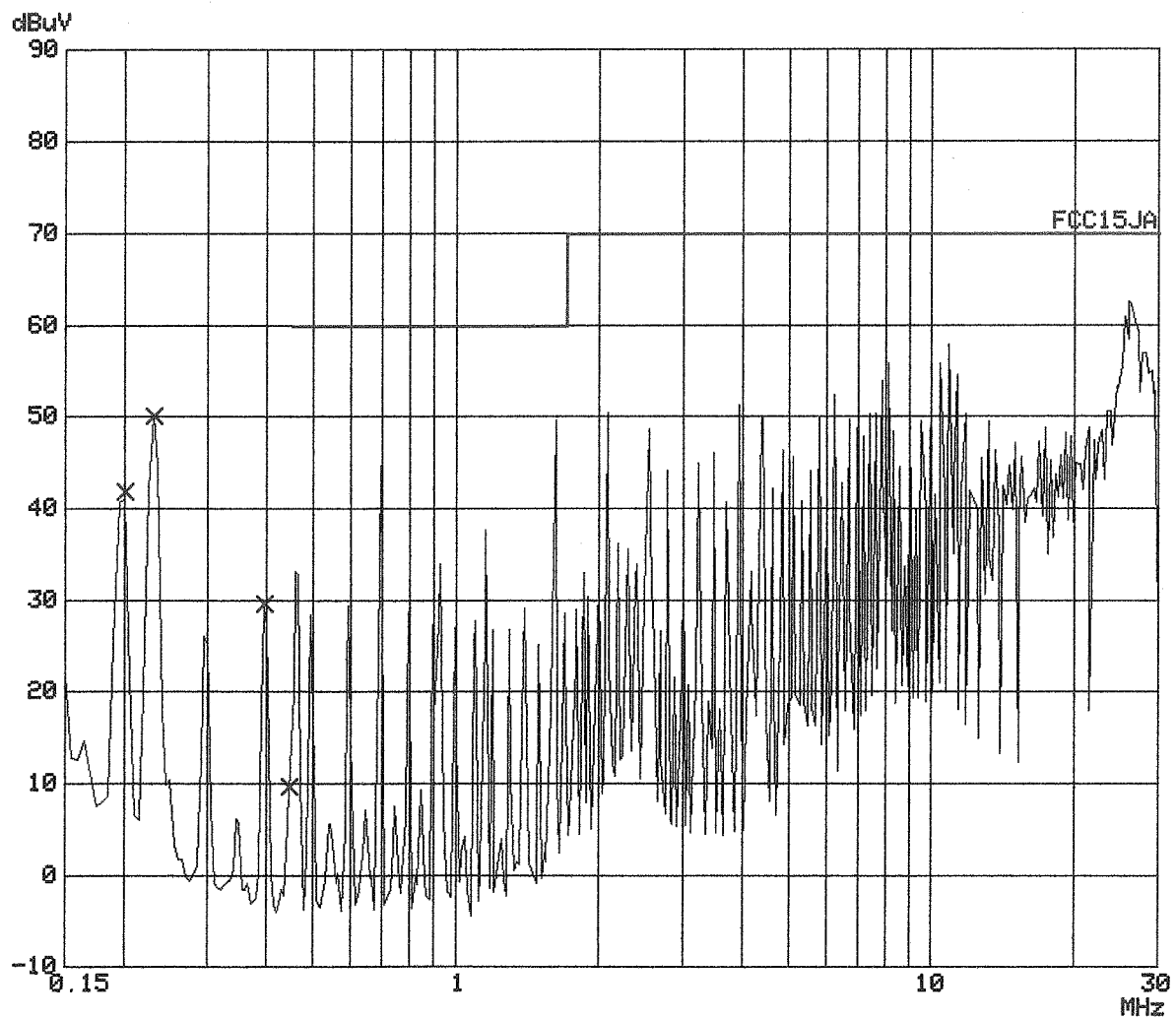
Vin= 265 vac

LEAKAGE CURRENT TEST

<u>TEST POINTS</u>	<u>NORMAL</u>	<u>REVERSE</u>
LINE-GROUND	540 uA	550 uA
NEUTRAL-GROUND	540 uA	550 uA

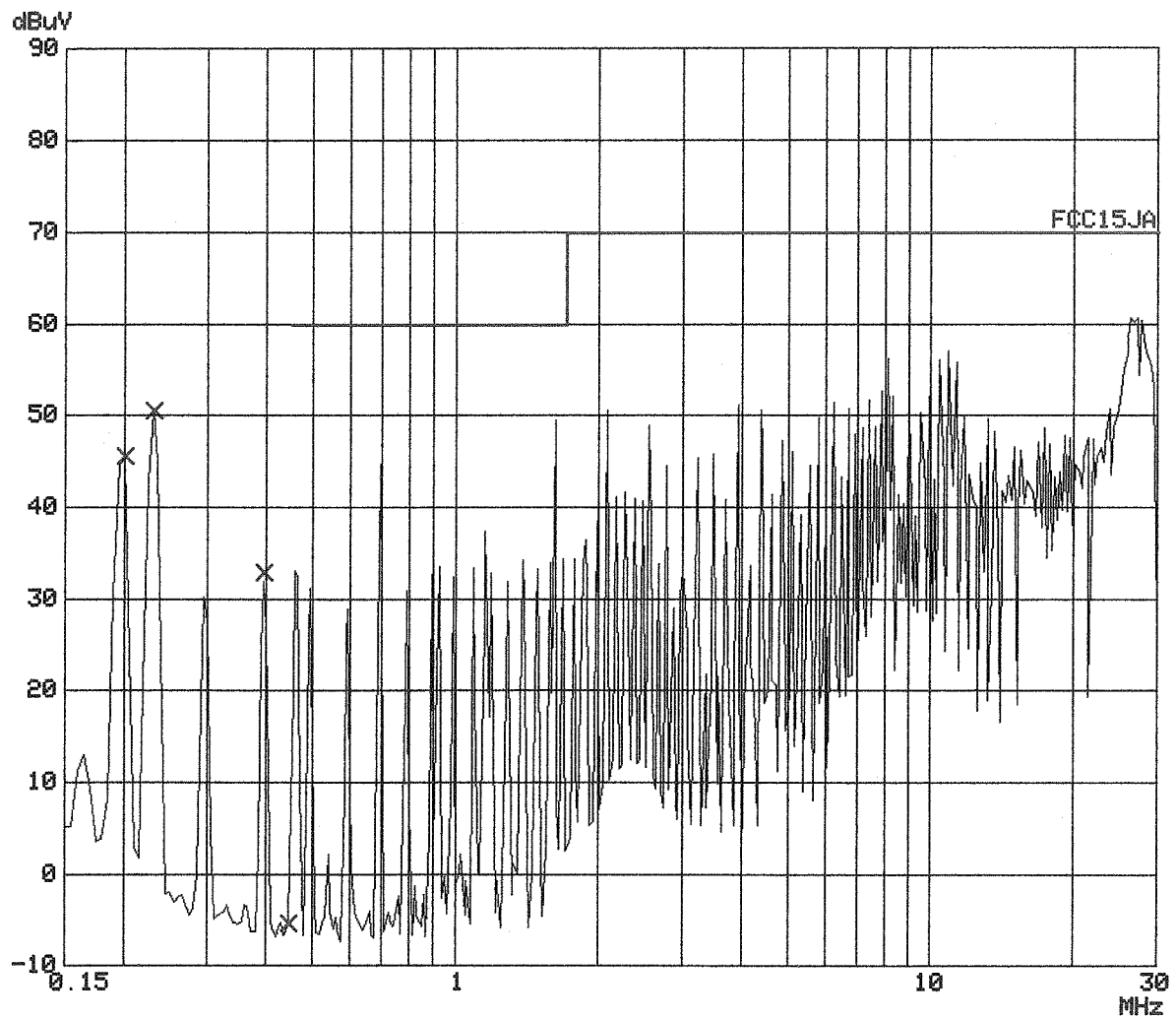
EVALUATION BOARD PFE50048

Manuf: FEDERICO_JUAN
Op Cond: 10.5A_48V_265VAC
Operator: TIMO
Date: 10. Oct 07 11:28



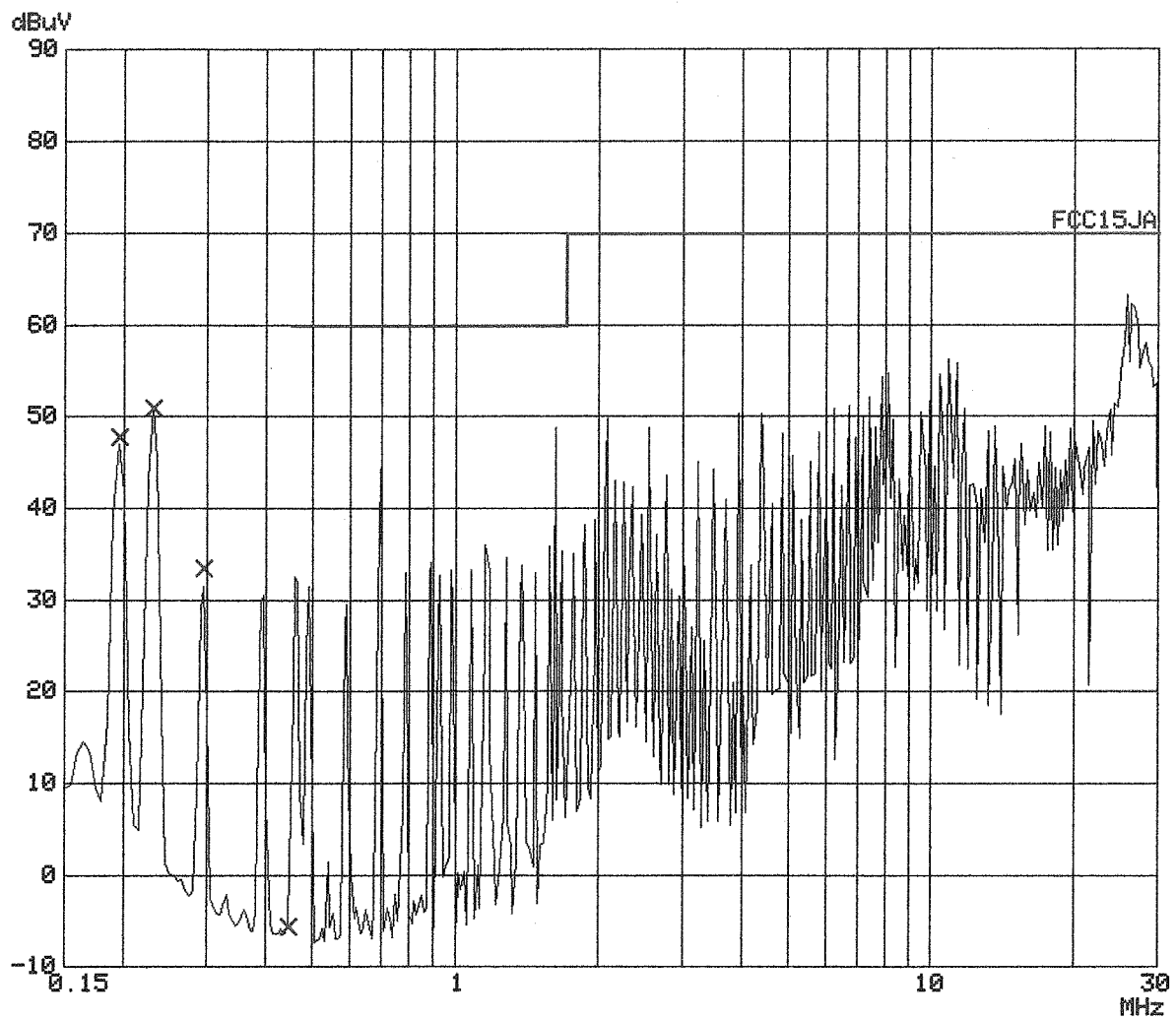
EVALUATION BOARD PFE50048

Manuf: FEDERICO_JUAN
Op Cond: 10.5A_48V_100VAC
Operator: TIMO
Date: 10. Oct 07 11:44



EVALUATION BOARD PFE50048

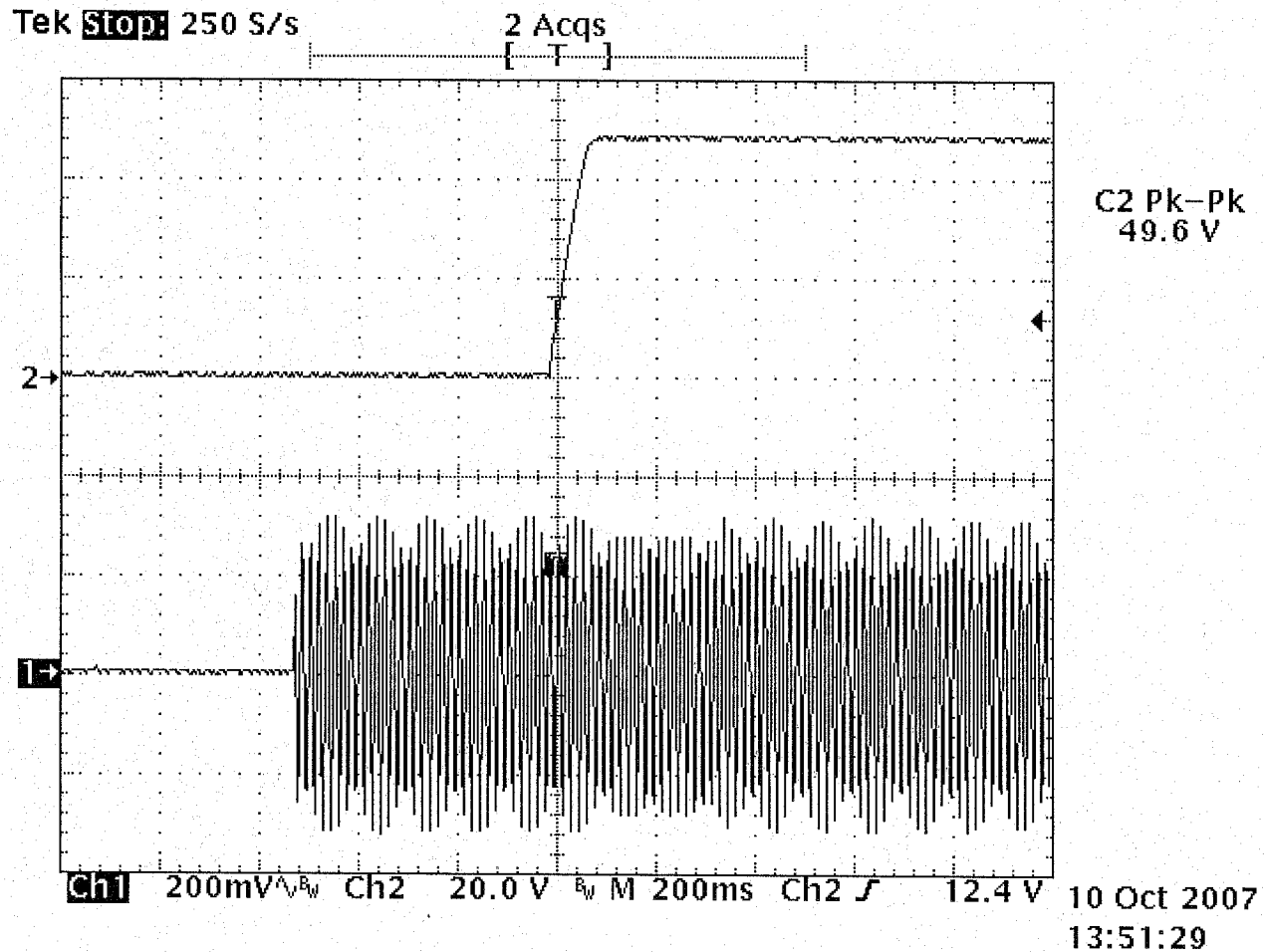
Manuf: FEDERICO_JUAN
Op Cond: 10.5A_48V_85VAC
Operator: TIMO
Date: 10. Oct 07 11:55



$V_{in} = 100 \text{ VAC}$
 $I_o = 10.5 \text{ Amps}$
 $V_o = 48 \text{ Vdc}$

PFES0048-EB
HAL021689 rev. P1

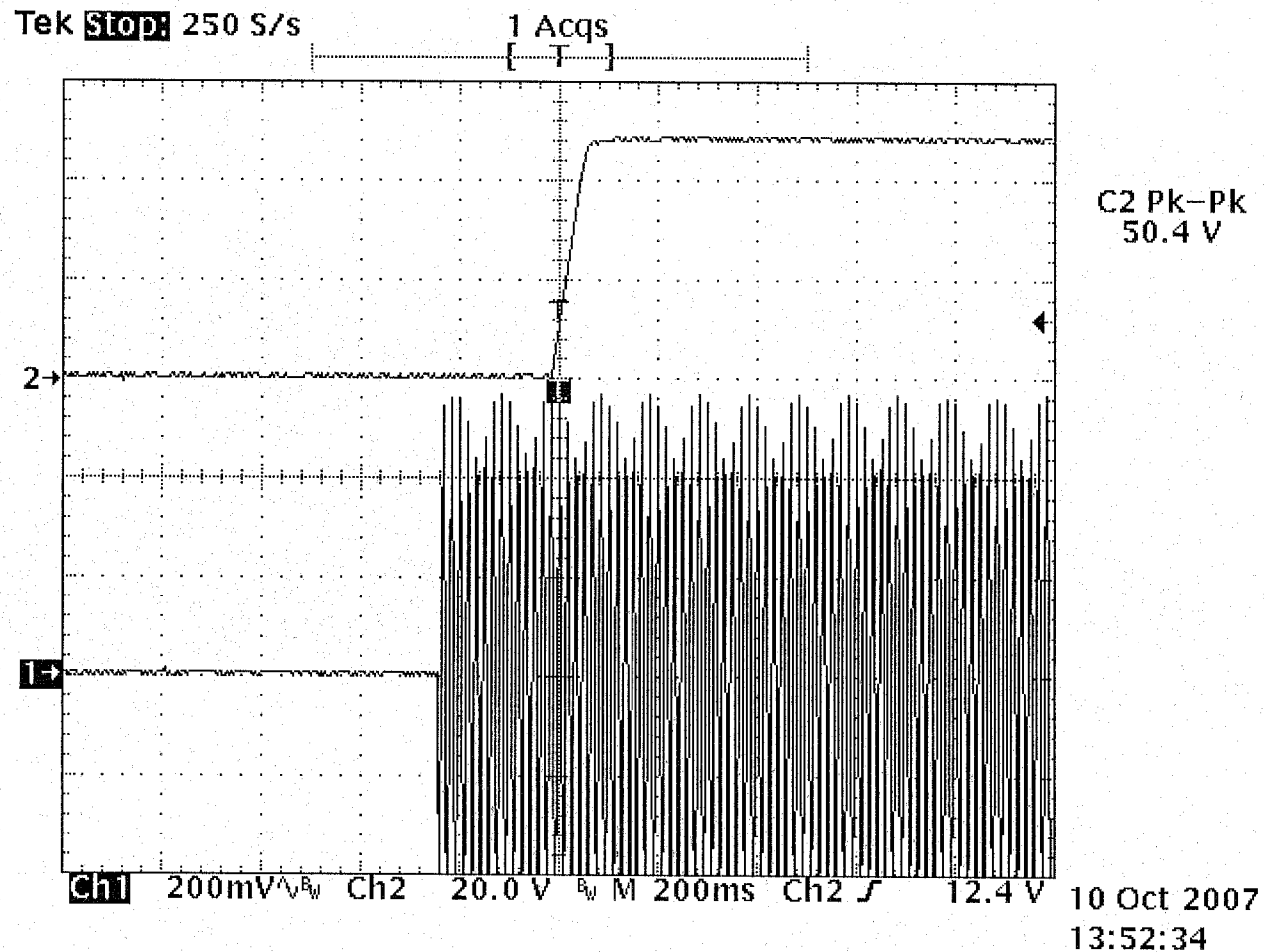
OUTPUT RISE
(TURN-ON TIME)



$V_{in} = 200 \text{ VAC}$
 $I_o = 10.5 \text{ Amps}$
 $V_o = 48 \text{ Vdc}$

PFES0048-EB
HA6021689 rev. P1

OUTPUT RISE
(TURN-ON TIME)

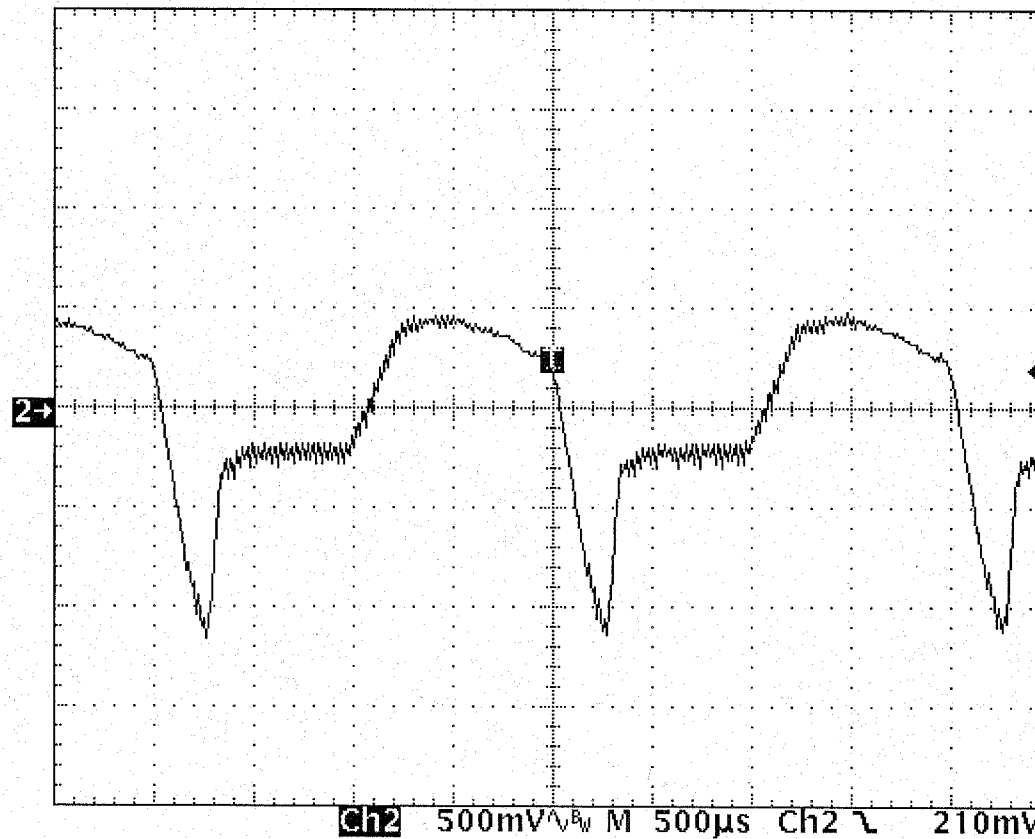


$V_{in} = 100 \text{ VAC}$
 $V_o = 48 \text{ Vdc}$
 $I_o = 0 - 25\%$

PFE50048-EB
HA4021689 rev. P1

Tek Run: 100kS/s

Sample



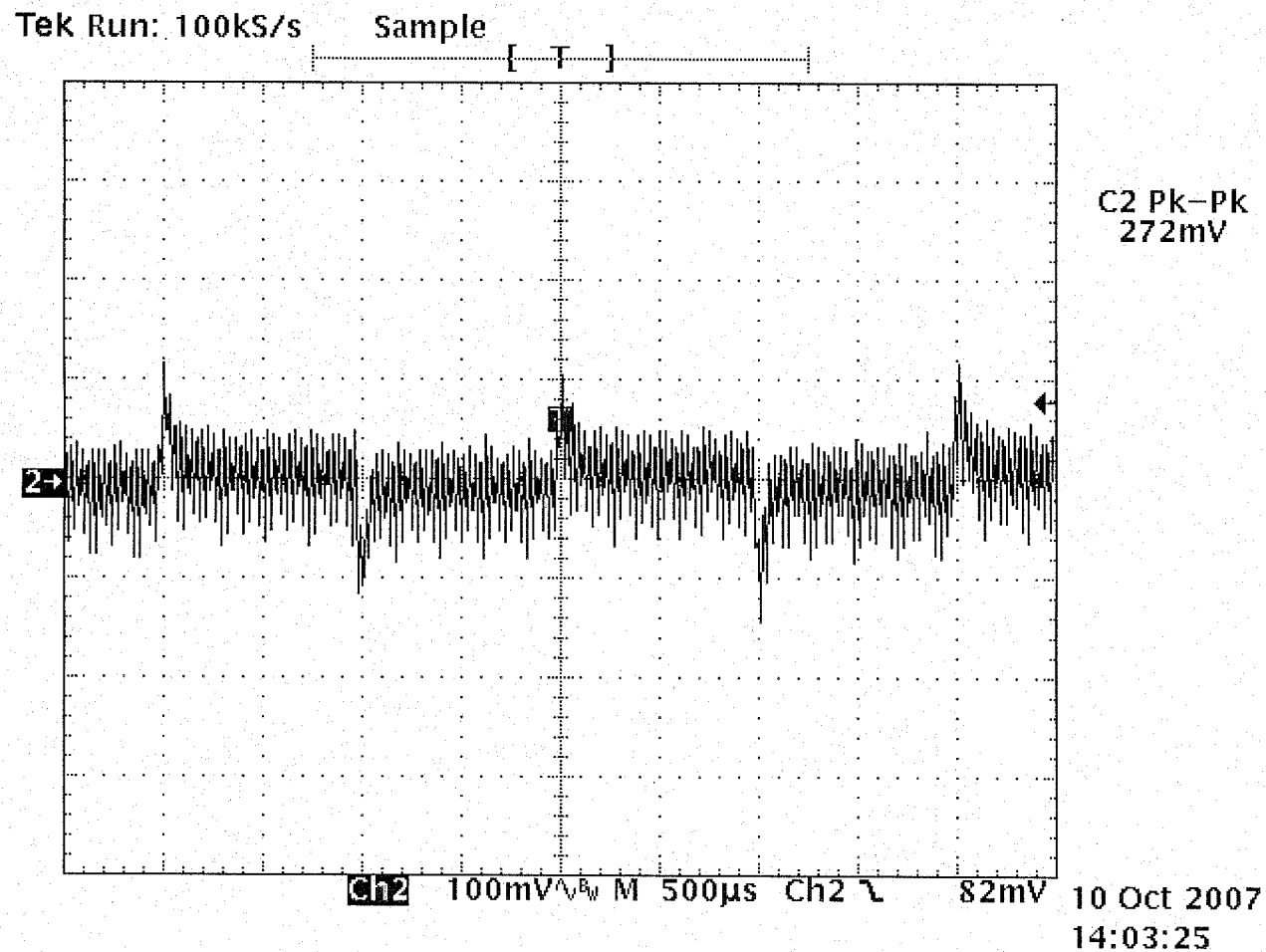
C2 Pk-Pk
1.64 V

ch2 500mV V_{BW} M 500µs Ch2 210mV

10 Oct 2007
14:01:15

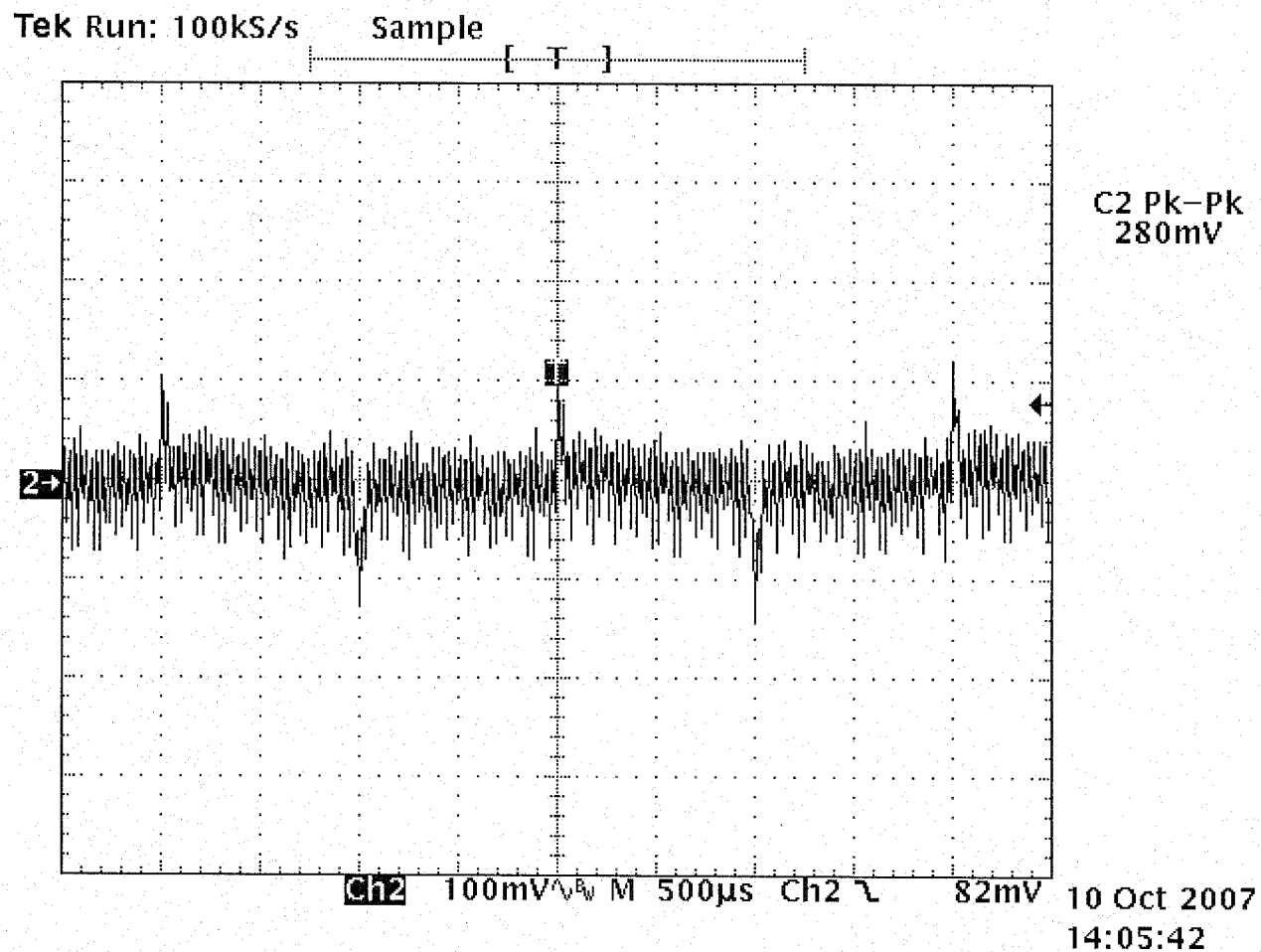
$V_{in} = 100V_{AC}$
 $I_o = 25 - 50\%$
 $V_o = 48V_{dc}$

PFE50048-EB
HAL024689 rev. P1



$V_{in} = 100VAC$
 $V_o = 48Vdc$
 $I_o = 50-75\%$

PFES0048-EB
HA021689 rev. P1



$V_{IN} = 100 \text{ VAC}$

$V_o = 48 \text{ Vdc}$

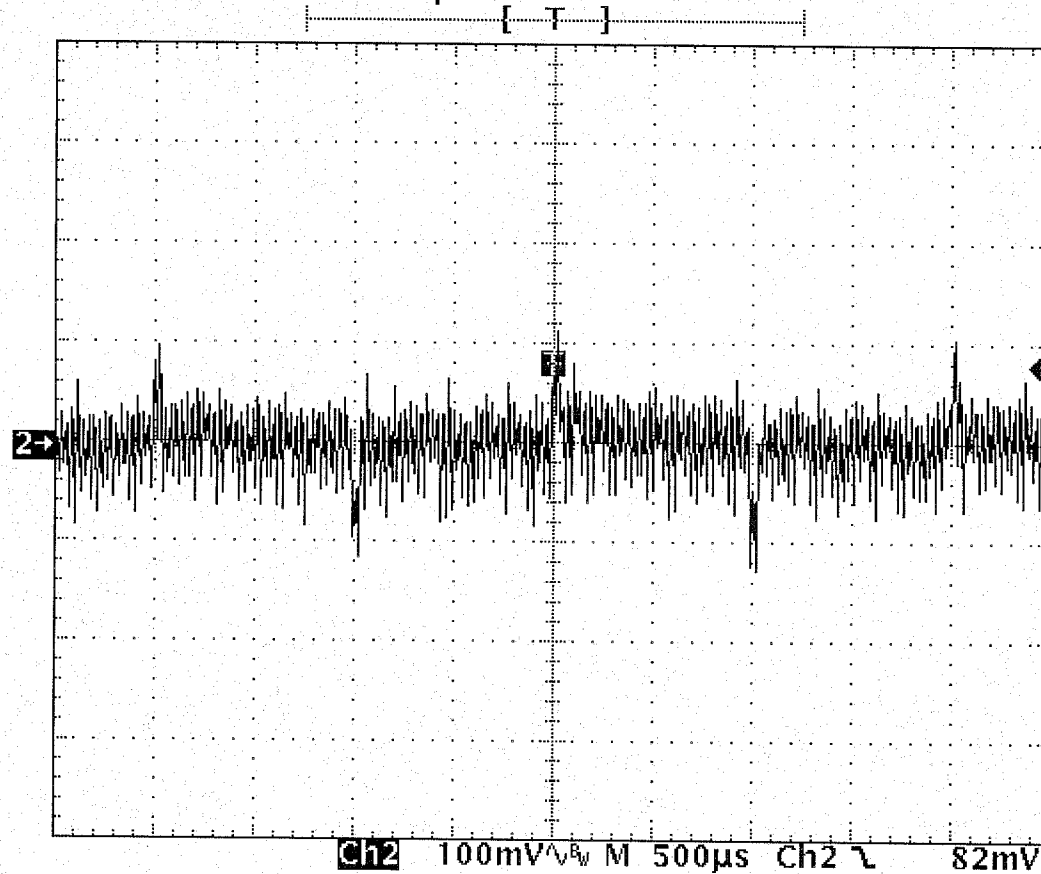
$I_o = 75 - 100\%$

PFES0048-EB

HA021689 rev. P1

Tek Run: 100kS/s

Sample



C2 Pk-Pk
284mV

10 Oct 2007
14:06:59

$V_{in} = 100 \text{ VAC}$

$V_o = 48 \text{ Vdc}$

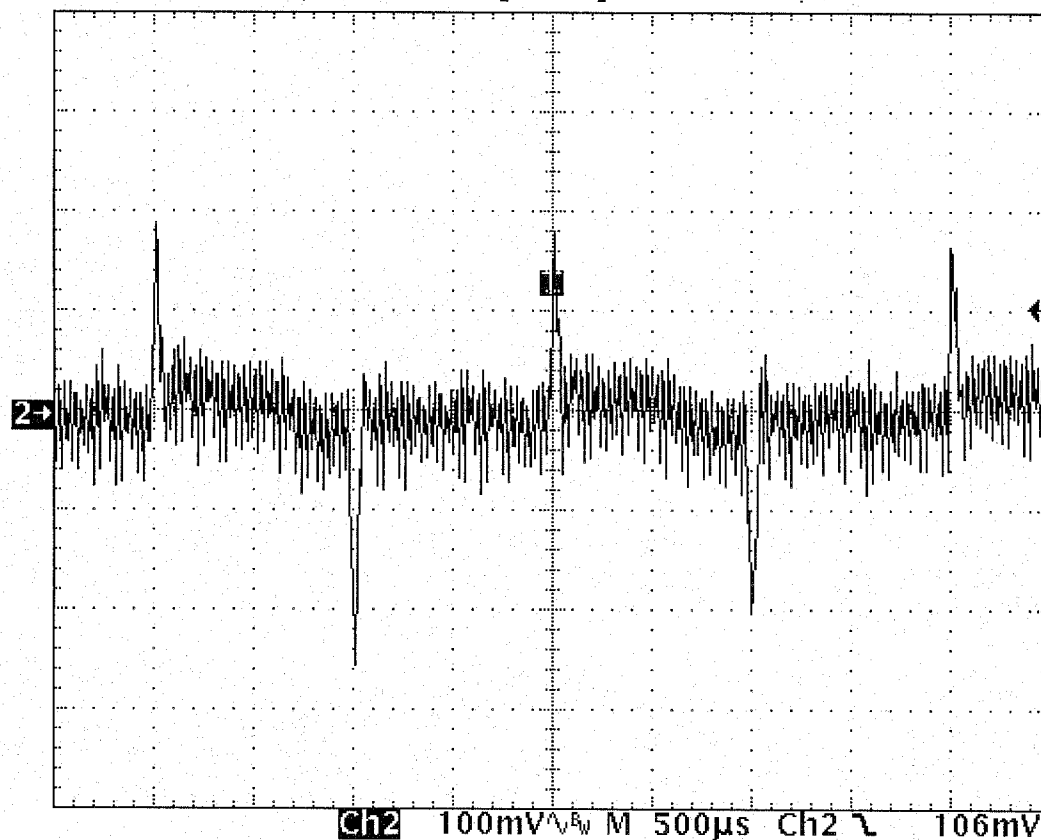
$I_o = 50-100\%$

PFES0048-EB

HA021689 rev. P1

Tek Run: 100kS/s

Sample



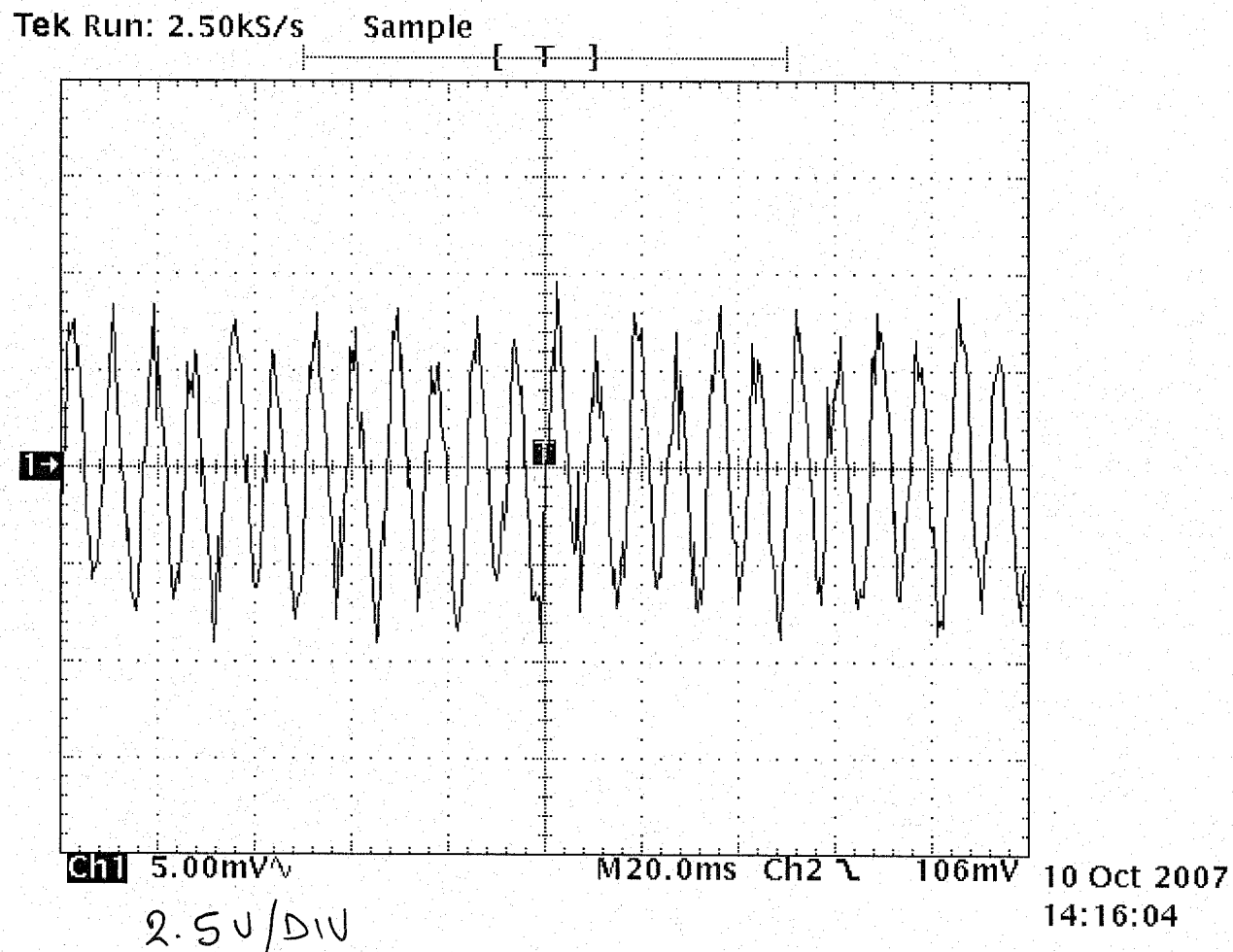
C2 Pk-Pk
448mV

Ch2 100mV V_{B_W} M 500µs Ch2 106mV

10 Oct 2007
14:08:15

$V_{in} = 100 \text{ VAC}$
 $V_o = 48 \text{ Vdc}$
 $I_o = 10.5 \text{ Amps}$

PFES0048-EB
HAL021689 rev. P1



$$V_{C102} = V_{\text{BOOST}} (P-P)$$

BOOST RIPPLE

$V_{in} = 200 \text{ VAC}$

$V_o = 48 \text{ Vdc}$

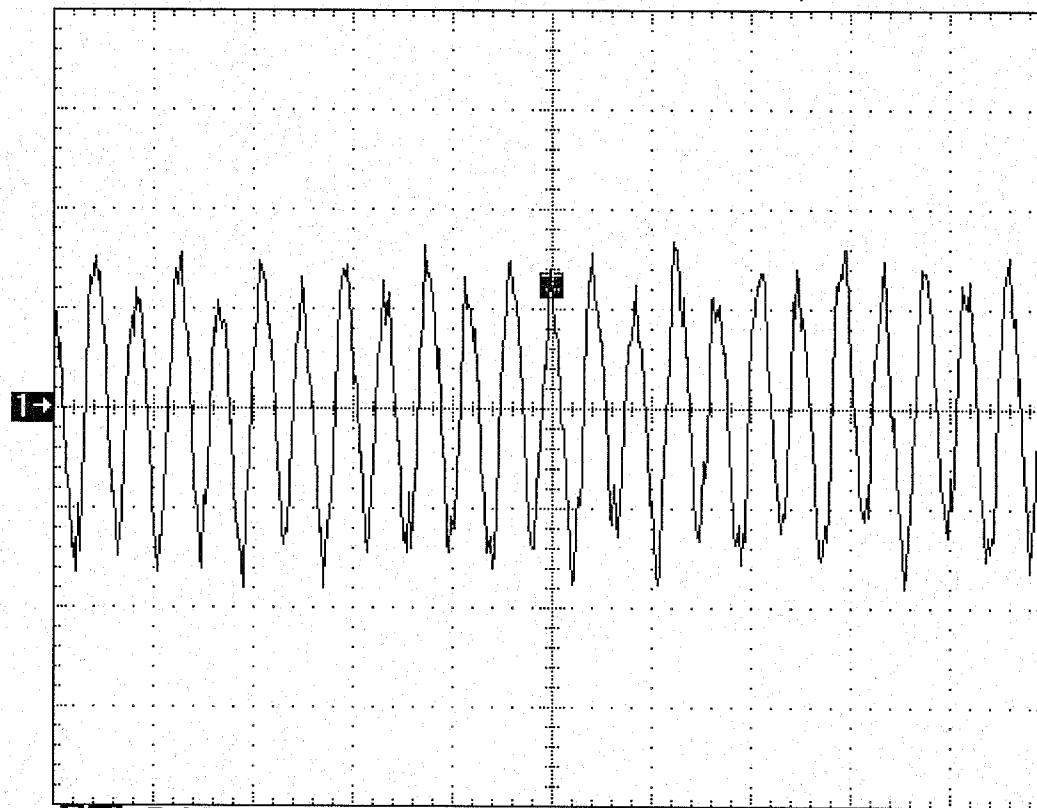
$I_o = 10.5 \text{ Amps}$

PFES0048-EB

HAL021689 rev. P1

Tek Run: 2.50kS/s

Sample



Ch1 5.00mV/div

2.5V/DIV

M20.0ms

Ch2

106mV

10 Oct 2007

14:16:48

$V_{C102} = \text{BOOST RIPPLE}$

$V_{in} = 100 \text{ VAC}$
 $I_a = 10.5 \text{ AMPS}$
 $V_o = 48 \text{ Vdc}$

PFES0048-EB
HA021689 rev. P1

