

FMC-SDP Interposer

Allows any Analog Devices SDP Evaluation Board to be used on a Xilinx® evaluation board with a FMC connector

Uses Low Pin Count (LPC) FMC connector

Can be used on either LPC or HPC hosts

The FMC-SDP interposer allows any Analog Devices SDP Evaluation Board to be used on a Xilinx® evaluation board with a FMC connector. The interposer uses the Low Pin Count (LPC) version of the FMC connector, so it can be used on either LPC or HPC hosts. The interposer can only be used with FPGA boards that support 3.3VIO for the FMC connection.

For more information and a list of existing projects which can be used with the FMC-SDP Interposer, please visit:

<http://wiki.analog.com/sdp-i-fmc>

