

TOSHIBA INTELLIGENT POWER DEVICE
SILICON MONOLITHIC POWER MOS INTEGRATED CIRCUIT

TPD1033F

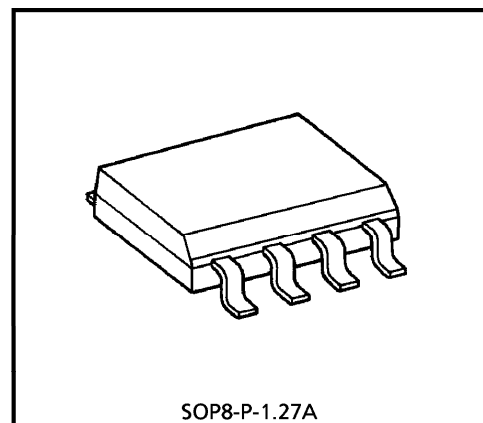
HIGH-SIDE POWER SWITCH for MOTORS, SOLENOIDS, and LAMP DRIVERS

TPD1033F is a monolithic power IC for high-side switches. The IC has a vertical MOS FET output which can be directly driven from a CMOS or TTL logic circuit (eg, an MPU). The device offers intelligent self-protection and diagnostic functions.

FEATURES

- A monolithic power IC with a new structure combining a control block (Bi-CMOS) and a vertical power MOS FET (π -MOS) on a single chip.
- One side of load can be grounded to a high-side switch.
- Can directly drive a power load from a microprocessor.
- Built-in protection against overheating and load short circuiting.
- Incorporates a diagnosis function that allows diagnosis output to be read externally at load short circuiting, opening, or overheating.
- Up to $-10V$ of counterelectromotive force from an L load can be applied.
- Low on resistance : $R_{ON} = 220\text{ m}\Omega$ (Max)
- Low operating current : $I_{DD} = 1\text{ mA}$ (Typ.), at $V_{DD} = 12\text{ V}$, $V_{IN} = 0$
- 8-pin SOP package for surface mounting can be packed in tape.

(Note) : That because of its MOS structure, this product is sensitive to static electricity.

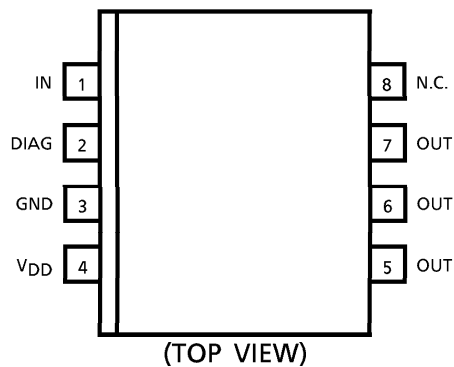


Weight : 0.08 g (Typ.)

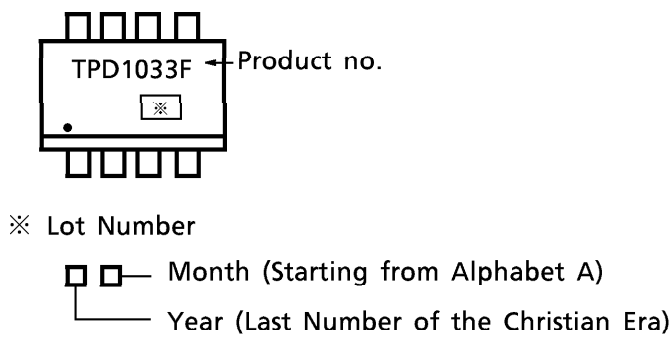
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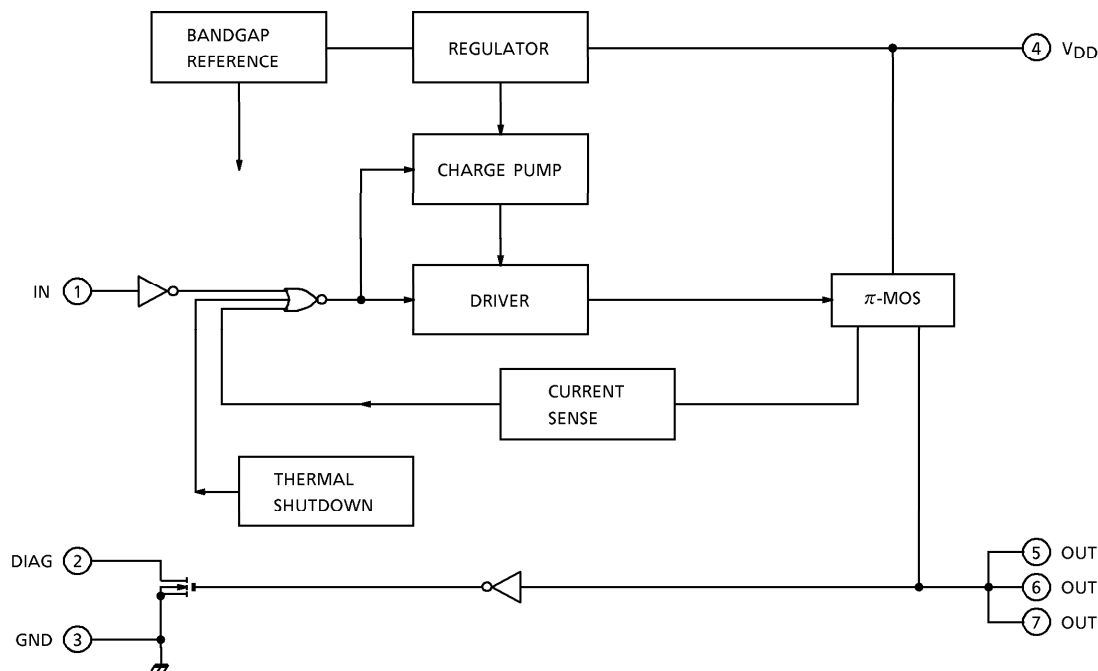
PIN ASSIGNMENT



MARKING



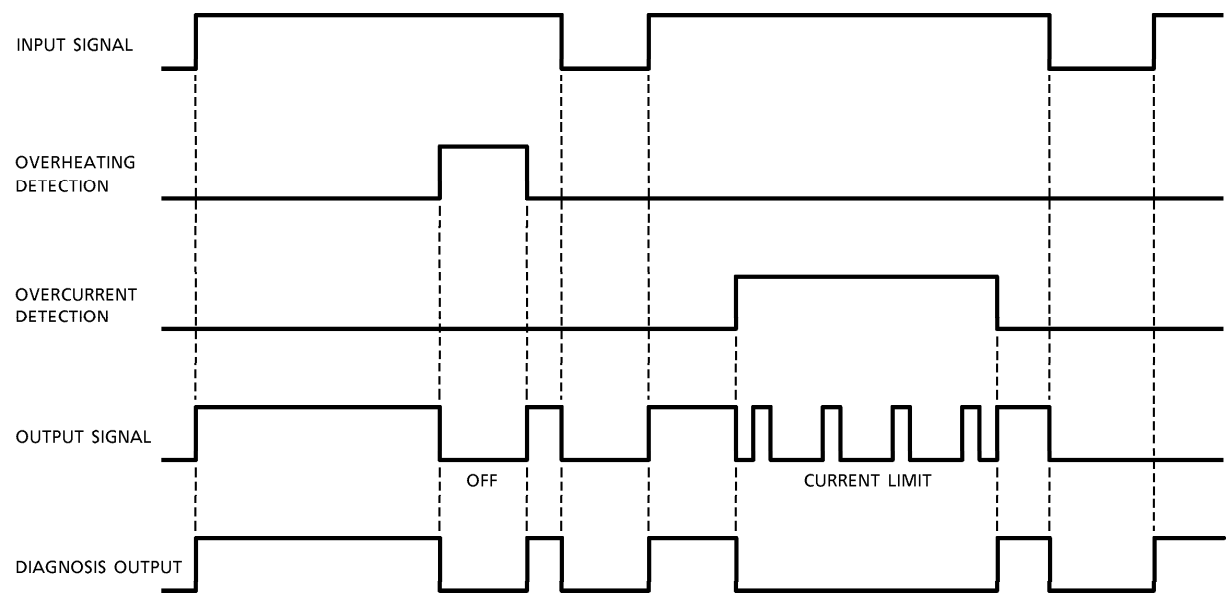
BLOCK DIAGRAM



PIN DESCRIPTION

PIN No.	SYMBOL	FUNCTION
1	IN	Input is CMOS-compatible, with pull-down resistor connected. Even if the input is open, output will not accidentally turn on.
2	DIAG	Self-diagnosis detection pin. Goes low when overheating is detected or when output is short circuited with input on (high). N-channel open drain.
3	GND	Ground pin.
4	VDD	Power pin.
5, 6, 7	OUT	Output pin. When the load is short circuited and current in excess of the detection current (8A Typ.) flows to the output pin, the output automatically turns on or off.

TIMING CHART



TRUTH TABLE

INPUT SIGNAL	OUTPUT SIGNAL	DIAGNOSIS OUTPUT	STATE
H	H	H	Normal
L	L	L	
H	L	L	Load short circuited
L	L	L	
H	H	H	Load open
L	H	H	
H	L	L	Overheating
L	L	L	

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-source Voltage		V _{DS}	60	V
Supply Voltage	DC	V _{DD} (1)	25	V
	Pulse	V _{DD} (2)	60 (R _s = 1 Ω, τ = 250 ms)	V
Input Voltage	DC	V _{IN} (1)	− 0.5~12	V
	Pulse	V _{IN} (2)	V _{DD} (1) + 1.5 (t = 100 ms)	V
Diagnosis Output Voltage		V _{DIAG}	− 0.5~25	V
Output Current		I _O	Internally Limited	A
Input Current		I _{IN}	± 10	mA
Diagnosis Output Current		I _{DIAG}	5	mA
Power Dissipation (T _a = 25°C)		P _D	1.4 (Note 1)	W
			2.4 (Note 2)	
Operating Temperature		T _{opr}	− 40~110	°C
Junction Temperature		T _j	150	°C
Storage Temperature		T _{stg}	− 55~150	°C
Lead Temperature / time		T _{SOL}	275 (5 s), 260 (10 s)	°C

THERMAL RESISTANCE

CHARACTERISTIC	SYMBOL	TEST CONDITION	UNIT
Thermal Resistance	R _{th} (j-a)	89.3 (Note 1)	°C / W
		52.1 (Note 2)	

(Note 1) : Mounted on glass epoxy board (1 inch² × 0.8 t) (DC)(Note 2) : Mounted on glass epoxy board (1 inch² × 0.8 t) (t_w ≤ 10 s)

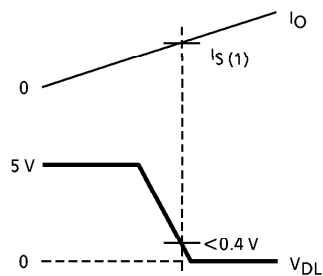
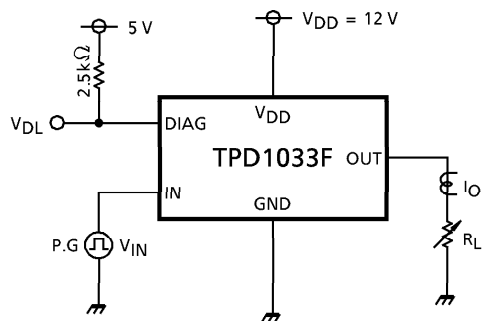
ELECTRICAL CHARACTERISTICS (Unless otherwise specified, $T_c = -40 \sim 110^\circ\text{C}$, $V_{DD} = 8 \sim 18\text{ V}$)

CHARACTERISTIC		SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Operating Supply Voltage		$V_{DD}(\text{OPR})$	—	—	5	12	18	V
Current Dissipation		I_{DD}	—	$V_{DD} = 12\text{ V}$, $V_{IN} = 0$	—	1	5	mA
Input Voltage		V_{IH}	—	$V_{DD} = 12\text{ V}$, $I_O = 2\text{ A}$	3.5	—	—	V
		V_{IL}	—	$V_{DD} = 12\text{ V}$, $I_O = 1.2\text{ mA}$	—	—	1.5	V
Input Current		$I_{IN}(1)$	—	$V_{DD} = 12\text{ V}$, $V_{IN} = 5\text{ V}$	—	50	200	μA
		$I_{IN}(2)$	—	$V_{DD} = 12\text{ V}$, $V_{IN} = 0$	-0.2	—	0.2	μA
On Voltage		$V_{DS}(\text{ON})$	—	$V_{DD} = 12\text{ V}$, $I_O = 2\text{ A}$, $T_c = 25^\circ\text{C}$	—	—	0.44	V
On Resistance		$R_{DS}(\text{ON})$	—	$V_{DD} = 12\text{ V}$, $I_O = 2\text{ A}$, $T_c = 25^\circ\text{C}$	—	—	0.22	Ω
Output Leakage Current		I_{OL}	—	$V_{DD} = 18\text{ V}$, $V_{IN} = 0$	—	—	1.2	mA
Diagnosis Output Voltage	"L" Level	V_{DL}	—	$V_{DD} = 12\text{ V}$, $I_{DL} = 2\text{ mA}$	—	—	0.4	V
Diagnosis Output Current	"H" Level	I_{DH}	—	$V_{DD} = 18\text{ V}$, $V_{DH} = 18\text{ V}$	—	—	10	μA
Overcurrent Detection		$I_S(1)$ (Note 3)	1	$V_{DD} = 12\text{ V}$, $T_c = 25^\circ\text{C}$	4	6	8	A
		$I_S(2)$ (Note 4)	2		4	8	12	A
Overheating Detection	Temperature	T_s	—	—	150	160	200	$^\circ\text{C}$
	Hysteresis	ΔT_s	—		—	10	—	$^\circ\text{C}$
Open Detection Resistance		R_{ops}	—	$V_{DD} = 8\text{ V}$	1	20	100	$\text{k}\Omega$
Switching Time		t_{ON}	3	$V_{DD} = 12\text{ V}$, $R_L = 5\text{ }\Omega$, $T_c = 25^\circ\text{C}$	10	100	—	μs
		t_{OFF}	3		10	30	—	μs

(Note 3) : Overcurrent detection value when load is short circuited and $V_{IN} = \text{"L"} \rightarrow \text{"H"}$ (Note 4) : Overcurrent detection value when load current is increased while $V_{IN} = \text{"H"}$

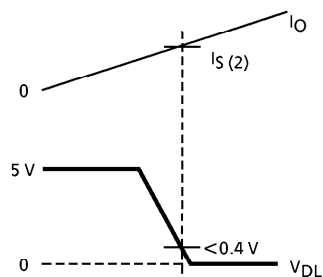
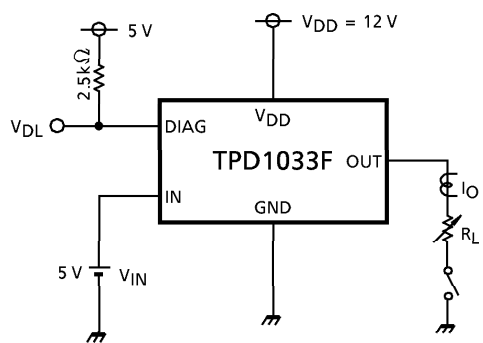
TEST CIRCUIT 1

Over-voltage detection



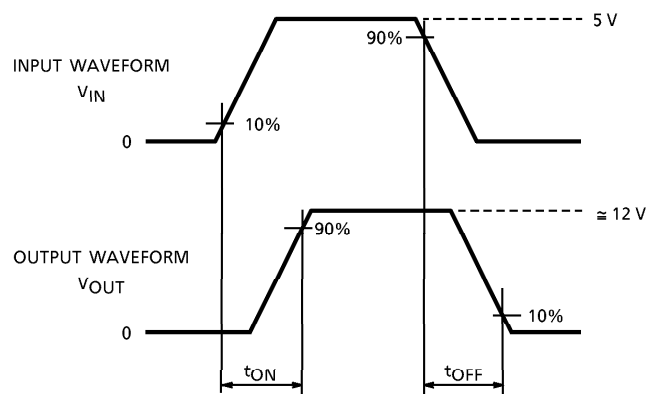
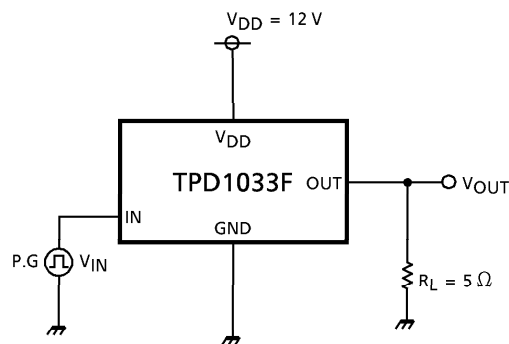
TEST CIRCUIT 2

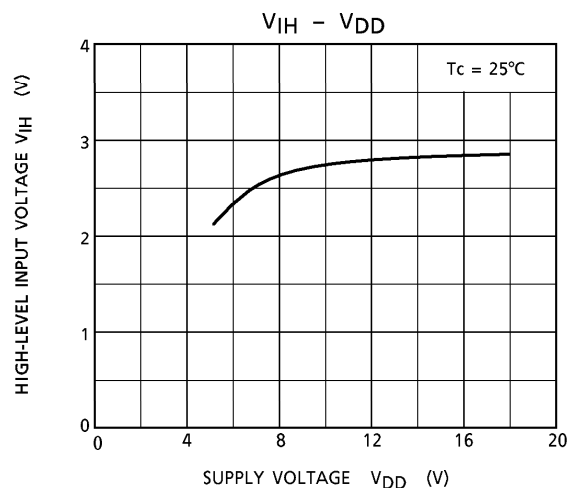
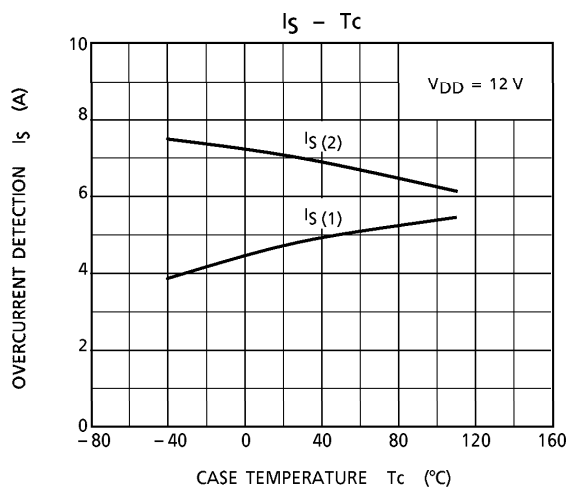
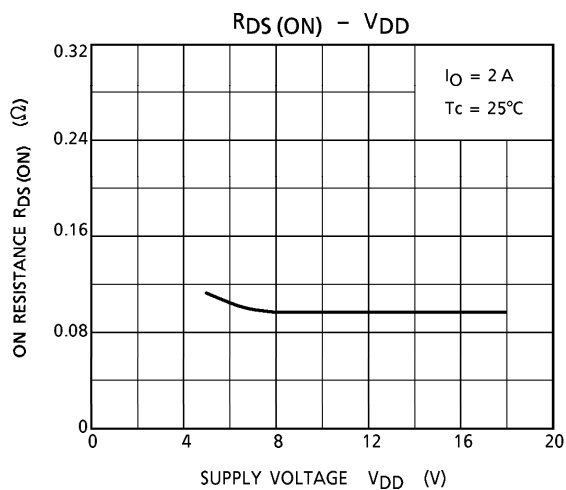
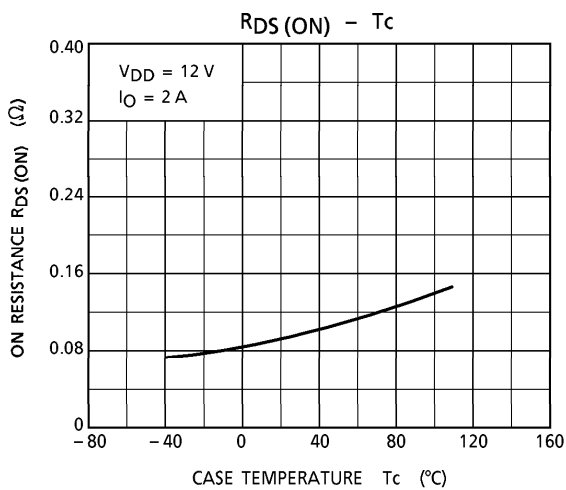
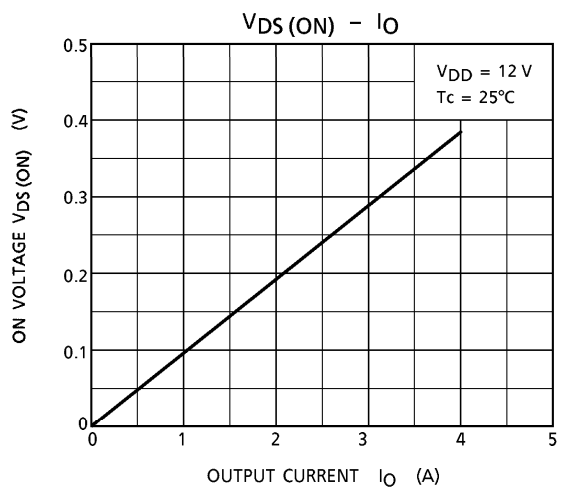
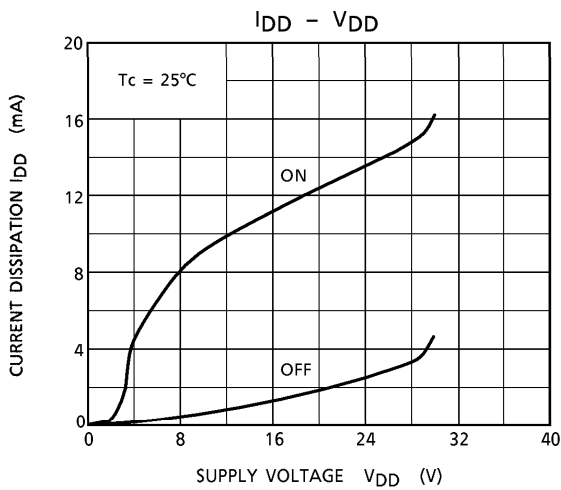
Over-voltage detection

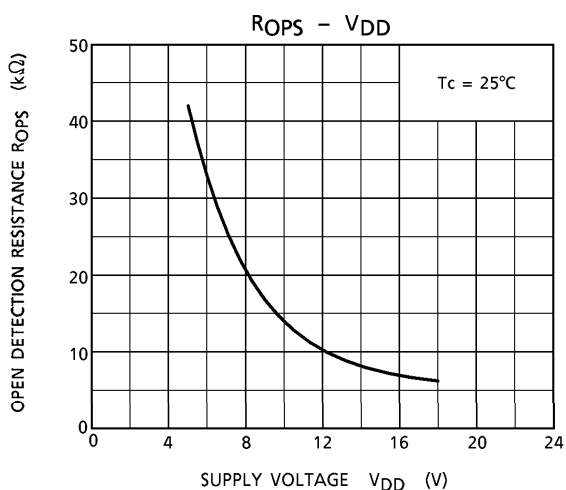
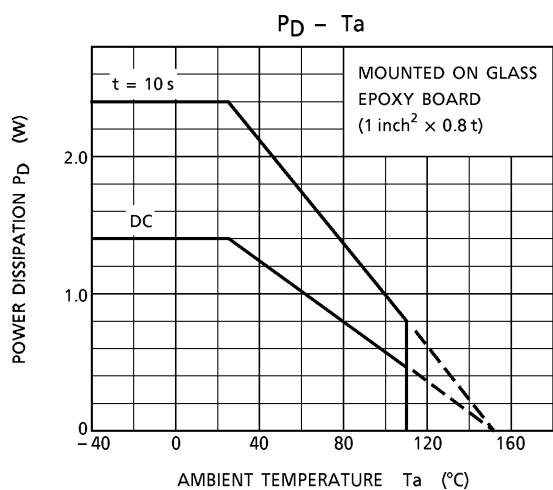
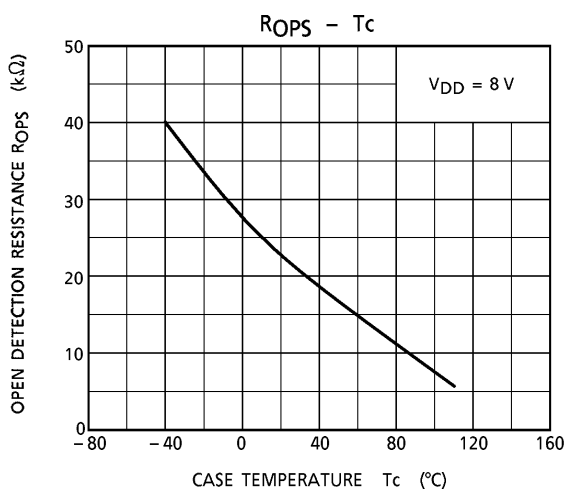
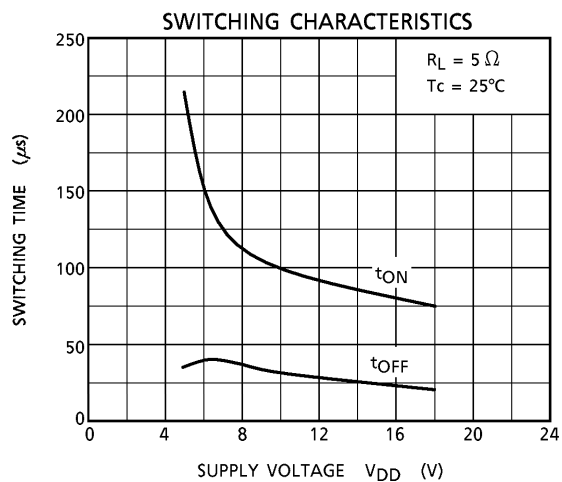
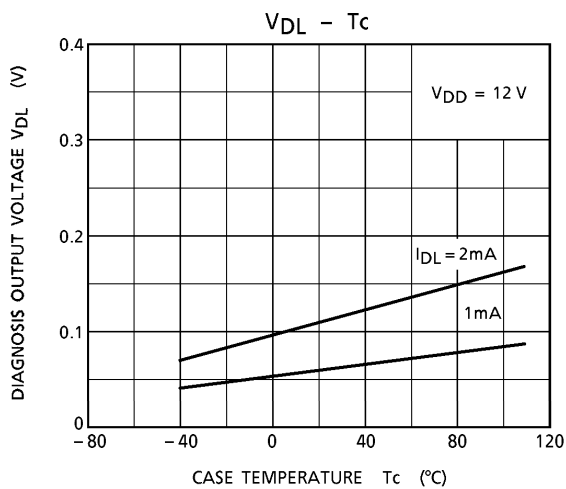


TEST CIRCUIT 3

Switching time

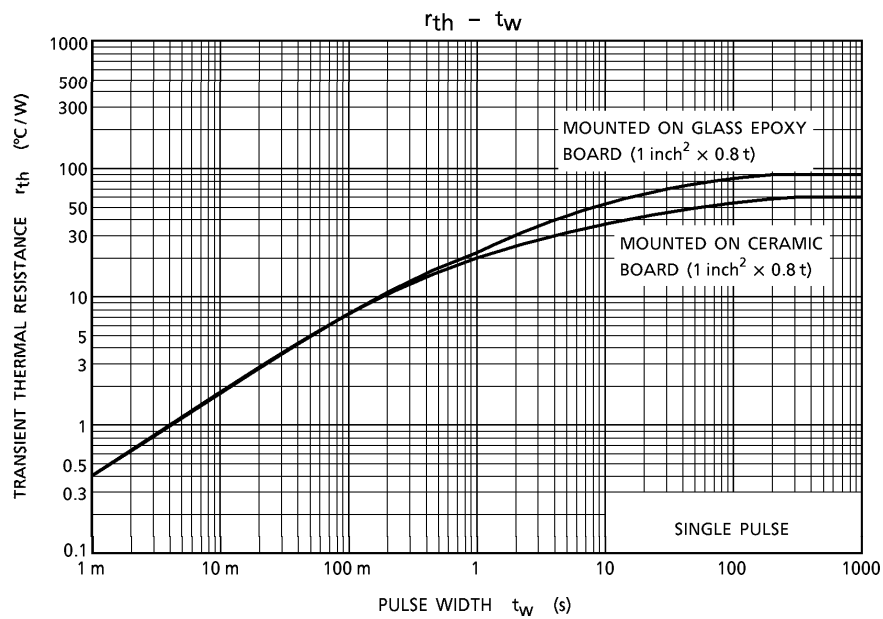






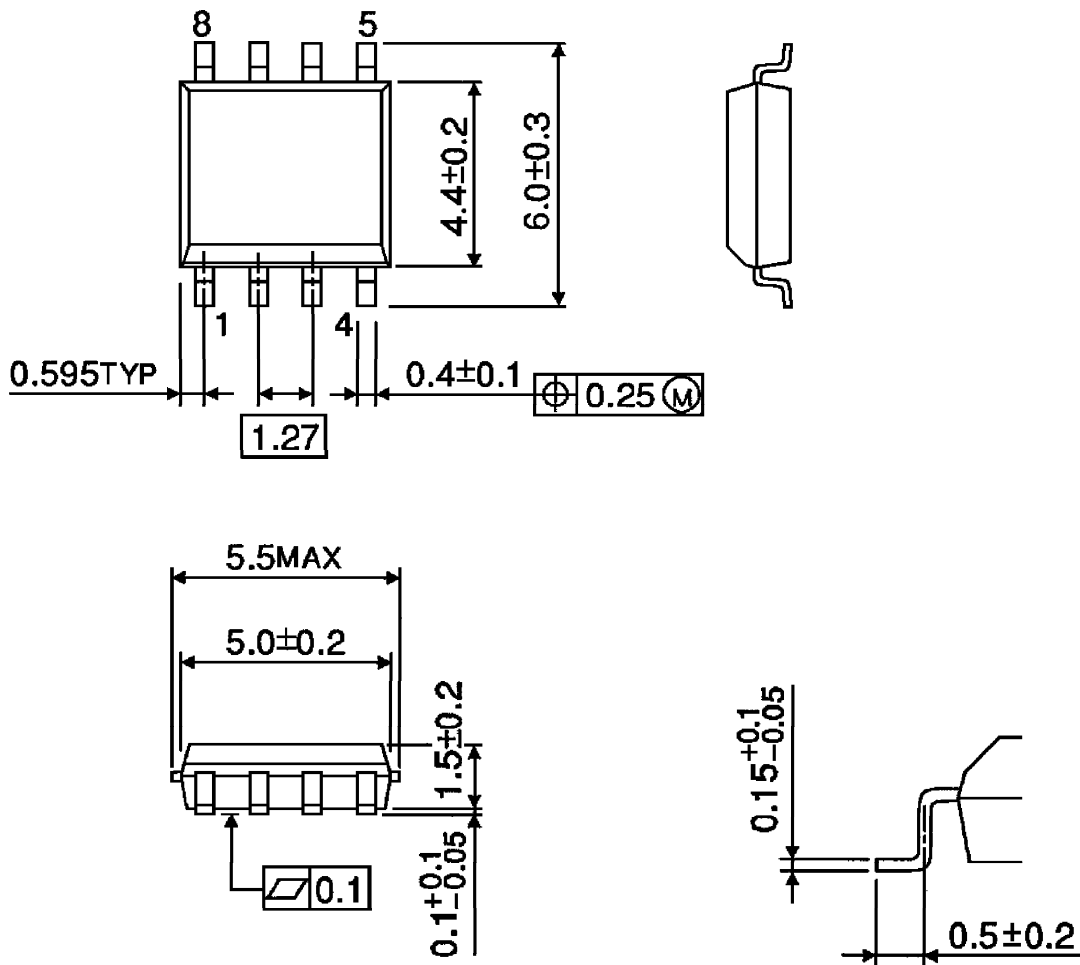
PRECAUTION :

1. Since protection for, for example, reverse connection of the battery is not provided, provide protection using external circuits.



PACKAGE DIMENSIONS
SOP8-P1.27A

Unit : mm



Weight : 0.08 g (Typ.)