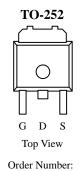


N-Channel 30-V (D-S), 175°C MOSFET

Product Summary

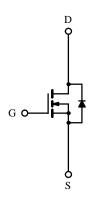
V _{DS} (V)	$\mathbf{r_{DS(on)}}(\Omega)$	I _D (A)
30	$0.010 @ V_{GS} = 10 V$	±15
	$0.019 @ V_{GS} = 4.5 V$	±12





SUD50N03-10

Drain Connected to Tab



N-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^{\circ}C$ Unless Otherwise Noted)

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V_{DS}	30	17	
Gate-Source Voltage		$V_{ m GS}$	±20	V	
Continuous Drain Current ^a	$T_A = 25^{\circ}C$	I _D	±15		
Continuous Drain Current	$T_A = 100^{\circ}C$		±10		
Pulsed Drain Current		I_{DM}	± 100	A	
Continuous Source Current (Diode Conduction) ^a		I_S	15		
Maximum Power Dissipation	$T_C = 25^{\circ}C$	D_	83	W	
Maximum 1 ower Dissipation	$T_A = 25^{\circ}C$	P _D	4 ^a		
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	°C	

Thermal Resistance Ratings

Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}		30	00/84	
Maximum Junction-to-Case	R _{thJC}		1.8	°C/W	

Notes

Updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #70265.

a. Surface Mounted on FR4 Board, $t \le 10$ sec.

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Specifications ($T_J = 25^{\circ}C$ Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typa	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0	2.0		V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
7 C. W. D. C.	,	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			50	μA	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			A	
		$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$			0.010	Ω	
Drain-Source On-State Resistance ^b	r _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}, T_J = 125^{\circ}\text{C}$			0.018		
		$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$			0.019		
Forward Transconductance ^b	gfs	$V_{DS} = 15 \text{ V}, I_D = 15 \text{ A}$	20			S	
Dynamic ^a	•		•	•	•		
Input Capacitance	C _{iss}			3200	6000	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, F = 1 \text{ MHz}$		800			
Reverse Transfer Capacitance	C _{rss}			150			
Total Gate Charge ^c	Q_{g}			55	100	пС	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 15 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_D = 50 \text{ A}$		10			
Gate-Drain Charge ^c	Qgd			9			
Turn-On Delay Time ^c	t _{d(on)}			16	30	ns	
Rise Time ^c	t _r	$V_{DD} = 15 \text{ V}, R_L = 0.3 \Omega$		8	20		
Turn-Off Delay Time ^c	$t_{d(off)}$	$I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 2.5 \Omega$		33	60		
Fall Time ^c	t_{f}			20	40		
Source-Drain Diode Ratings and C	haracteristic ($T_C = 25^{\circ}C$	-	-	-		
Pulsed Current	I_{SM}				100	A	
Diode Forward Voltage ^b	V_{SD}	$I_F = 100 \text{ A}, V_{GS} = 0 \text{ V}$		1.2	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 50 \text{ A}, di/dt = 100 A/\mu s$		55	100	ns	

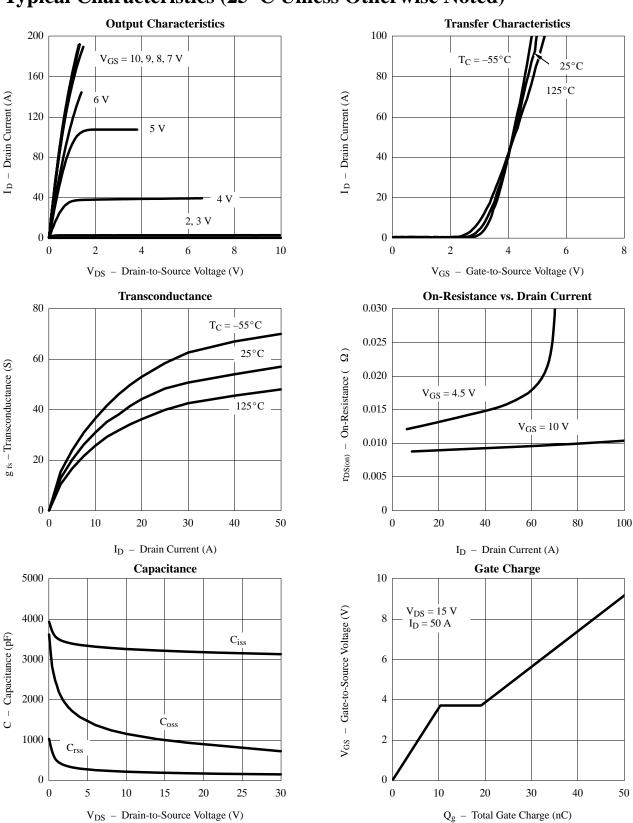
Notes

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width $\leq 300 \,\mu\text{s}$, duty cycle $\leq 2\%$.
- c. Independent of operating temperature.





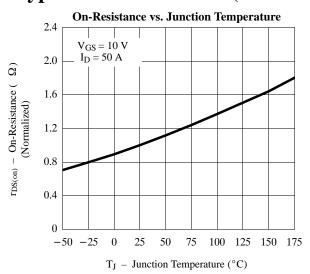
Typical Characteristics (25°C Unless Otherwise Noted)

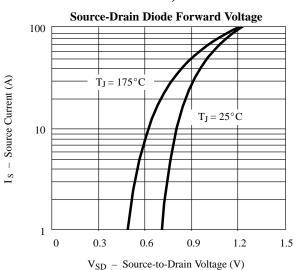


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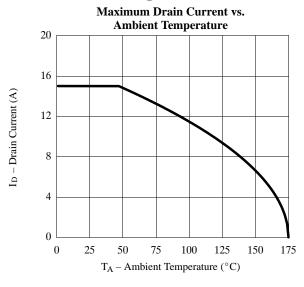


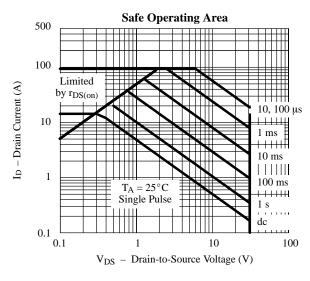
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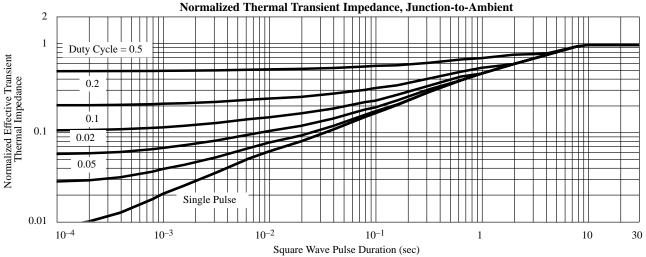




Thermal Ratings







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