



Micro Commercial Components 20736 Marilla Street Chatsworth

CA 91311

Phone: (818) 701-4933 Fax: (818) 701-4939 **SI2303** 

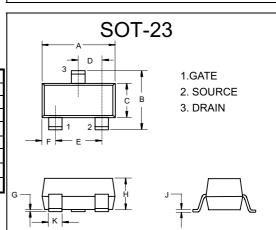
## **Features**

- Halogen free available upon request by adding suffix "-HF"
- -30V,-2.6A,  $R_{DS(ON)}$ =130m  $\Omega$  @V<sub>GS</sub>= -10V -30V,-2.0A,  $R_{DS(ON)}$ =180m  $\Omega$  @V<sub>GS</sub>= -4.5V
- High dense cell design for extremely low R<sub>DS(ON)</sub>
- Rugged and reliable
- · Lead free product is acquired
- SOT-23 PackageMarking Code: S3
- Epoxy meets UL 94 V-0 flammability rating

Moisture Sensitivity Level 1
 Maximum Ratings @ 25°C Unless Otherwise Specified

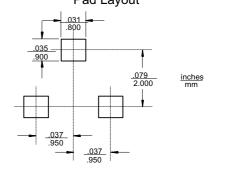
Symbol	Parameter	Rating	Unit
$V_{DS}$	Drain-source Voltage	-30	V
$I_D$	Drain Current-Continuous	urrent-Continuous -3	
I <sub>DM</sub>	Drain Current-Pulsed <sup>a</sup>	-10	Α
$V_{GS}$	Gate-source Voltage	±20	V
P <sub>D</sub>	Total Power Dissipation	0.25	W
R <sub>0 JA</sub>	Thermal Resistance Junction to Ambient <sup>b</sup>	500	°C/W
$T_J$	Operating Junction Temperature	-55 to +150	$^{\circ}\!\mathbb{C}$
T <sub>STG</sub>	Storage Temperature	-55 to +150	$^{\circ}$ C

# P-Channel Enhancement Mode Field Effect Transistor

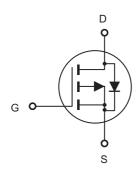


DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.110	.120	2.80	3.04	
В	.083	.104	2.10	2.64	
С	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
Е	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
Η	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

# .0005 .0044 .89 1.12 .003 .007 .085 .180 .015 .020 .37 .51 .51 .020 .37 .51 .031 .800 .031 .800 .031 .800 .031 .800 .031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031 .800 .0031



## **Internal Block Diagram**





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# **SI2303**

# **Electrical Characteristics** $T_A = 25^{\circ}C$ unless otherwise noted

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BVDSS	Vgs = 0V, ID = -10μA	-30			V
Zero Gate Voltage Drain Current	IDSS	VDS = -30V, VGS = 0V			-1	μΑ
Gate Body Leakage Current, Forward	IGSSF	Vgs = 20V, Vps = 0V			100	nA
Gate Body Leakage Current, Reverse	Igssr	Vgs = -20V, Vps = 0V			-100	nA
On Characteristics <sup>c</sup>						
Gate Threshold Voltage	VGS(th)	Vgs = Vps, Ip =-250μA	-1		-3	V
Static Drain-Source On-Resistance	-	Vgs = -10V, ID = -2.6A			130	mΩ
	RDS(on)	VGS = -4.5V, ID = -2.0A			180	mΩ
Forward Transconductance	gFS	VDS = -10V, ID = -1.7A		2.4		S
Dynamic Characteristics <sup>d</sup>						
Input Capacitance	Ciss	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		226		pF
Output Capacitance	Coss	VDS = -15V, VGS = 0V, f = 1.0 MHz		87		pF
Reverse Transfer Capacitance	Crss	- 1 - 1.0 IVIH2		19		pF
Switching Characteristics d						•
Turn-On Delay Time	td(on)	\/ 45\/ l 40		9	20	ns
Turn-On Rise Time	tr	VDD = -15V, ID = -1A,		9	20	ns
Turn-Off Delay Time	td(off)	VGEN = -10V, RG=6Ω, RL=15Ω		18	35	ns
Turn-Off Fall Time	tf	- KL-1977		6	20	ns
Total Gate Charge	Qg	VDS = -15V, ID = -1.7A,		5.8	10	nC
Gate-Source Charge	Qgs			0.8		nC
Gate-Drain Charge	Qgd	VGS 1UV		1.5		nC
Drain-Source Diode Characteristics and Maximun Ratings						
Drain-Source Diode Forward Voltage <sup>c</sup>	VsD	Vgs = 0V, Is = -1.25A			-1.2	V

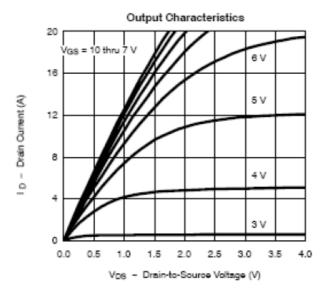
Notes:
a.Repetitive Rating: Pulse width limited by maximum junction temperature. b. Surface Mounted on FR4 Board, t < 10 sec. c.Pulse Test: Pulse Width < 300µs, Duty Cycle < 2%. d.Guaranteed by design, not subject to production testing.

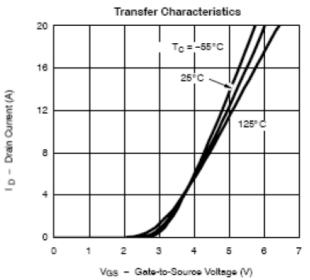
# **SI2303**

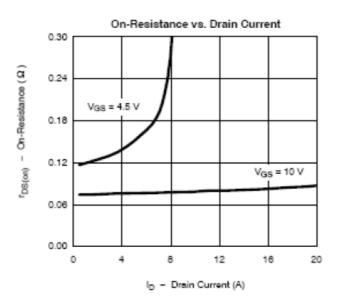
# $\cdot M \cdot C \cdot C \cdot$

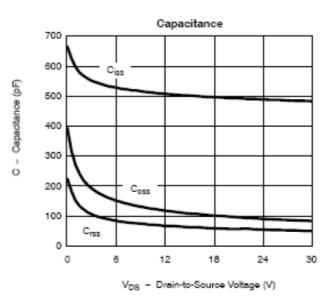
# **Typical characteristics**

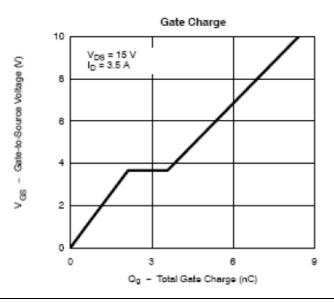
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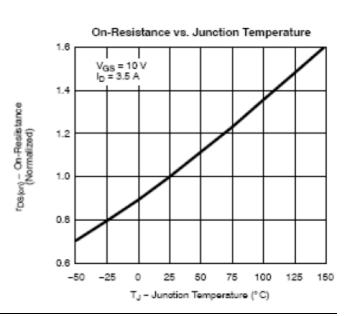








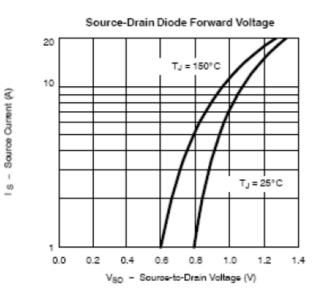


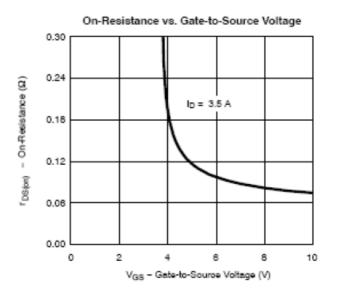


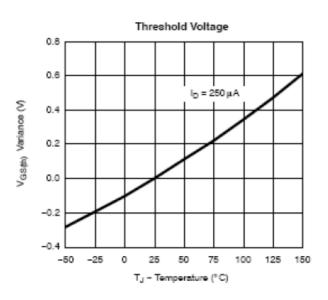
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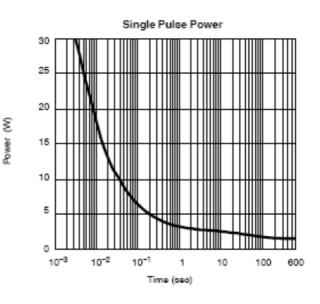
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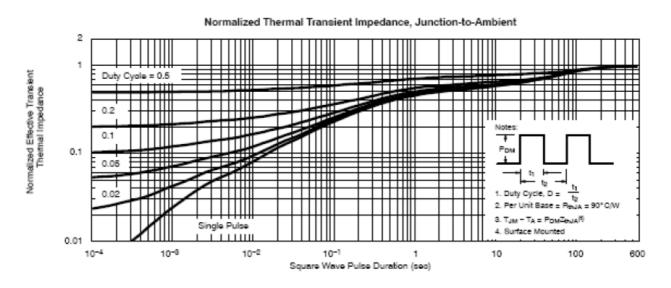
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### Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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