SDAS005A - MARCH 1984 - REVISED MAY 1986

- Package Options Include Plastic Small Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

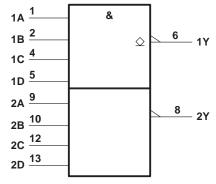
These devices contain two independent 4-input NAND gates. They perform the Boolean functions $Y = \overline{A} \bullet \overline{B} \bullet \overline{C} \bullet \overline{D}$ or $Y = \overline{A} + \overline{B} + \overline{C} + \overline{D}$ in positive logic. The open-collector outputs require pullup resistors to perform correctly. They may be connected to other open-collector outputs to implement active-low wired-OR or active-high wired-AND functions. Open-collector devices are often used to generate higher V_{OH} levels.

The SN54ALS22B is characterized for operation over the full military temperature range of 55°C to 125°C. The SN74ALS22B is characterized for operation from 0°C to 70°C.

FUNCTION TABLE (each gate)

	INF	OUTPUT		
Α	В	С	D	Y
Н	Н	Н	Н	L
L	Χ	Χ	Χ	Н
Х	L	Χ	Χ	Н
Х	X	L	Χ	Н
Χ	Χ	Χ	L	Н

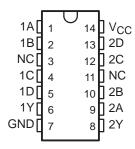
logic symbol†



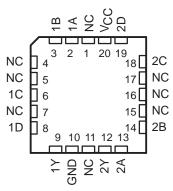
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

SN54ALS22B . . . J PACKAGE SN74ALS22B . . . D OR N PACKAGE (TOP VIEW)

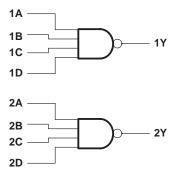


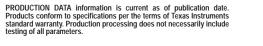
SN54ALS22B . . . FK PACKAGE (TOP VIEW)



NC-No internal connection

logic diagram (positive logic)







SN54ALS22B, SN74ALS22B DUAL 4-INPUT POSITIVE-NAND GATES WITH OPEN-COLLECTOR OUTPUTSWITH OPEN-COLLECTOR OUTPUTS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC}		 7 V
Input voltage		 7 V
Off-state output voltage		 7 V
Operating free-air temperature range:	SN54ALS22B	 -55°C to 125°C
	SN74ALS22B	 0°C to 70°C
Storage temperature range		-65°C to 150°C

recommended operating conditions

		SN54ALS22B		SN74ALS22B			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
IOH	High-level output current			5.5			5.5	mA
lOL	Low-level output current			4			8	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN	SN54ALS22B			SN74ALS22B		
PARAMETER			MIN	TYP†	MAX	MIN	TYP [†]	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.5			-1.5	V
Val	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 4 \text{ mA}$		0.25	0.4		0.25	0.4	V
VOL	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 8 \text{ mA}$					0.35	0.5	
^I ОН	$V_{CC} = 4.5 \text{ V},$	V _{OH} = 5.5 V			0.1			0.1	mA
ΙĮ	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1			0.1	mA
lіН	$V_{CC} = 5.5 V,$	V _I = 2.7 V			20			20	μΑ
I _{IL}	$V_{CC} = 5.5 \text{ V},$	V _I = 0.4 V			-0.1			-0.1	mA
ICCH	$V_{CC} = 5.5 \text{ V},$	V _I = 0 V		0.22	0.4		0.22	0.4	mA
ICCL	$V_{CC} = 5.5 \text{ V},$	V _I = 4.5 V		0.8	1.5		0.8	1.5	mA

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 2 \text{ k}\Omega,$ $T_A = 25^{\circ}\text{C}$ $^{\prime}\text{ALS22B}$ $^{\prime}\text{TYP}$	C _L	= 50 p = 2 k Ω = MIN t	2,		UNIT
t _{PLH}	Any	Y	35	23	65	23	45	ns
^t PHL	Any	Y	8	4	32	4	18	ns

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.



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