

BGF101

USB ESD Protection

AIM
Discrete Semiconductors



Never stop thinking.

Edition 2005-06-01

**Published by Infineon Technologies AG,
St.-Martin-Strasse 53,
D-81541 München**

**© Infineon Technologies AG 2004
All Rights Reserved.**

Attention please!

The information herein is given to describe certain components and shall not be considered as warranted characteristics.

Terms of delivery and rights to technical change reserved.

We hereby disclaim any and all warranties, including but not limited to warranties of non-infringement, regarding circuits, descriptions and charts stated herein.

Infineon Technologies is an approved CECC manufacturer.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office in Germany or our Infineon Technologies Representatives worldwide (see address list).

Warnings

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in life-support devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.

BGF101**Data sheet****Revision History: 2005-06-01**

Previous Version: 2004-03-08

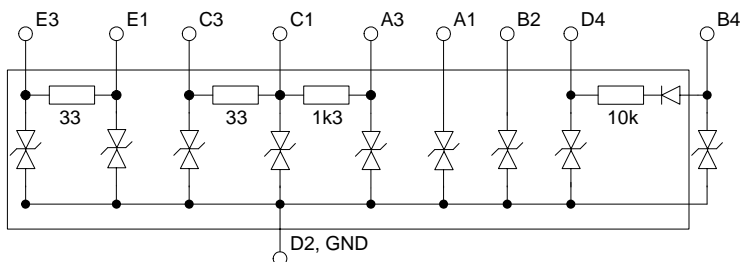
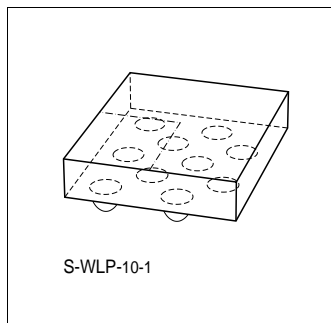
Page	Subjects (major changes since last revision)
------	--

all	Preliminary status removed
-----	----------------------------

For questions on technology, delivery and prices please contact the Infineon Technologies Offices in Germany or the Infineon Technologies Companies and Representatives worldwide: see our webpage at <http://www.infineon.com>

Features

- ESD protection circuit for full speed USB interfaces
- Integrated ESD protection according to IEC61000-4-2 up to 15 kV contact discharge
- Green wafer level package with SnAgCu balls
- 500 μ m diagonal ball pitch
- 300 μ m ball diameter



Description

The BGF101 is an ESD protection circuit for a full speed USB interface (12 Mbit/sec). External pins are protected against ESD up to 15 kV contact discharge according to IEC61000-4-2. The wafer level package is a green package with a size of only 1.65 mm x 2.0 mm and a total height of 0.65 mm.

Type	Package	Marking	Chip
BGF101	WLP-10-1	GF101	N0706

Maximum Ratings

Parameter	Symbol	Value	Unit
Voltage at all pins to GND	V_P	7	V
Operating temperature range	T_{OP}	-40 ... +85	°C
Storage temperature range	T_{STG}	-65 ... +150	°C
Summed up input power for all pins, $T_S < 70^\circ\text{C}$	P_{IN}	60	mW
Electrostatic discharge according to IEC61000-4-2 ¹⁾ at pins A1, A3, B2, C1, C3, D4, E1, E3 at pin B2	V_{ESD}	15 8	kV

¹⁾ Contact discharge

Electrical Characteristics at $T_A = 25^\circ\text{C}$

Parameter	Symbol	min.	typ.	max.	Unit
Resistors					
R1, R2	$R_{1,2}$	31.3	33	34.7	Ω
R3	R_3	1235	1300	1365	Ω
R4 ¹⁾	R_4	9.5	10	10.5	k Ω
Temperature coefficient ²⁾	$ TCR $		100	130	ppm/K
ESD protection diodes					
Reverse current	I_R				
$V_R = 3\text{ V to GND}$			1	100	nA
$V_R = 7\text{ V to GND}$			1	100	μA
Pin capacitance					
Cap. of pins A3, C1, C3, E1, E3 to GND $V_R = 0\text{ V}^{3)}$	C_T		48	53	pF
Diode D1					
Forward voltage	V_F				V
$I_F = 1\text{ }\mu\text{A}$			0.27	0.35	

¹⁾ in production not tested separately, but correlated to R3

²⁾ averaged over full operating temperature range from -40 to +85°C, see figure 2

³⁾ not subject to production test, verified by design/ characterization

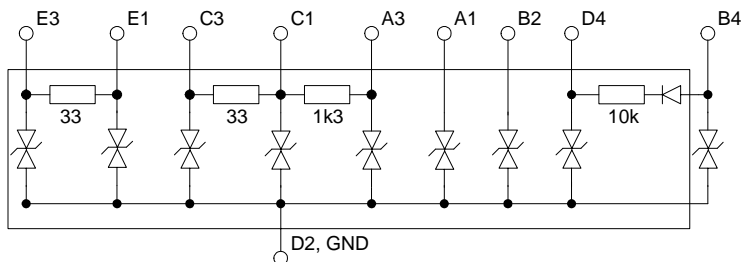


Fig.1: Schematic

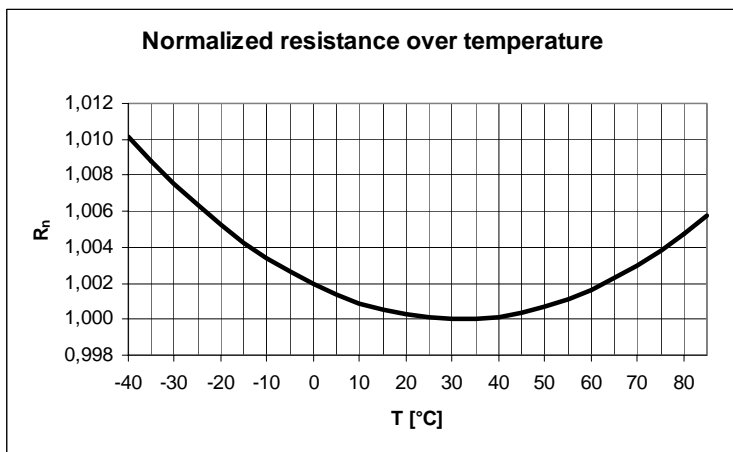
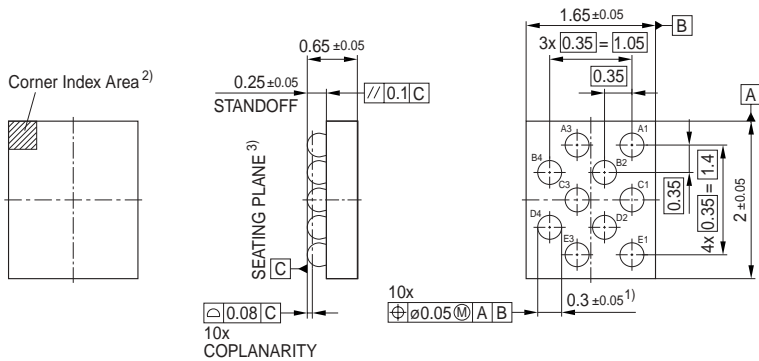


Fig.2: Normalized resistance over temperature

Package Outline



- 1) Dimension is measured at the maximum ball diameter, parallel to primary datum C
- 2) A1 corner identified by marking
- 3) Primary datum C and seating plane are defined by the domed crowns of the balls

GWL09661

WLP-10-1