

Features

- 2 Differential Channel, 4:1 Mux/DeMux
- PCI Express® 2.0 performance, 5.0 Gbps
- Low Bit-to-Bit Skew, 7ps Max.
- Low Crosstalk: -23dB@3GHz
- Low Off Isolation: -23dB@3GHz
- V_{DD} Operating Range: +1.8V ±10%
- ESD Tolerance 2kV HBM on data I/O
- Packaging (Pb-free & Green):
 - 42 contact TQFN

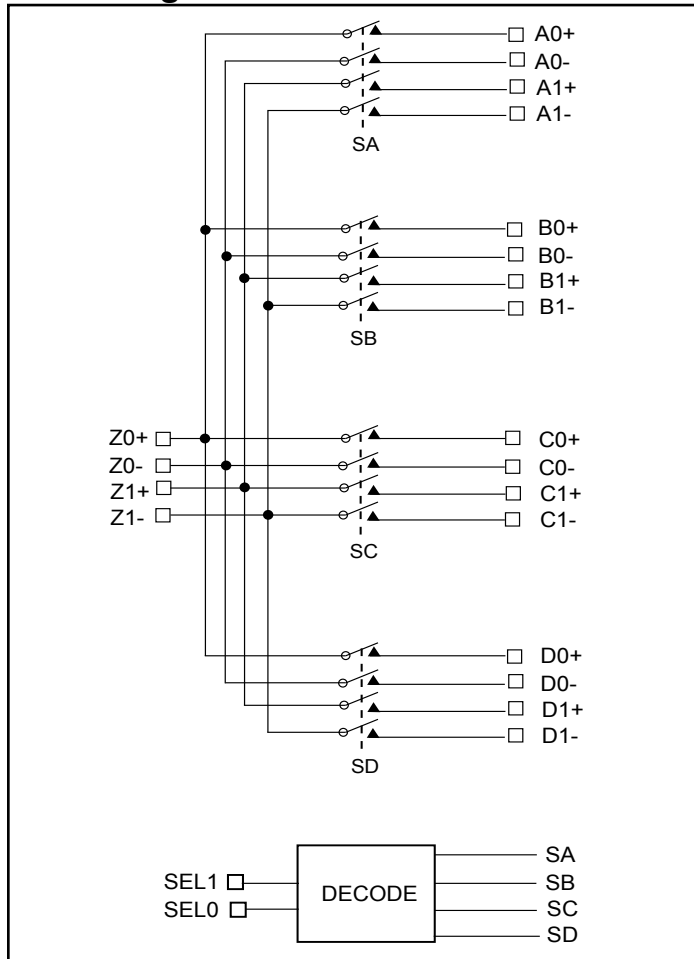
Description

Pericom Semiconductor's PI2PCIE2214 is a 8 to 2 differential channel multiplexer/demultiplexer switch. Due to its low bit-to-bit skew, high channel-to-channel noise isolation and high bandwidth, this product is ideal for PCI Express® switching to 5.0 Gbps.

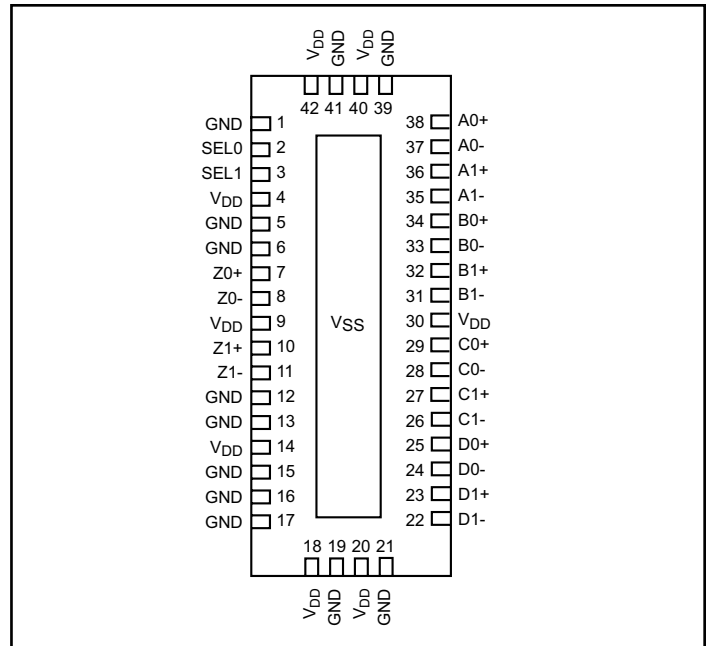
Application

Switch a PCI Express® lane output between four PCI Express lane inputs

Block Diagram



Pin Description



Truth Table

SEL1	SEL0	FUNCTION
0	0	Z to A
0	1	Z to B
1	0	Z to C
1	1	Z to D

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Supply Voltage to Ground Potential	-0.5V to +2.5V
DC Input Voltage	-0.5V to +V _{DD}
DC Output Current	120mA
Power Dissipation	0.5W

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{DD}	Quiescent Power Supply Current	V _{DD} = Max., V _{IN} = GND or V _{DD}			300	μA

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{DD} = 1.8V, T_A = 25°C ambient and maximum loading.

DC Electrical Characteristics for Switching over Operating Range

(T_A = -40°C to +85°C, V_{DD} = 1.8V ± 10%)

Parameter	Description	Test Conditions	Min.	Typ. ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed HIGH level	0.65 x V _{DD}	-	-	V
V _{IL}	Input LOW Voltage	Guaranteed LOW level	-	-	0.35 x V _{DD}	
V _{IK}	Clamp Diode Voltage	V _{DD} = Max., I _{IN} = -18mA	-	-0.7	-1.2	
I _{IH}	Input HIGH Current	V _{DD} = Max., V _{IN} = V _{DD}	-	-	±5	μA
I _{IL}	Input LOW Current	V _{DD} = Max., V _{IN} = GND	-	-	±5	

Switching Characteristics (T_A = -40° to +85°C, V_{DD} = 1.8V ± 10%)

Parameter	Description	Min.	Typ. ⁽²⁾	Max.	Units
t _{PZH} , t _{PZL}	Line Enable Time - SEL to A _N , B _N	0.5	-	8.0	ns
t _{PHZ} , t _{PLZ}	Line Disable Time - SEL to A _N , B _N	0.5	-	10	
t _{b-b}	Bit-to-bit skew within the same differential pair		7		ps
t _{ch-ch}	Channel-to-channel skew		35		ps

Notes:

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

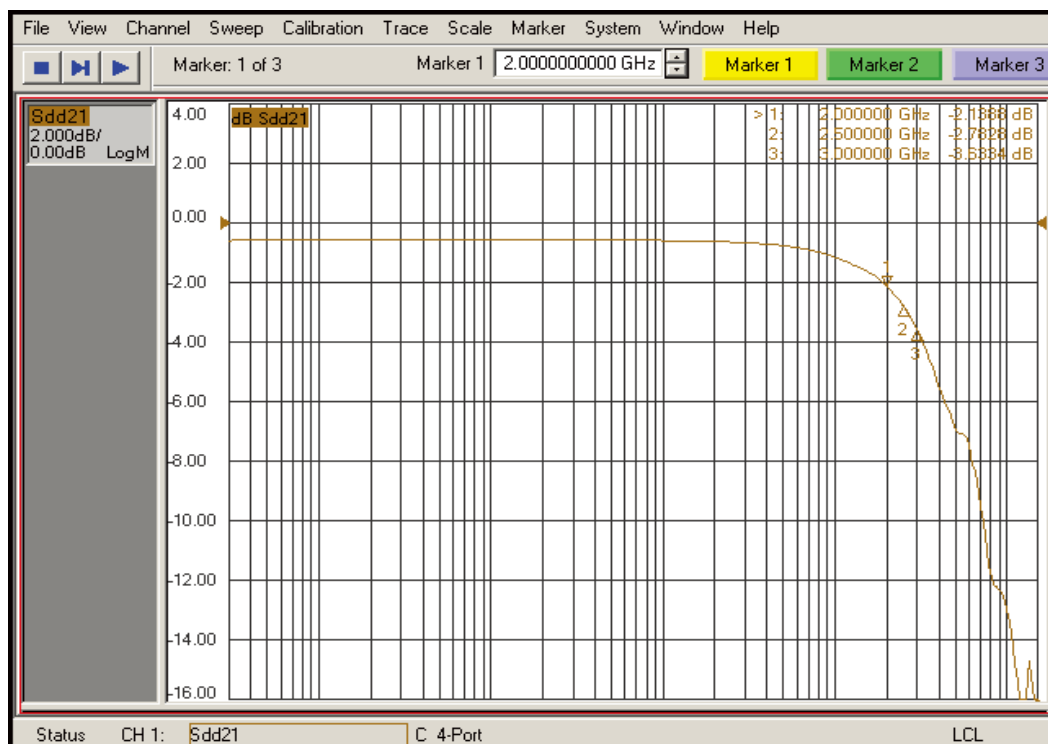
Dynamic Electrical Characteristics Over the Operating Range

(T_A = -40° to +85°C, V_{DD} = 1.8V ± 10%)

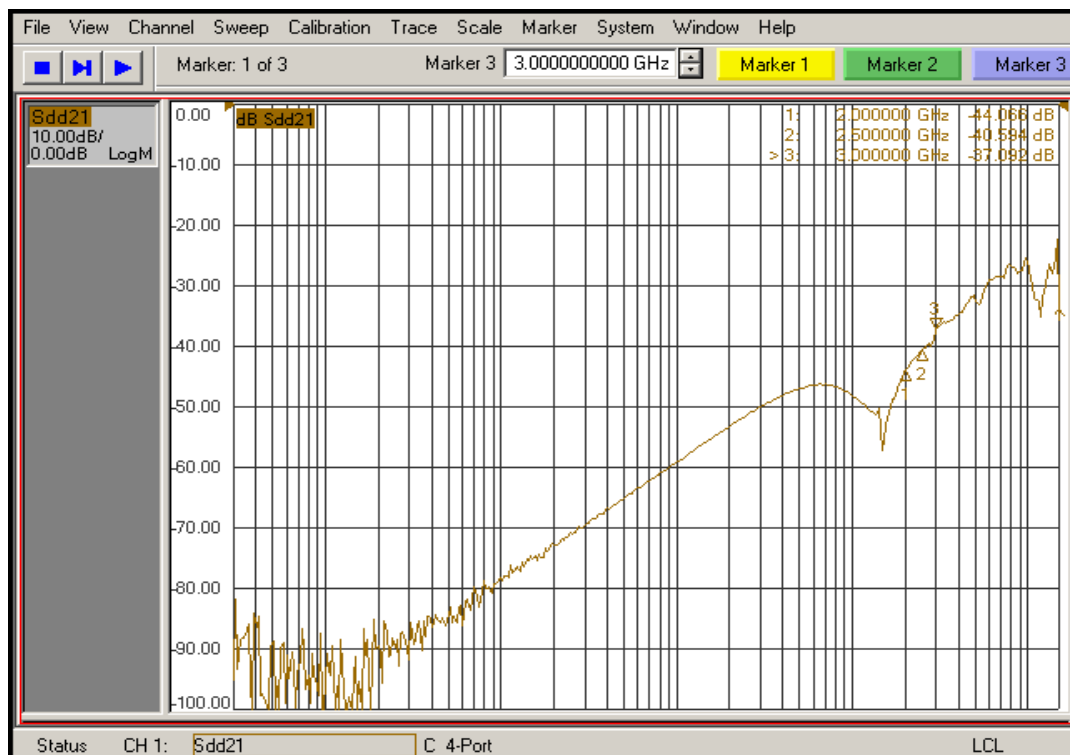
Parameter	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
X _{TALK}	Crosstalk	f = 2.5 GHz		-40		dB
O _{IRR}	OFF Isolation	f = 2.5 GHz		-25		dB
I _{LOSS}	Differential Insertion Loss	f = 2.5 GHz		-3.0		dB
BW	Bandwidth -3dB			2.6		GHz

Notes:

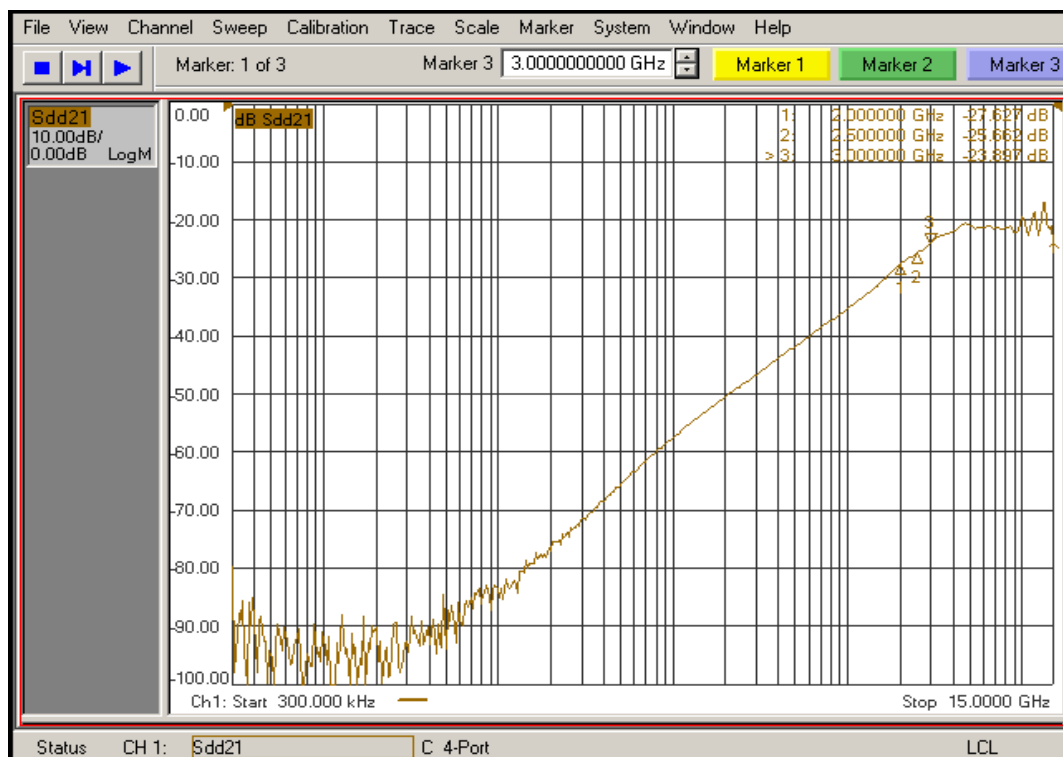
- Guaranteed by design.
- Typical values are at V_{DD} = 1.8V, T_A = 25°C ambient and maximum loading.



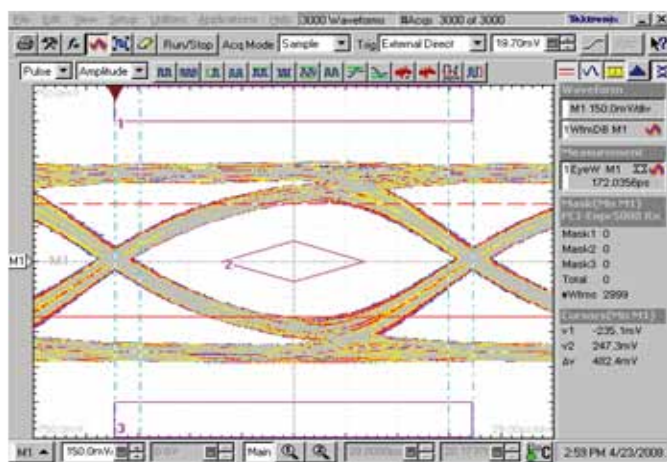
Insertion Loss



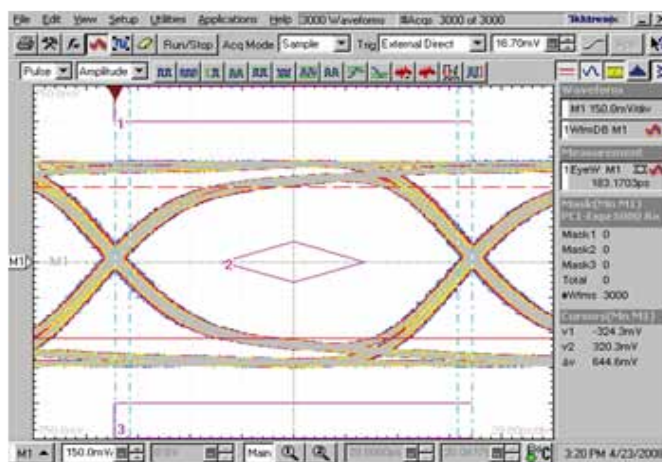
Crosstalk



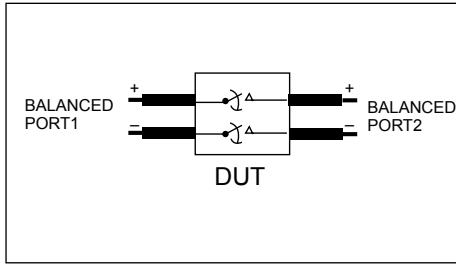
Off Isolation



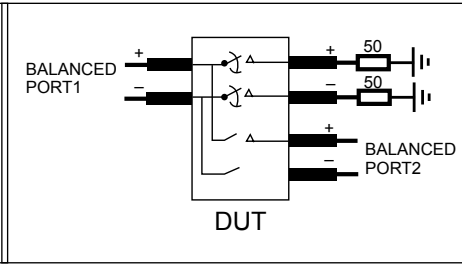
Signal Eye with Switch



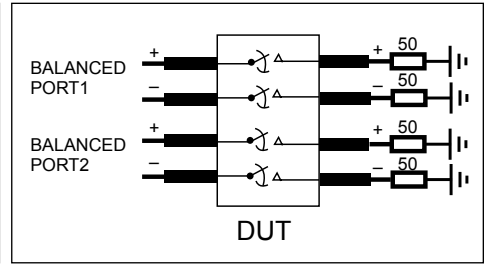
Signal Eye without Switch



Diff. Insertion Loss and Return Test Circuit

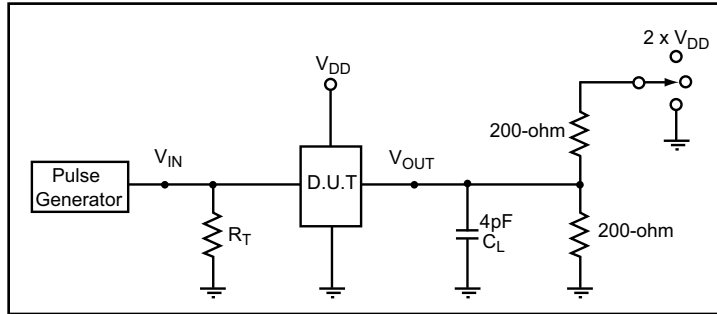


Diff. Off Isolation Test Circuit



Diff. Near End Xtalk Test Circuit

Test Circuit for Electrical Characteristics⁽¹⁻⁵⁾



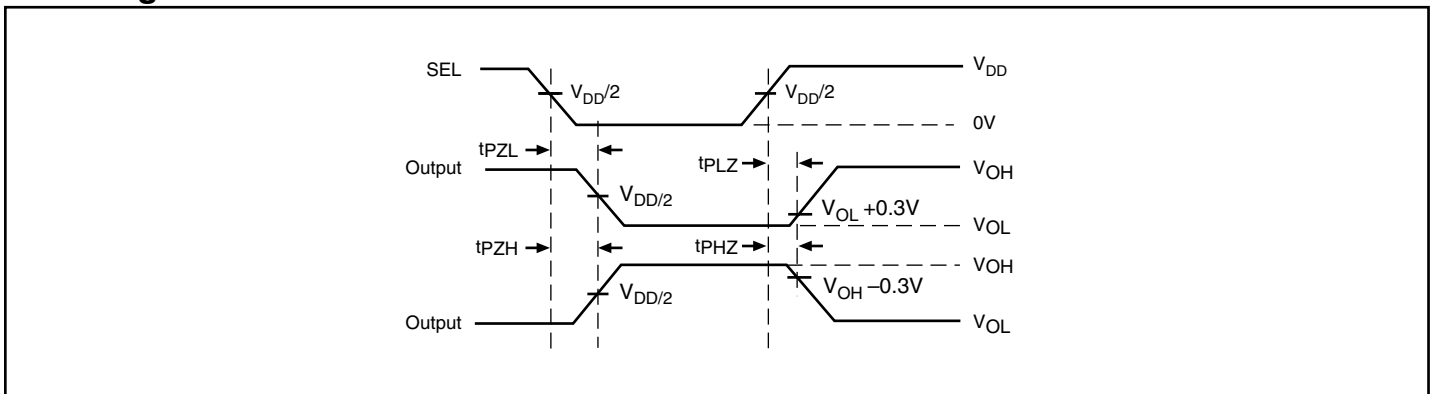
Notes:

1. C_L = Load capacitance: includes jig and probe capacitance.
2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
3. Output 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
output 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
4. All input impulses are supplied by generators having the following characteristics: $PRR \leq \text{MHz}$, $Z_O = 50\Omega$, $t_R \leq 2.5\text{ns}$, $t_F \leq 2.5\text{ns}$.
5. The outputs are measured one at a time with one transition per measurement.

Switch Positions

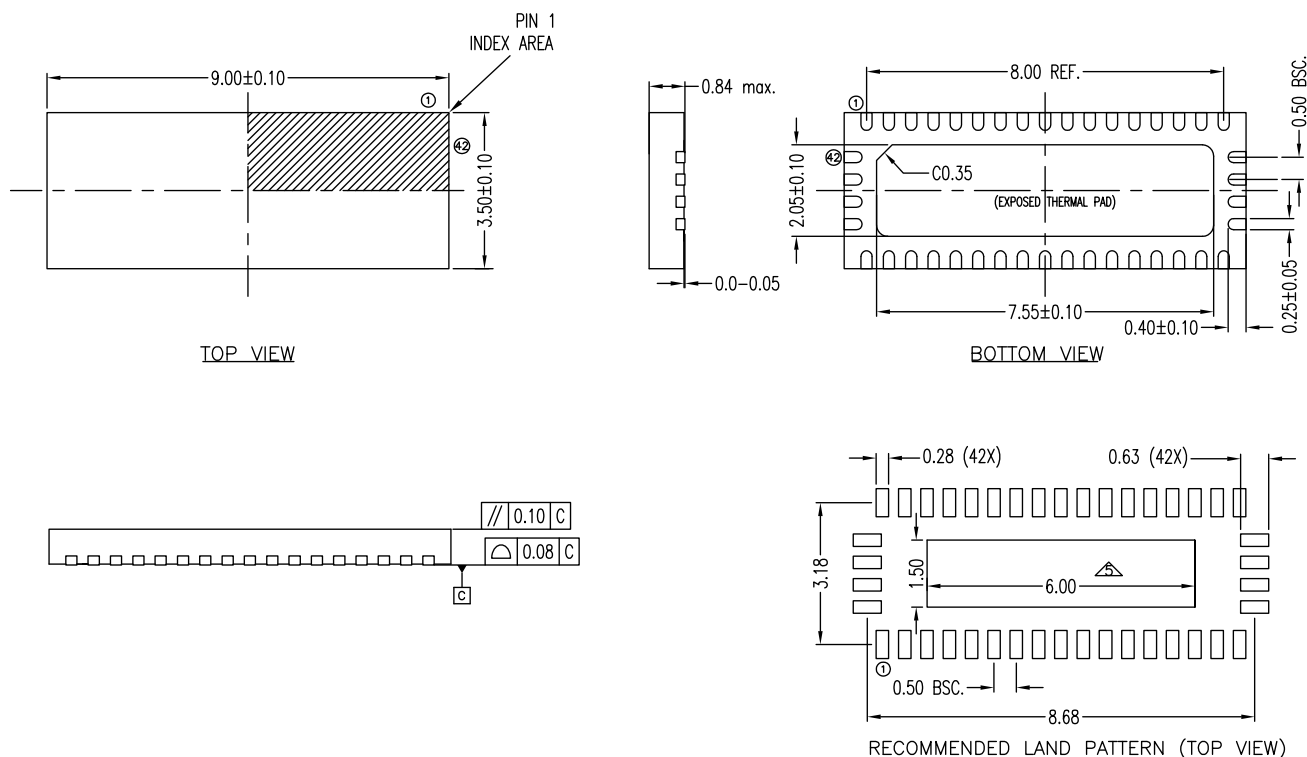
Test	Switch
t_{PLZ} , t_{PZL}	$2 \times V_{DD}$
t_{PHZ} , t_{PZH}	GND
Prop Delay	Open

Switching Waveforms



Voltage Waveforms Enable and Disable Times

Package Mechanical: 42-Contact TQFN (ZH)



Notes:

1. All dimensions are in millimeters. Angles in degrees.
2. Coplanarity applies to the exposed pad as well as the terminals.
3. Refer JEDEC MO-220.
4. Recommended land pattern is for reference only.
5. Thermal pad soldering area



DATE: 11/14/12

DESCRIPTION: 42-contact Thin Fine Pitch Quad Flat No-Lead (TQFN)

PACKAGE CODE: ZH42

DOCUMENT CONTROL #: PD-2035

REVISION: D

Ordering Information

Ordering Code	Package Code	Package Type
PI2PCIE2214ZHEX	ZH	42-Contact, Thin Fine Pitch Quad Flat No-Lead (TQFN)

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- "E" denotes Pb-free and Green
- Adding an "X" at the end of the ordering code denotes tape and reel packaging