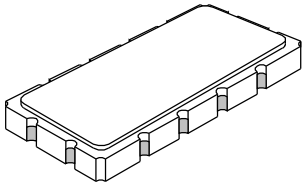
SF1087A 208 MHz SAW Filter

- *Designed for GSM BTS Transmitter IF Applications*
- *Low Insertion Loss*
- *Excellent Size-to-Performance Ratio*
- *Hermetic 13.3 x 6.5 mm Surface-Mount Case*
- *Unbalanced Input and Output*



Characteristic		Sym	Min	Typ	Max	Units	Notes
Nominal Center Frequency		fc		208.000		MHz	1
Passband	Insertion Loss at fc	IL		5	7.0	dB	
	1 dB Passband	BW_1	±150	±260		kHz	1, 2
	Amplitude Ripple over fc ±150 kHz				1.25	dB_{P-P}	
	Group Delay Variation over fc ±150 kHz	GDV		<100	150	ns_{P-P}	
	Absolute Group Delay	GD	0.7	1.0	1.7	µs	
Rejection	fc-0.6 to fc-0.4 and fc+0.4 to fc+0.6 MHz		2			dB	1, 2, 3
	fc-1.2 to fc-0.6 and fc+0.6 to fc+1.2 MHz		8				
	fc-1.8 to fc-1.2 and fc+1.2 to fc+1.8 MHz		20	22			
	fc-3.4 to fc-1.8 and fc+1.8 to fc+3.4 MHz		25	36			
	fc-9.5 to fc-3.4 and fc+3.4 to fc+9.5 MHz		30	47			
	fc-13 to fc-9.5 and fc+9.5 to fc+13 MHz		43	64			
	DC to fc-13 and fc+13 to 450 MHz		55	65			
	Except Spurious Rejection near 1.6, 1.8, and 2.0 fc		50				
Operating Temperature Range		T_A	-10		+85	°C	1

Impedance Matching to 50 Ω unbalanced	External L-C
Case Style	SM13365-12 13.3 x 6.5 mm Nominal Footprint
Lid Symbolization (YY = year, WW = week)	RFM SF1087A YYWW

Absolute Maximum Ratings

Rating	Value	Units
Maximum Incident Power in Passband	+10	dBm
Max. DC voltage between any 2 terminals	30	VDC
Storage Temperature Range	-40 to +85	°C
Max Soldering Profile	265°C for 10 s	

Electrical Connections

Connection	Terminals
Port 1 Hot	2
Port 1 Gnd Return	3
Port 2 Hot	8
Port 2 Gnd Return	9
Case Ground	All others

Notes:
1. Unless noted otherwise, all specifications apply *over the operating temperature range* with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network analyzer.
2. Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, fc.
3. Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.
4. "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
5. The design, manufacturing process, and specifications of this filter are subject to change.
6. Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
7. US and international patents may apply.
8. RFM, stylized RFM logo, and RF Monolithics, Inc. are registered trademarks of RF Monolithics, Inc.
9.
10. Electrostatic Sensitive Device. Observe precautions for handling.

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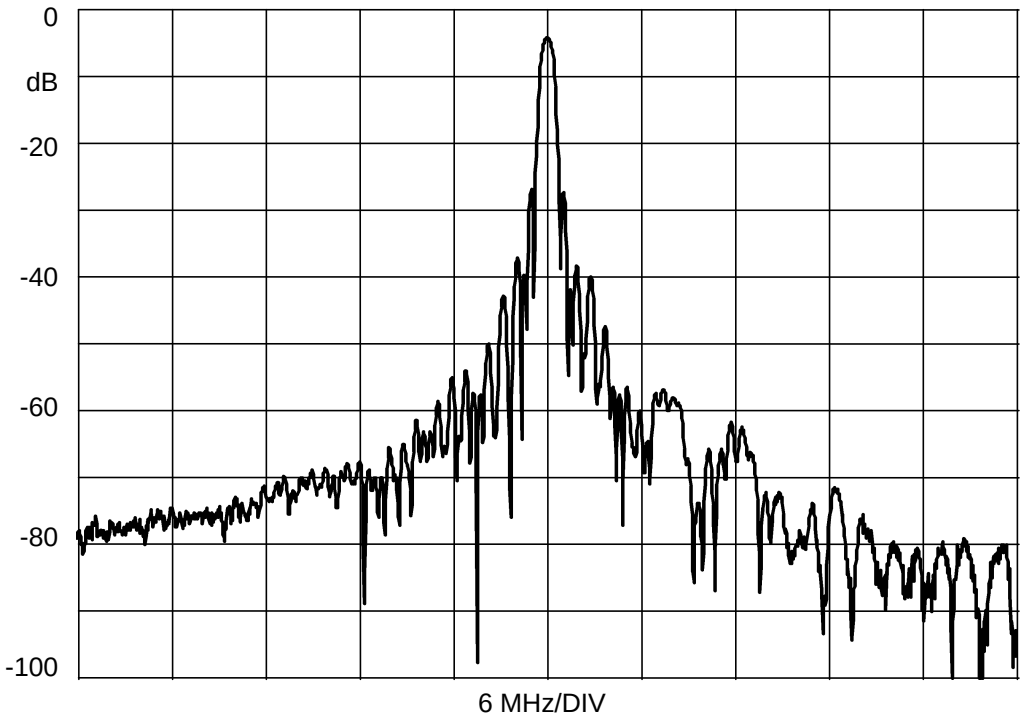
European Sales Office
44 1963 251383
44 1963 251510

SF1087A 208 MHz SAW Filter



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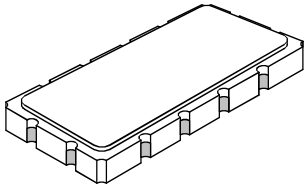
SM13365-12 Case

12-Terminal Ceramic Surface-Mount Case
13.3 x 6.5 mm Nominal Footprint

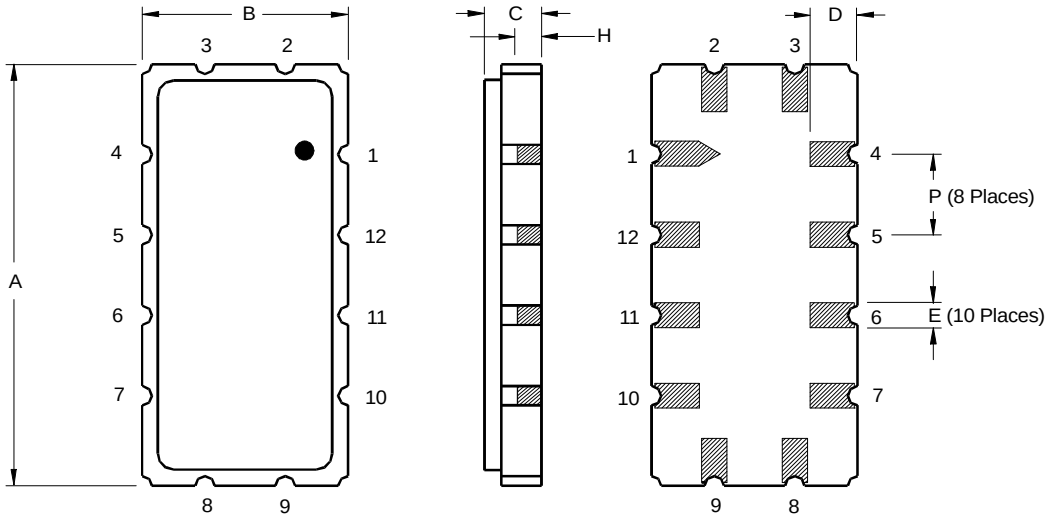


Case Dimensions

Dimension	mm			Inches		
	Min	Nom	Max	Min	Nom	Max
A	13.08	13.31	13.60	0.515	0.524	0.535
B	6.27	6.50	6.80	0.247	0.256	0.268
C		1.91	2.00		0.075	0.079
D		1.50			0.059	
E		0.79			0.031	
H		1.0			0.039	
P		2.54			0.100	

Electrical Connections

Connection		Terminals
Port 1	Input or Return	2
	Return or Input	3
Port 2	Output or Return	8
	Return or Output	9
Ground		All others
Single Ended Operation		**Return is ground**
Differential Operation		**Return is hot**



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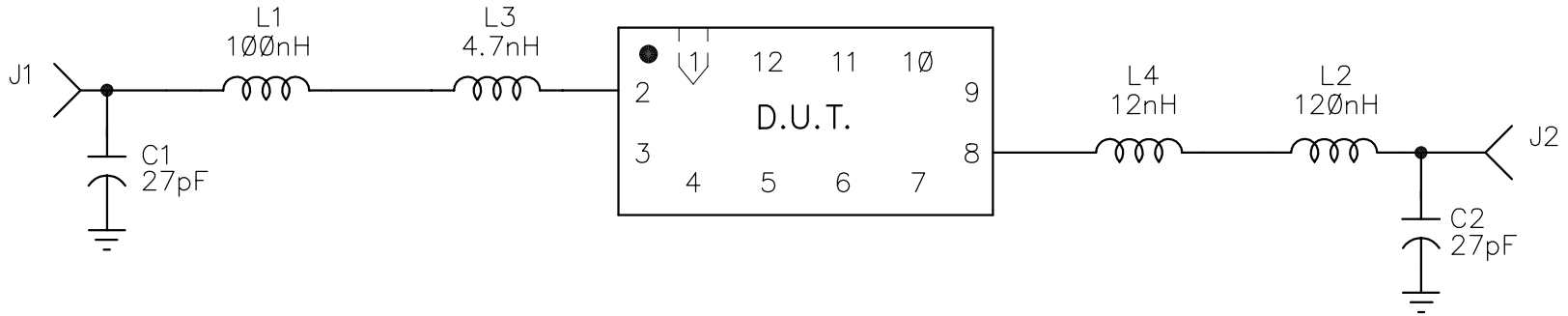
RFM Europe Phone: 44 1963 251383 Fax: 44 1963 251510 http://www.rfm.com

NOTES:

1. L4 MAY BE INTERCHANGED WITH L5 DEPENDING ON TUNED RESPONSE.
2. ORIENTATION OF COMPONENTS MAY VARY FROM ASSEMBLY DIAGRAM IN ORDER TO FINE TUNE DEVICE.

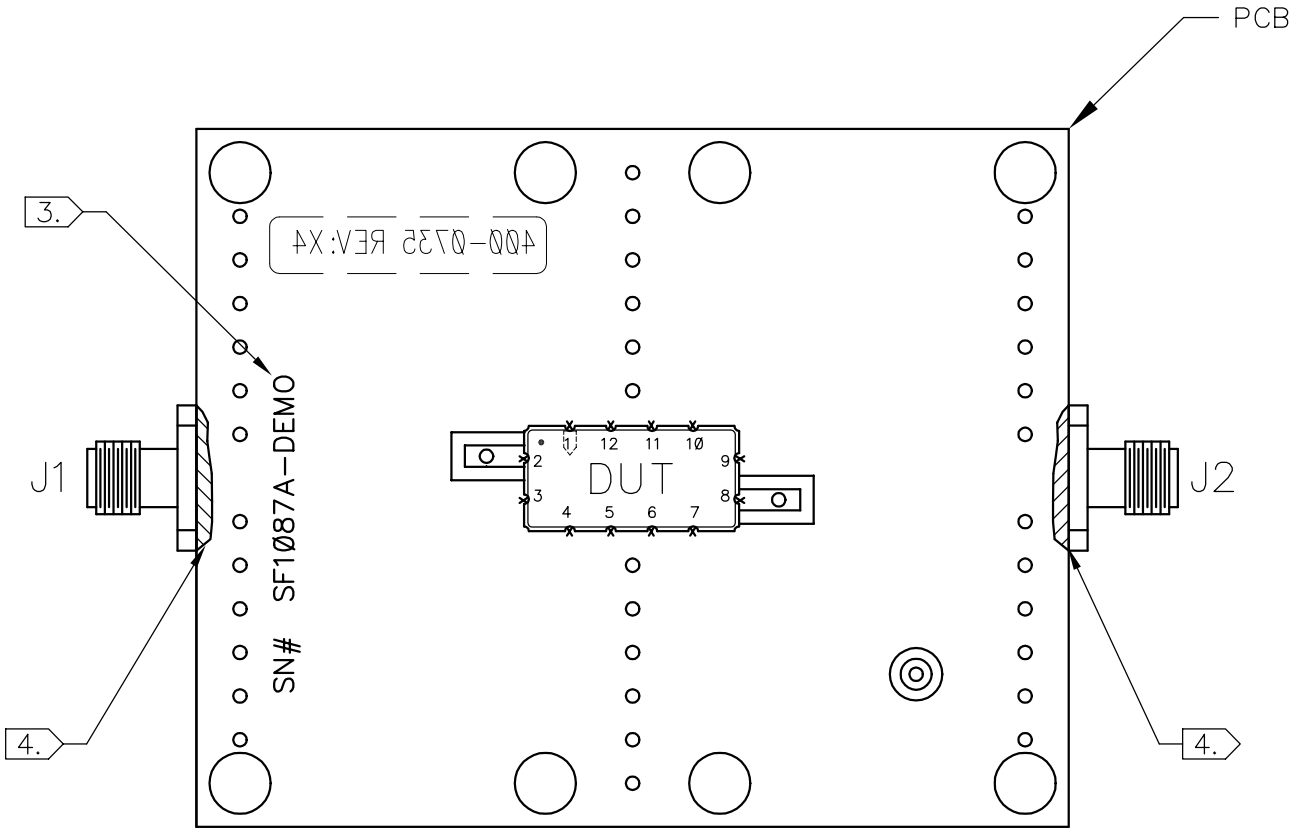
REV	ECN NO.	DESCRIPTION	DATE
A	6714	INITIAL RELEASE	15may98
B	10225	REVISED PIN NUMBERS	04oct01



DRAWN BY/DATE: J.J. LAYTON 04/10/98			TITLE: ASSY DIAGRAM, DEMO BOARD, SF1087A				
RF Monolithics, Inc. DALLAS, TEXAS 75244	CHECKED/APPROVED	SIZE A	CODE IDENT 2U874	DWG. NO. SF1087A-000	REV B	SHEET 1/5	

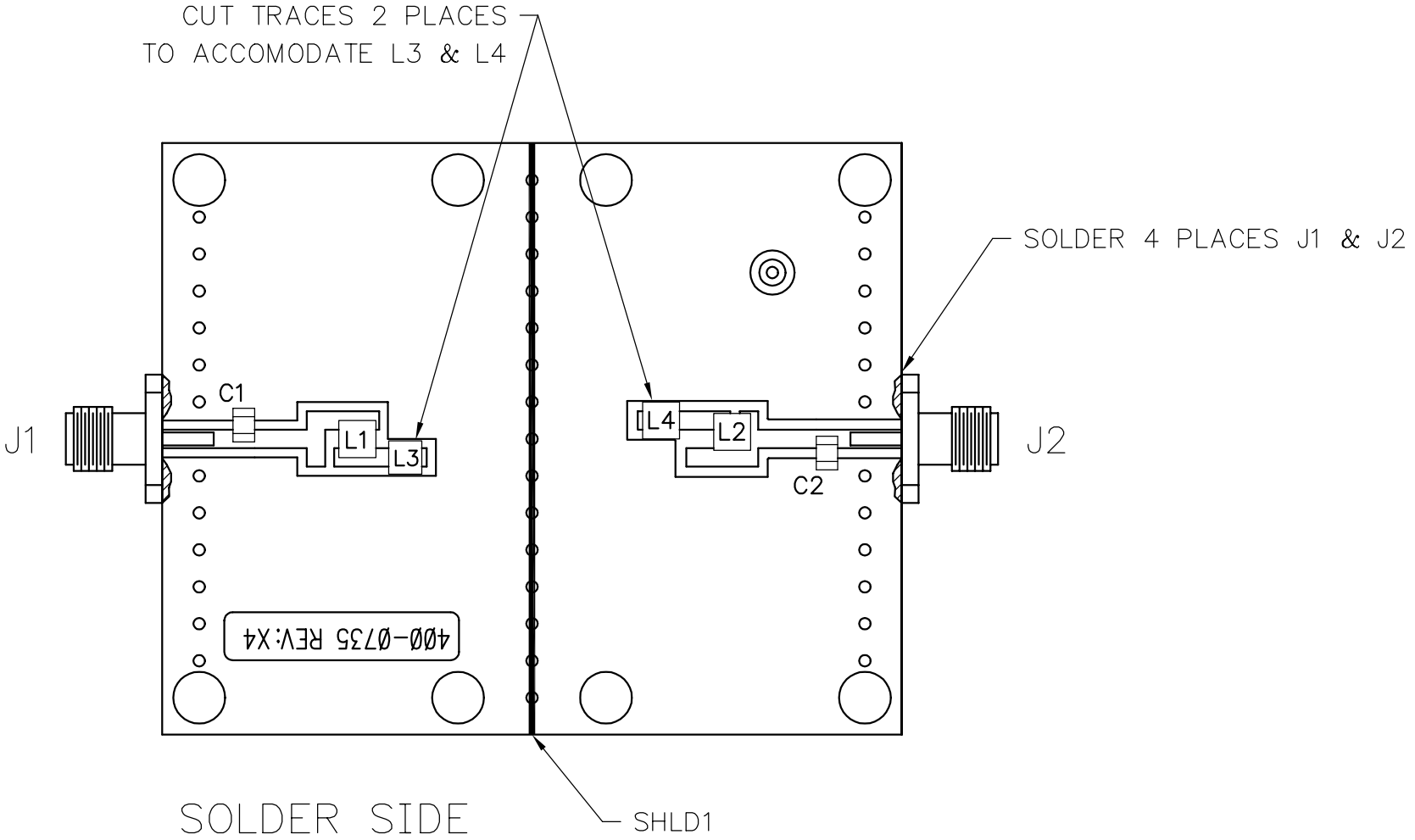
3. LABEL FIXTURE WITH ELECTRONIC METHOD AS SHOWN.

4. SOLDER J1 & J2 TO PCB AS SHOWN.



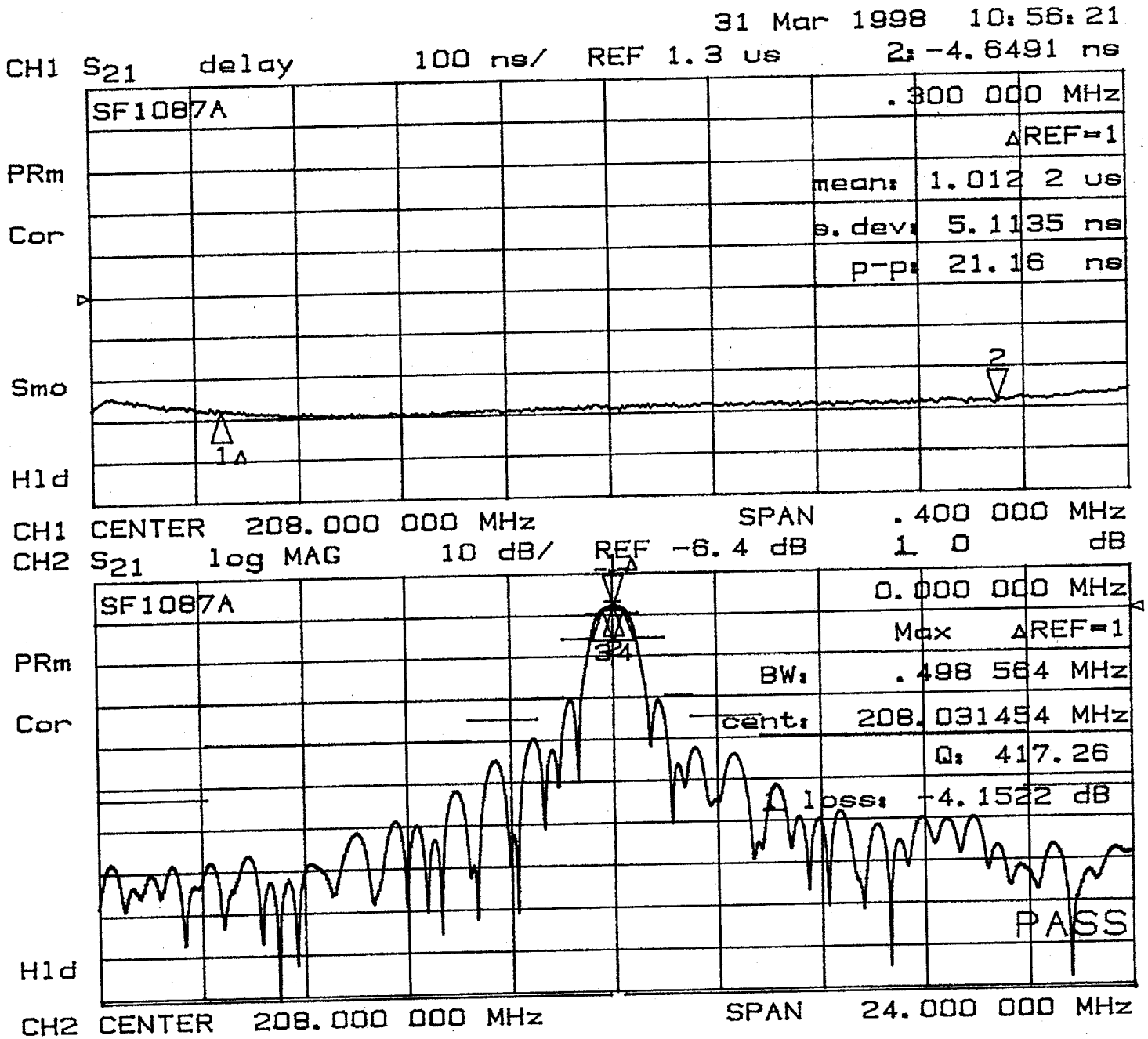
COMPONENT SIDE

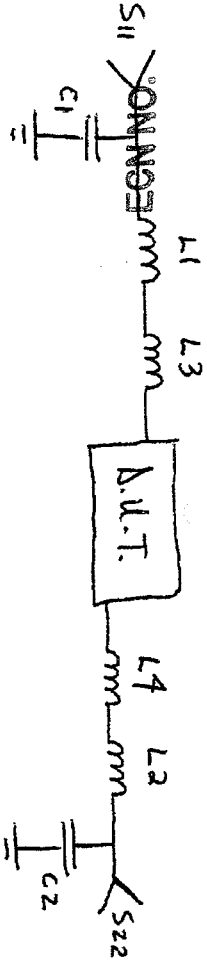
RF Monolithics, Inc. DALLAS, TEXAS 75244		SIZE A	CODE IDENT 2U874	DWG. NO. SF1087A-000	REV B	SHEET 2



RF Monolithics, Inc. DALLAS, TEXAS 75244		SIZE A	CODE IDENT 2U874	DWG. NO. SF1087A-000	REV B	SHEET 3

SF1087A (DEMO), M.T., 3-31-98, Dc#9812





C1, C2 = 27pf
L1 = 100nH
L2 = 120nH
L3 = 4.7nH
L4 = 12.0nH

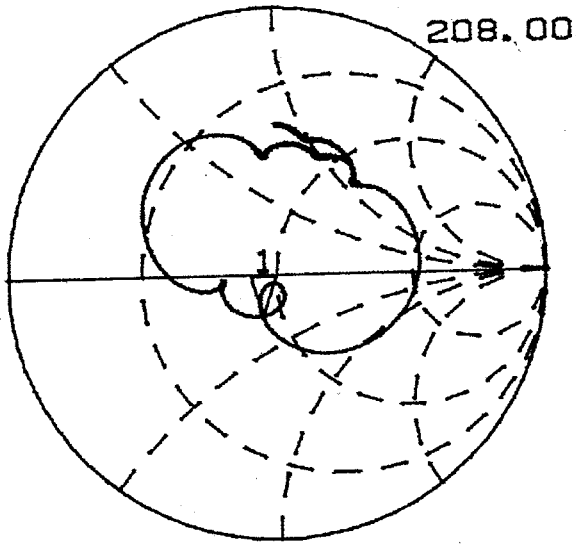
31 Mar 1998 11:03:04

CH1 S_{11} 1 U FS 1: 43.58 Ω -13.717 Ω 55.783 pF

SF1087A 208.000 000 MHz

PRm

Cor



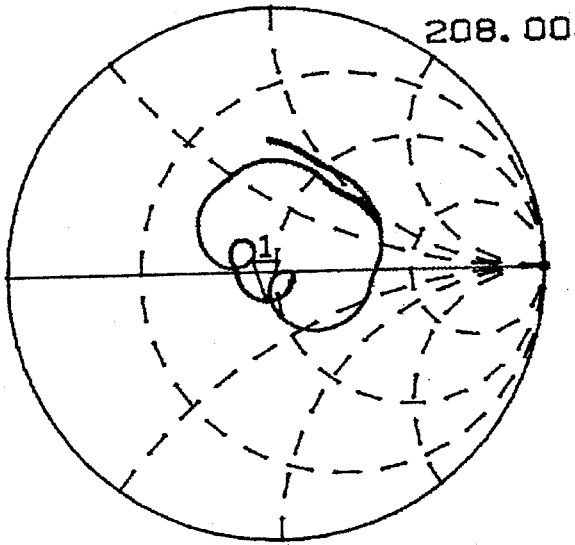
Hld

CH2 S_{22} 1 U FS 1 45.633 Ω -9.9844 Ω 76.637 pF

SF1087A 208.000 000 MHz

PRm

Cor



Hld

CENTER 208.000 000 MHz SPAN 5.000 000 MHz

ECN NO. 10225

BILL OF MATERIALS

PART IDENTIFIER	DESCRIPTION 1	DESCRIPTION 2	QTY/ASSY	REFERENCE DESCRIPTION
SF1087A-DEMO	DEMO BOARD,SF1087A			
SF1087A-000	ASSY DIAGRAM,DEMO BOARD,	SF1087A	0	
400-0735-001	PCB,DEMO BOARD,13.3 X 6.5		1.0000	PCB
400-0533-001	SHIELD,TO-39 TEST FIXTURE		1.0000	SHLD1
500-0003-270	CAP,CHIP,NPO,27(J),STD		2.0000	C 1,2
500-0248-001	CONN,COAX,FLANGE MT.JACK	4 HOLE	2.0000	J 1,2
500-0010-101	IND,CHIP,1008CS,100NH,10%		1.0000	L 1
500-0010-121	IND,CHIP,1008CS,120NH,10%		1.0000	L 2
500-0010-047	IND,CHIP,1008CS,4.7NH,10%		1.0000	L 3
500-0010-120	IND,CHIP,1008CS,12NH,10%		1.0000	L 4
500-0010-150	IND,CHIP,1008CS,15NH,10%		1.0000	L 5 (MAY SUB. FOR L4)

REV HISTORY

REV	ECN	DATE	DESCRIPTION
A	6714	05/08/98	INITIAL RELEASE

FRFM	SIZE A	FSCM NO. 2U874	DWG NO. SF1087A-DEMO
SCALE NONE	W/O or ECN 6714	REV A	SHEET 2 OF 2