

## MAX II Development Kit

from *Altera Corporation*

The MAX<sup>®</sup> II CPLD Development Kit, from the #1 market share supplier of CPLDs, enables you to evaluate the MAX II feature set or begin prototyping your own design. It includes reference designs (LCD controller, PCI, USB, and Slot Machine), demo designs, software, cables, all the accessories needed to ensure fast and easy use of the MAX II CPLD.

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### Ordering Information

**Table 1. MAX II Development Kit Ordering Code and Pricing Information**

Ordering Code	Price	Ordering Information
DK-MAXII-1270N	\$150 <a href="#">Buy Now</a>	<a href="#">Buy online</a> via Altera's eStore or contact your <a href="#">local Altera® distributor</a> to place your order.

### Development Kit Contents

The MAX II Development Kit includes the following:

- RoHS-compliant MAX II development board, PCI edge form-factor
  - MAX II CPLD, EPM1270F256C5N
  - SRAM (128K x 8 bit)
  - 4-channel analog-to-digital converter (8-bit resolution)
  - USB media access control (MAC) with physical layer (PHY)
  - 16x2 character LCD module
  - Temperature gauge with serial peripheral interface (SPI)
  - Onboard power meter
  - Active I/O sense circuitry
  - Four user-defined, push-button switches
  - Four user-defined LEDs
- Connectors
  - PCI edge form-factor (3.3- and 5.0-V tolerant)
  - JTAG connectors
  - USB connector (Type B)
  - One 3.3-V-tolerant expansion/prototype header (41 available user I/O pins)
- Cables and accessories
  - [USB Blaster™ download programming cable](#)
  - Type A-B USB cable (3 feet)
- Reference designs and demos for MAX II CPLDs (partial list):
  - USB reference design (with software drivers)
  - PCI 32-bit target, reference design (with software drivers)
  - LCD controller reference design
  - Low power demo
  - Real-time in-system programmability (ISP) demo
- [Quartus® II Web Edition design software](#)
- Complete documentation
  - Getting Started Guide, Reference Manual, Bill of Materials, Schematic, and board assembly files

**Figure 1. MAX II Development Board**



[View Full Size](#)

### Documentation

**Table 2. MAX II Development Kit Documents Available**

Document	Comments
<a href="#">MAX II Development Kit Getting Started User Guide</a>	PDF in English
<a href="#">MAX II Development Board Data Sheet</a>	PDF in English
<a href="#">Complete kit document</a>	FTP download

### Related Links

- [MAX II design examples](#) (some include Verilog source code & testbench, Quartus II project files, and ModelSim files)
- [MAX IIZ zero-power devices](#), the latest generation of CPLDs from Altera
- [Other MAX II development kits](#)

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