

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: $\{P_3-P_5-P_6\} : \{J_3-J_6\}$: $1CT : 1CT \pm 3\%$	1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.
($P_1-P_4-P_2\}$: $\{J_1-J_2\}$)	: $1CT : 1CT \pm 3\%$	
2.0 INDUCTANCE: $\{P_1-P_2\}$: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias	
$\{P_3-P_6\}$: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias	
3.0 LEAKAGE INDUCTANCE: P_6-P_3 (WITH J6 AND J3 SHORT)	: 0.3 MAX. @ 1MHz	
P_2-P_1 (WITH J2 AND J1 SHORT)	: 0.3 MAX. @ 1MHz	
4.0 INTERWINDING CAPACITANCE: (P_6, P_5, P_3) TO (J_6, J_3)	: 30pf MAX @ 1MHz	
(P_2, P_4, P_1) TO (J_2, J_1)	: 30pf MAX. @ 1MHz	
5.0 DC RESISTANCE: $(J_6-J_3)=(J_2-J_1)$: 1.2 ohms Max.	

NOTES

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RECEIVE

6.0 RETURN LOSS: $(P_6 - P_4) = 100$ OHMS AND $(P_1 - P_2) = 100$ OHM REF.

1MHz TO 30MHz
60MHz TO 80MHz

: 18dB MIN.
: 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).

7.0 DIELECTRIC WITHSTAND: (J_1, J_2) TO (P_1, P_2)
 (J_3, J_6) TO (P_3, P_6)

: 1500 VAC
: 1500 VAC

8.0 INSERTION LOSS: $R_S = R_L = 100$ ohms
100KHz TO 100MHz

: 1.1 dB TYP

9.0 RISE TIME: $R_S = 100$ OHMS AND $R_L = 100$ OHMS
OUTPUT VOLTAGE = 1 V peak
PULSE WIDTH= 112nS

: 3.0 nS MAX
: 3.0 nS MAX

10.0 CROSS TALK: 1MHz TO 100MHz

: 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 30MHz TO 100MHz

: 35dB TYP

12.0 OPERATING TEMPERATURE

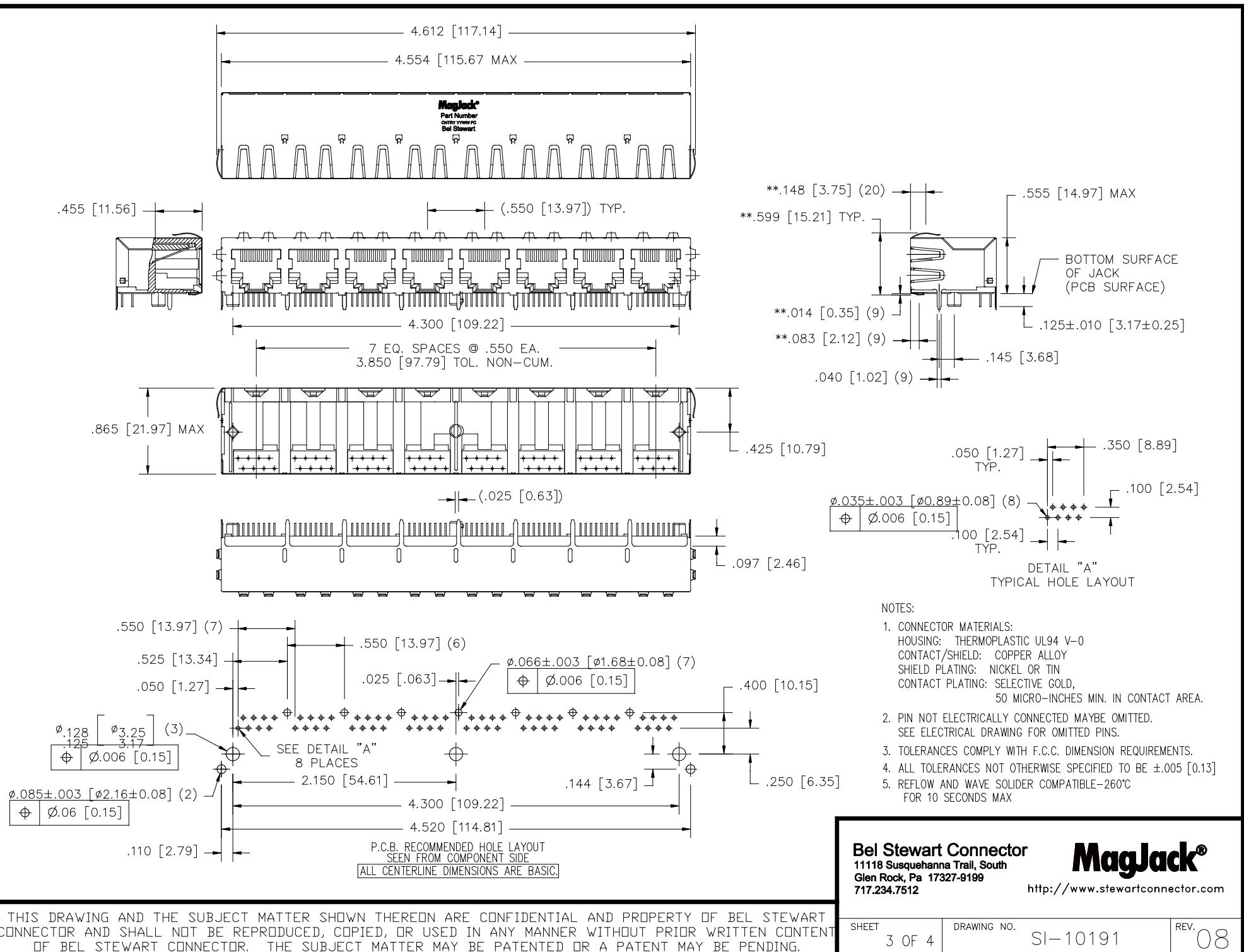
: 0°C TO 70°C

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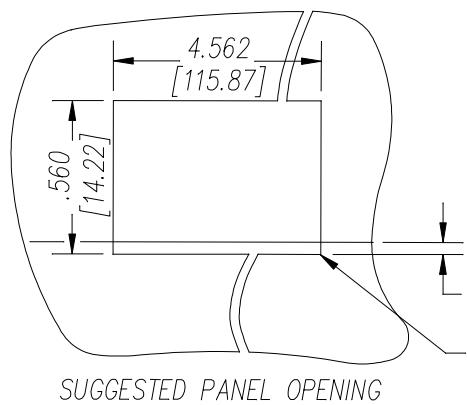
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SHEET 2 OF 4 DRAWING NO. SI-10191 REV. 09

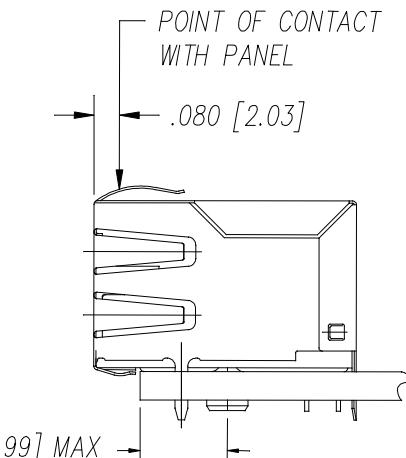


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.000 (TOP OF PCB TO BOTTOM OF OPENING)

.010 [0.25] MAX. RADIUS(4)



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]

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