

# 74LVC3G07

## Triple buffer with open-drain output

Rev. 15 — 24 August 2023

Product data sheet

## 1. General description

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The 74LVC3G07 provides three non-inverting buffers.

The output of the device is an open-drain and can be connected to other open-drain outputs to implement active-LOW wired-OR or active-HIGH wired-AND functions.

Input can be driven from either 3.3 V or 5 V devices. This feature allows the use of this device in a mixed 3.3 V and 5 V environment.

Schmitt trigger action at all inputs makes the circuit tolerant for slower input rise and fall time.

This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

## 2. Features and benefits

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- Wide supply voltage range from 1.65 V to 5.5 V
- 5 V tolerant input/output for interfacing with 5 V logic
- High noise immunity
- Complies with JEDEC standard:
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8-B/JESD36 (2.7 V to 3.6 V).
- -24 mA output drive ( $V_{CC} = 3.0$  V)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5 V
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C.

3. Ordering information

Table 1. Ordering information

| Type number                 | Package           |        |                                                                                             |                          |
|-----------------------------|-------------------|--------|---------------------------------------------------------------------------------------------|--------------------------|
|                             | Temperature range | Name   | Description                                                                                 | Version                  |
| <a href="#">74LVC3G07DP</a> | -40 °C to +125 °C | TSSOP8 | plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm     | <a href="#">SOT505-2</a> |
| <a href="#">74LVC3G07DC</a> | -40 °C to +125 °C | VSSOP8 | plastic very thin shrink small outline package; 8 leads; body width 2.3 mm                  | <a href="#">SOT765-1</a> |
| <a href="#">74LVC3G07GT</a> | -40 °C to +125 °C | XSON8  | plastic extremely thin small outline package; no leads; 8 terminals; body 1 × 1.95 × 0.5 mm | <a href="#">SOT833-1</a> |
| <a href="#">74LVC3G07GF</a> | -40 °C to +125 °C | XSON8  | extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1 × 0.5 mm         | <a href="#">SOT1089</a>  |
| <a href="#">74LVC3G07GN</a> | -40 °C to +125 °C | XSON8  | extremely thin small outline package; no leads; 8 terminals; body 1.2 × 1.0 × 0.35 mm       | <a href="#">SOT1116</a>  |
| <a href="#">74LVC3G07GS</a> | -40 °C to +125 °C | XSON8  | extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1.0 × 0.35 mm      | <a href="#">SOT1203</a>  |

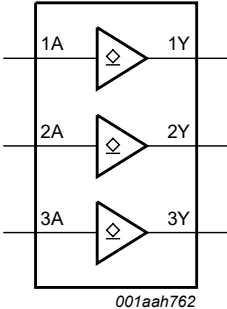
4. Marking

Table 2. Marking codes

| Type number | Marking code [1] |
|-------------|------------------|
| 74LVC3G07DP | V07              |
| 74LVC3G07DC | V07              |
| 74LVC3G07GT | V07              |
| 74LVC3G07GF | V7               |
| 74LVC3G07GN | V7               |
| 74LVC3G07GS | V7               |

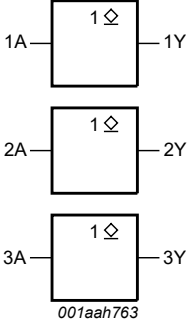
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



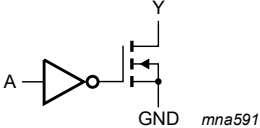
001aah762

Fig. 1. Logic symbol



001aah763

Fig. 2. IEC logic symbol



mna591

Fig. 3. Logic diagram (one driver)

6. Pinning information

6.1. Pinning

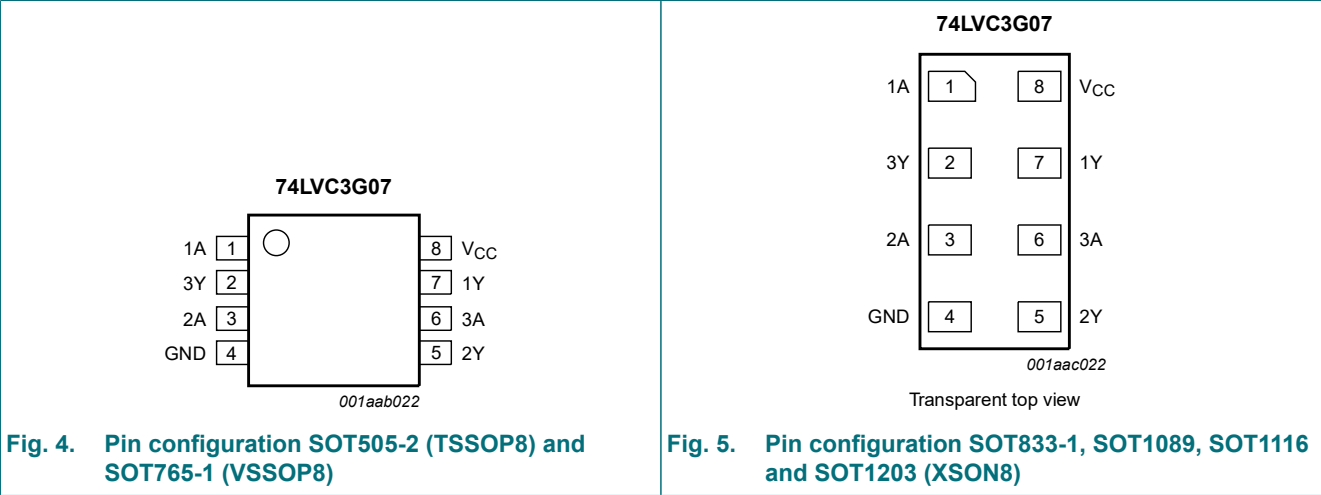


Fig. 4. Pin configuration SOT505-2 (TSSOP8) and SOT765-1 (VSSOP8)

Fig. 5. Pin configuration SOT833-1, SOT1089, SOT1116 and SOT1203 (XSON8)

6.2. Pin description

Table 3. Pin description

| Symbol          | Pin     | Description    |
|-----------------|---------|----------------|
| 1A, 2A, 3A      | 1, 3, 6 | data input     |
| GND             | 4       | ground (0 V)   |
| 1Y, 2Y, 3Y      | 7, 5, 2 | data output    |
| V <sub>CC</sub> | 8       | supply voltage |

7. Functional description

Table 4. Function table

*H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.*

| Input nA | Output nY |
|----------|-----------|
| L        | L         |
| H        | Z         |

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter               | Conditions                                 | Min  | Max  | Unit |
|------------------|-------------------------|--------------------------------------------|------|------|------|
| V <sub>CC</sub>  | supply voltage          |                                            | -0.5 | +6.5 | V    |
| I <sub>IK</sub>  | input clamping current  | V <sub>I</sub> < 0 V                       | -50  | -    | mA   |
| V <sub>I</sub>   | input voltage           | [1]                                        | -0.5 | +6.5 | V    |
| I <sub>OK</sub>  | output clamping current | V <sub>O</sub> < 0 V                       | -50  | -    | mA   |
| V <sub>O</sub>   | output voltage          | Active mode [1]                            | -0.5 | +6.5 | V    |
|                  |                         | Power-down mode; V <sub>CC</sub> = 0 V [1] | -0.5 | +6.5 | V    |
| I <sub>O</sub>   | output current          | V <sub>O</sub> = 0 V to 6.5 V              | -    | 50   | mA   |
| I <sub>CC</sub>  | supply current          |                                            | -    | 100  | mA   |
| I <sub>GND</sub> | ground current          |                                            | -100 | -    | mA   |
| T <sub>stg</sub> | storage temperature     |                                            | -65  | +150 | °C   |
| P <sub>tot</sub> | total power dissipation | T <sub>amb</sub> = -40 °C to +125 °C [2]   | -    | 250  | mW   |

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT505-2 (TSSOP8) package: P<sub>tot</sub> derates linearly with 4.6 mW/K above 96 °C.  
For SOT765-1 (VSSOP8) package: P<sub>tot</sub> derates linearly with 4.9 mW/K above 99 °C.  
For SOT833-1 (XSON8) package: P<sub>tot</sub> derates linearly with 3.1 mW/K above 68 °C.  
For SOT1089 (XSON8) package: P<sub>tot</sub> derates linearly with 4.0 mW/K above 88 °C.  
For SOT1116 (XSON8) package: P<sub>tot</sub> derates linearly with 4.2 mW/K above 90 °C.  
For SOT1203 (XSON8) package: P<sub>tot</sub> derates linearly with 3.6 mW/K above 81 °C.

9. Recommended operating conditions

Table 6. Operating conditions

| Symbol           | Parameter                           | Conditions                             | Min  | Max  | Unit |
|------------------|-------------------------------------|----------------------------------------|------|------|------|
| V <sub>CC</sub>  | supply voltage                      |                                        | 1.65 | 5.5  | V    |
| V <sub>I</sub>   | input voltage                       |                                        | 0    | 5.5  | V    |
| V <sub>O</sub>   | output voltage                      | Active mode                            | 0    | 5.5  | V    |
|                  |                                     | Power-down mode; V <sub>CC</sub> = 0 V | 0    | 5.5  | V    |
| T <sub>amb</sub> | ambient temperature                 |                                        | -40  | +125 | °C   |
| Δt/ΔV            | input transition rise and fall rate | V <sub>CC</sub> = 1.65 V to 2.7 V      | -    | 20   | ns/V |
|                  |                                     | V <sub>CC</sub> = 2.7 V to 5.5 V       | -    | 10   | ns/V |

10. Static characteristics

**Table 7. Static characteristics**  
 At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                 | Conditions                                                                                                             | T <sub>amb</sub> = -40 °C to +85 °C |         |                     | T <sub>amb</sub> = -40 °C to +125 °C |                     | Unit |
|------------------|---------------------------|------------------------------------------------------------------------------------------------------------------------|-------------------------------------|---------|---------------------|--------------------------------------|---------------------|------|
|                  |                           |                                                                                                                        | Min                                 | Typ [1] | Max                 | Min                                  | Max                 |      |
| V <sub>IH</sub>  | HIGH-level input voltage  | V <sub>CC</sub> = 1.65 V to 1.95 V                                                                                     | 0.65V <sub>CC</sub>                 | -       | -                   | 0.65V <sub>CC</sub>                  | -                   | V    |
|                  |                           | V <sub>CC</sub> = 2.3 V to 2.7 V                                                                                       | 1.7                                 | -       | -                   | 1.7                                  | -                   | V    |
|                  |                           | V <sub>CC</sub> = 2.7 V to 3.6 V                                                                                       | 2.0                                 | -       | -                   | 2.0                                  | -                   | V    |
|                  |                           | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                                       | 0.7V <sub>CC</sub>                  | -       | -                   | 0.7V <sub>CC</sub>                   | -                   | V    |
| V <sub>IL</sub>  | LOW-level input voltage   | V <sub>CC</sub> = 1.65 V to 1.95 V                                                                                     | -                                   | -       | 0.35V <sub>CC</sub> | -                                    | 0.35V <sub>CC</sub> | V    |
|                  |                           | V <sub>CC</sub> = 2.3 V to 2.7 V                                                                                       | -                                   | -       | 0.7                 | -                                    | 0.7                 | V    |
|                  |                           | V <sub>CC</sub> = 2.7 V to 3.6 V                                                                                       | -                                   | -       | 0.8                 | -                                    | 0.8                 | V    |
|                  |                           | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                                       | -                                   | -       | 0.3V <sub>CC</sub>  | -                                    | 0.3V <sub>CC</sub>  | V    |
| V <sub>OL</sub>  | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                                    |                                     |         |                     |                                      |                     |      |
|                  |                           | I <sub>O</sub> = 100 µA; V <sub>CC</sub> = 1.65 V to 5.5 V                                                             | -                                   | -       | 0.1                 | -                                    | 0.1                 | V    |
|                  |                           | I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V                                                                        | -                                   | -       | 0.45                | -                                    | 0.70                | V    |
|                  |                           | I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V                                                                         | -                                   | -       | 0.3                 | -                                    | 0.45                | V    |
|                  |                           | I <sub>O</sub> = 12 mA; V <sub>CC</sub> = 2.7 V                                                                        | -                                   | -       | 0.4                 | -                                    | 0.60                | V    |
|                  |                           | I <sub>O</sub> = 24 mA; V <sub>CC</sub> = 3.0 V                                                                        | -                                   | -       | 0.55                | -                                    | 0.80                | V    |
|                  |                           | I <sub>O</sub> = 32 mA; V <sub>CC</sub> = 4.5 V                                                                        | -                                   | -       | 0.55                | -                                    | 0.80                | V    |
| I <sub>I</sub>   | input leakage current     | V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V                                                          | -                                   | ±0.1    | ±1                  | -                                    | ±1                  | µA   |
| I <sub>OZ</sub>  | OFF-state output current  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> ; V <sub>O</sub> = V <sub>CC</sub> or GND; V <sub>CC</sub> = 5.5 V | -                                   | ±0.1    | ±2                  | -                                    | ±2                  | µA   |
| I <sub>OFF</sub> | power-off leakage current | V <sub>I</sub> or V <sub>O</sub> = 5.5 V; V <sub>CC</sub> = 0 V                                                        | -                                   | ±0.1    | ±2                  | -                                    | ±2                  | µA   |
| I <sub>CC</sub>  | supply current            | V <sub>I</sub> = 5.5 V or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.65 V to 5.5 V                                 | -                                   | 0.1     | 4                   | -                                    | 4                   | µA   |
| ΔI <sub>CC</sub> | additional supply current | per pin; V <sub>CC</sub> = 2.3 V to 5.5 V; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A              | -                                   | 5       | 500                 | -                                    | 500                 | µA   |
| C <sub>I</sub>   | input capacitance         |                                                                                                                        | -                                   | 2.5     | -                   | -                                    | -                   | pF   |

[1] All typical values are measured at V<sub>CC</sub> = 3.3 V and at T<sub>amb</sub> = 25 °C.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see Fig. 7.

| Symbol          | Parameter                     | Conditions                                                            | T <sub>amb</sub> = -40 °C to +85 °C |         |     | T <sub>amb</sub> = -40 °C to +125 °C |     | Unit |
|-----------------|-------------------------------|-----------------------------------------------------------------------|-------------------------------------|---------|-----|--------------------------------------|-----|------|
|                 |                               |                                                                       | Min                                 | Typ [1] | Max | Min                                  | Max |      |
| t <sub>pd</sub> | propagation delay             | nA to nY; see Fig. 6 [2]                                              |                                     |         |     |                                      |     |      |
|                 |                               | V <sub>CC</sub> = 1.65 V to 1.95 V                                    | 1.0                                 | 2.9     | 6.7 | 1.0                                  | 8.4 | ns   |
|                 |                               | V <sub>CC</sub> = 2.3 V to 2.7 V                                      | 0.5                                 | 1.7     | 4.3 | 0.5                                  | 5.5 | ns   |
|                 |                               | V <sub>CC</sub> = 2.7 V                                               | 1.0                                 | 2.3     | 4.2 | 1.0                                  | 5.3 | ns   |
|                 |                               | V <sub>CC</sub> = 3.0 V to 3.6 V                                      | 0.5                                 | 2.1     | 3.7 | 0.5                                  | 4.7 | ns   |
|                 |                               | V <sub>CC</sub> = 4.5 V to 5.5 V                                      | 0.5                                 | 1.5     | 2.9 | 0.5                                  | 3.7 | ns   |
| C <sub>PD</sub> | power dissipation capacitance | V <sub>I</sub> = GND to V <sub>CC</sub> ; V <sub>CC</sub> = 3.3 V [3] | -                                   | 6.5     | -   | -                                    | -   | pF   |

[1] Typical values are measured at T<sub>amb</sub> = 25 °C and V<sub>CC</sub> = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

[2] t<sub>pd</sub> is the same as t<sub>PLZ</sub> and t<sub>PZL</sub>.

[3] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$  where:

f<sub>i</sub> = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

Σ(C<sub>L</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>o</sub>) = sum of outputs.

11.1. Waveforms and test circuit

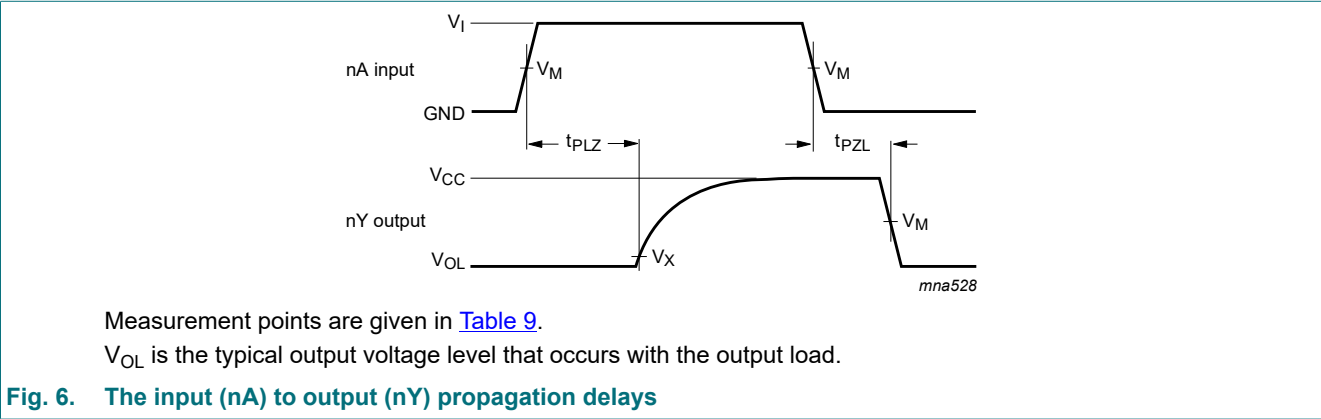


Table 9. Measurement points

| Supply voltage   | Input               | Output              |                           |
|------------------|---------------------|---------------------|---------------------------|
| Vcc              | VM                  | VM                  | Vx                        |
| 1.65 V to 1.95 V | $0.5 \times V_{CC}$ | $0.5 \times V_{CC}$ | $V_{OL} + 0.15 \text{ V}$ |
| 2.3 V to 2.7 V   | $0.5 \times V_{CC}$ | $0.5 \times V_{CC}$ | $V_{OL} + 0.15 \text{ V}$ |
| 2.7 V            | 1.5 V               | 1.5 V               | $V_{OL} + 0.3 \text{ V}$  |
| 3.0 V to 3.6 V   | 1.5 V               | 1.5 V               | $V_{OL} + 0.3 \text{ V}$  |
| 4.5 V to 5.5 V   | $0.5 \times V_{CC}$ | $0.5 \times V_{CC}$ | $V_{OL} + 0.3 \text{ V}$  |

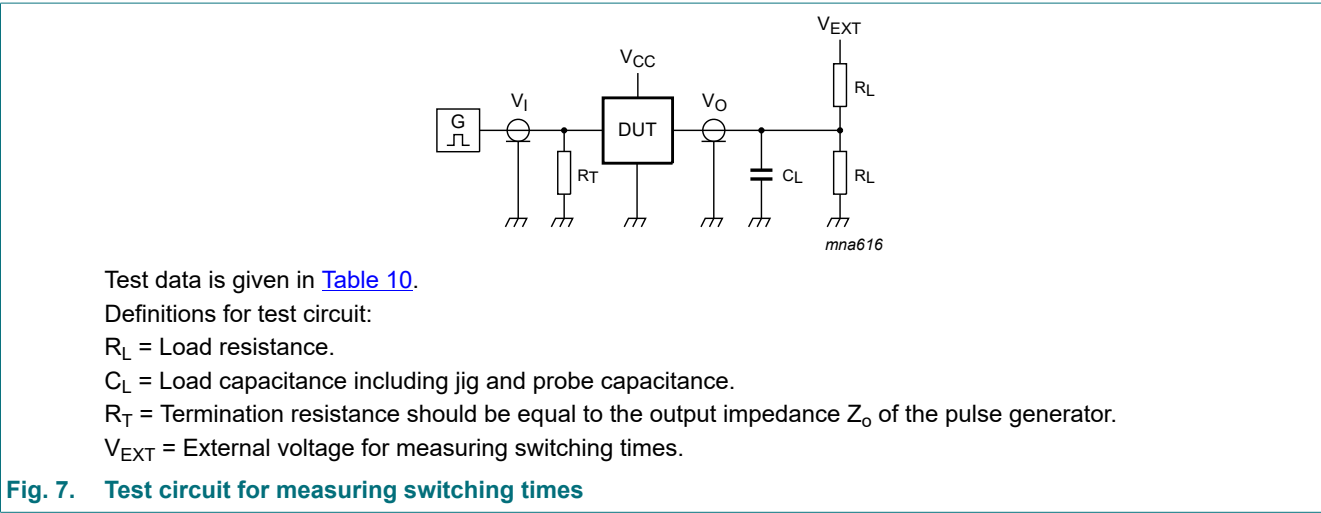


Table 10. Test data

| Supply voltage   | Input |                       | Load  |       | VEXT              |
|------------------|-------|-----------------------|-------|-------|-------------------|
| Vcc              | VI    | tr = tf               | CL    | RL    | tpZL, tPLZ        |
| 1.65 V to 1.95 V | VCC   | $\leq 2.0 \text{ ns}$ | 30 pF | 1 kΩ  | $2 \times V_{CC}$ |
| 2.3 V to 2.7 V   | VCC   | $\leq 2.0 \text{ ns}$ | 30 pF | 500 Ω | $2 \times V_{CC}$ |
| 2.7 V            | 2.7 V | $\leq 2.5 \text{ ns}$ | 50 pF | 500 Ω | 6 V               |
| 3.0 V to 3.6 V   | 2.7 V | $\leq 2.5 \text{ ns}$ | 50 pF | 500 Ω | 6 V               |
| 4.5 V to 5.5 V   | VCC   | $\leq 2.5 \text{ ns}$ | 50 pF | 500 Ω | $2 \times V_{CC}$ |

12. Package outline

TSSOP8: plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm    SOT505-2

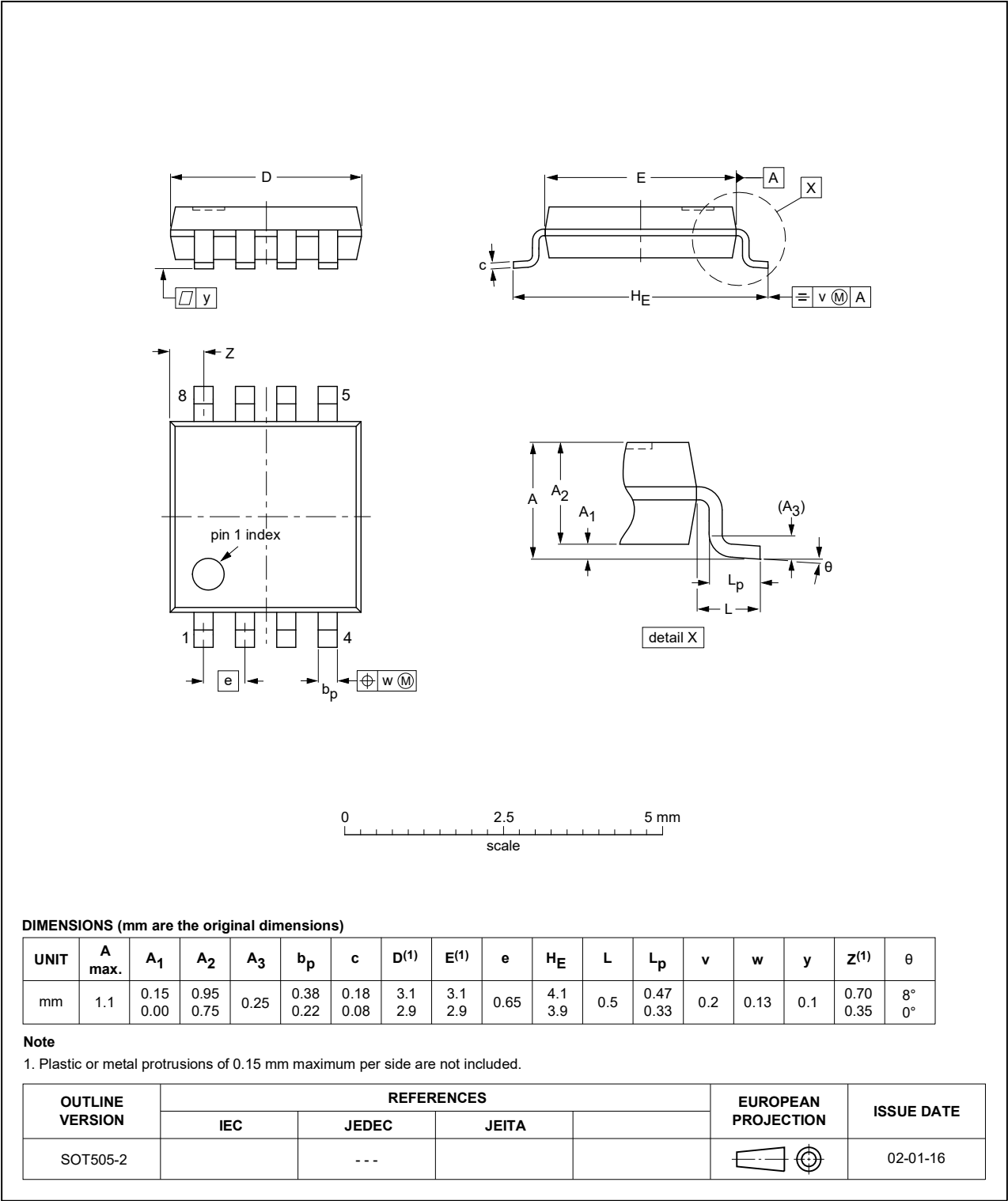


Fig. 8. Package outline SOT505-2 (TSSOP8)



VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1

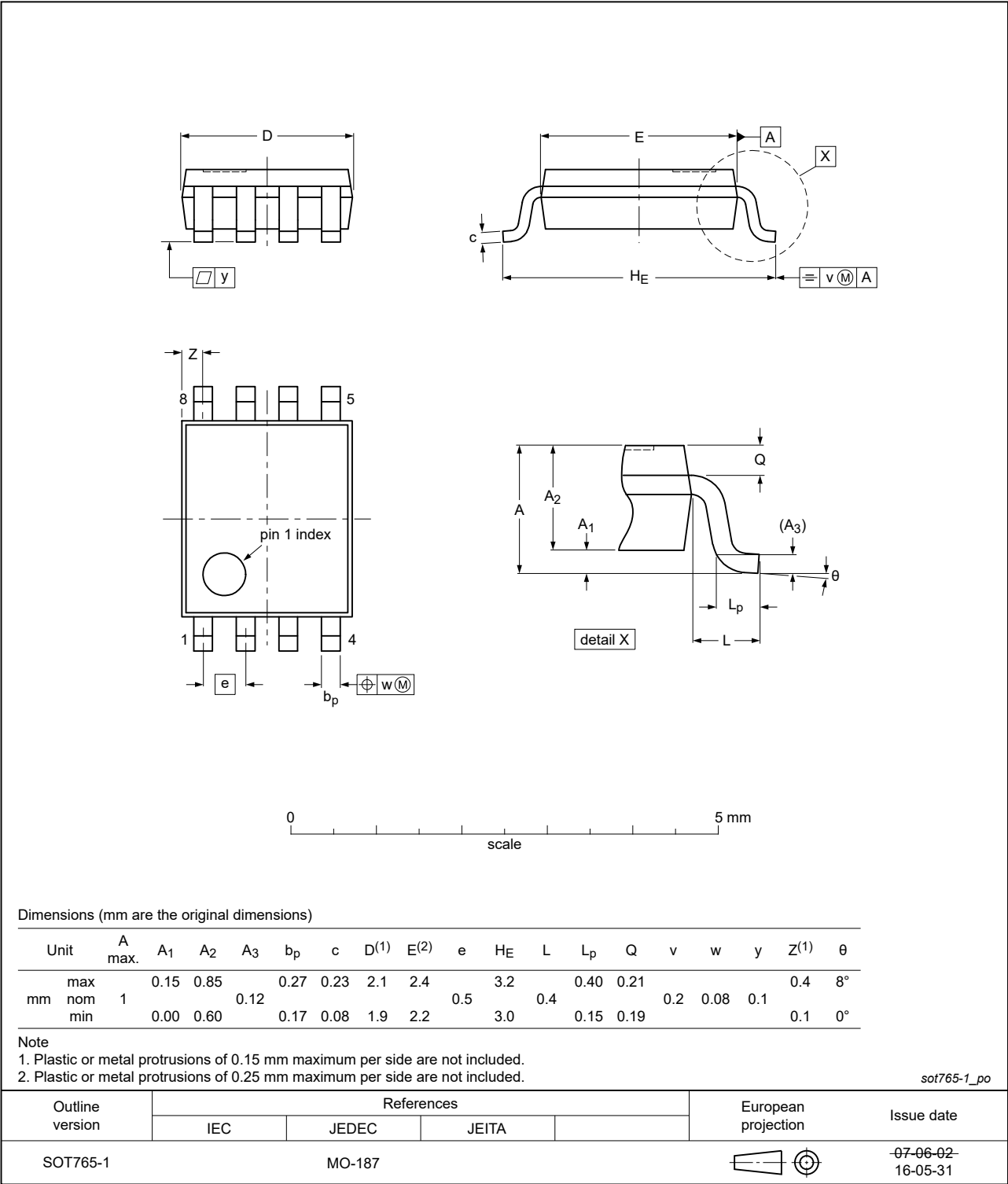


Fig. 9. Package outline SOT765-1 (VSSOP8)

XSON8: plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm

SOT833-1

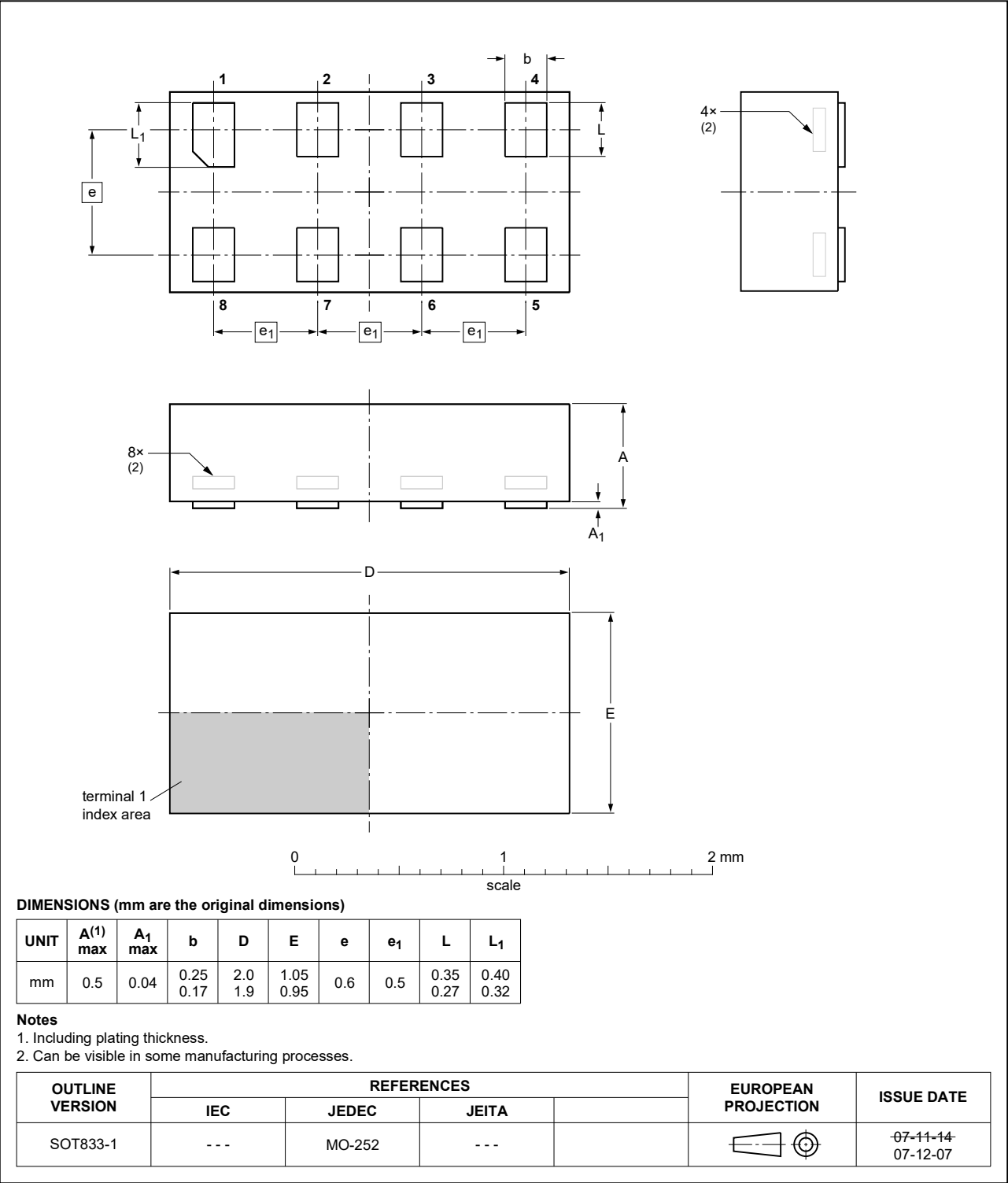


Fig. 10. Package outline SOT833-1 (XSON8)

XSON8: extremely thin small outline package; no leads;  
8 terminals; body 1.35 x 1 x 0.5 mm

SOT1089

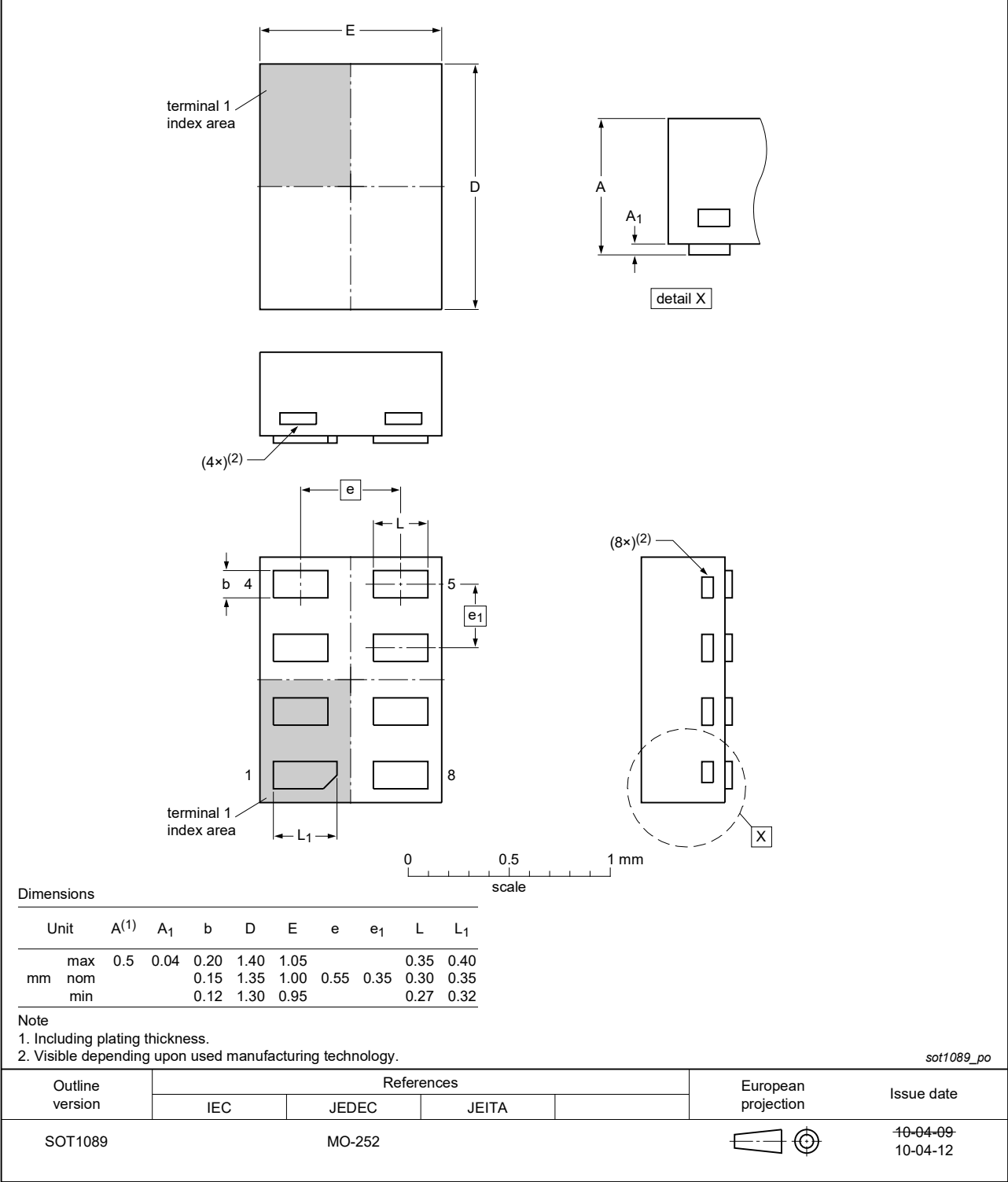


Fig. 11. Package outline SOT1089 (XSON8)

XSON8: extremely thin small outline package; no leads;  
8 terminals; body 1.2 x 1.0 x 0.35 mm

SOT1116

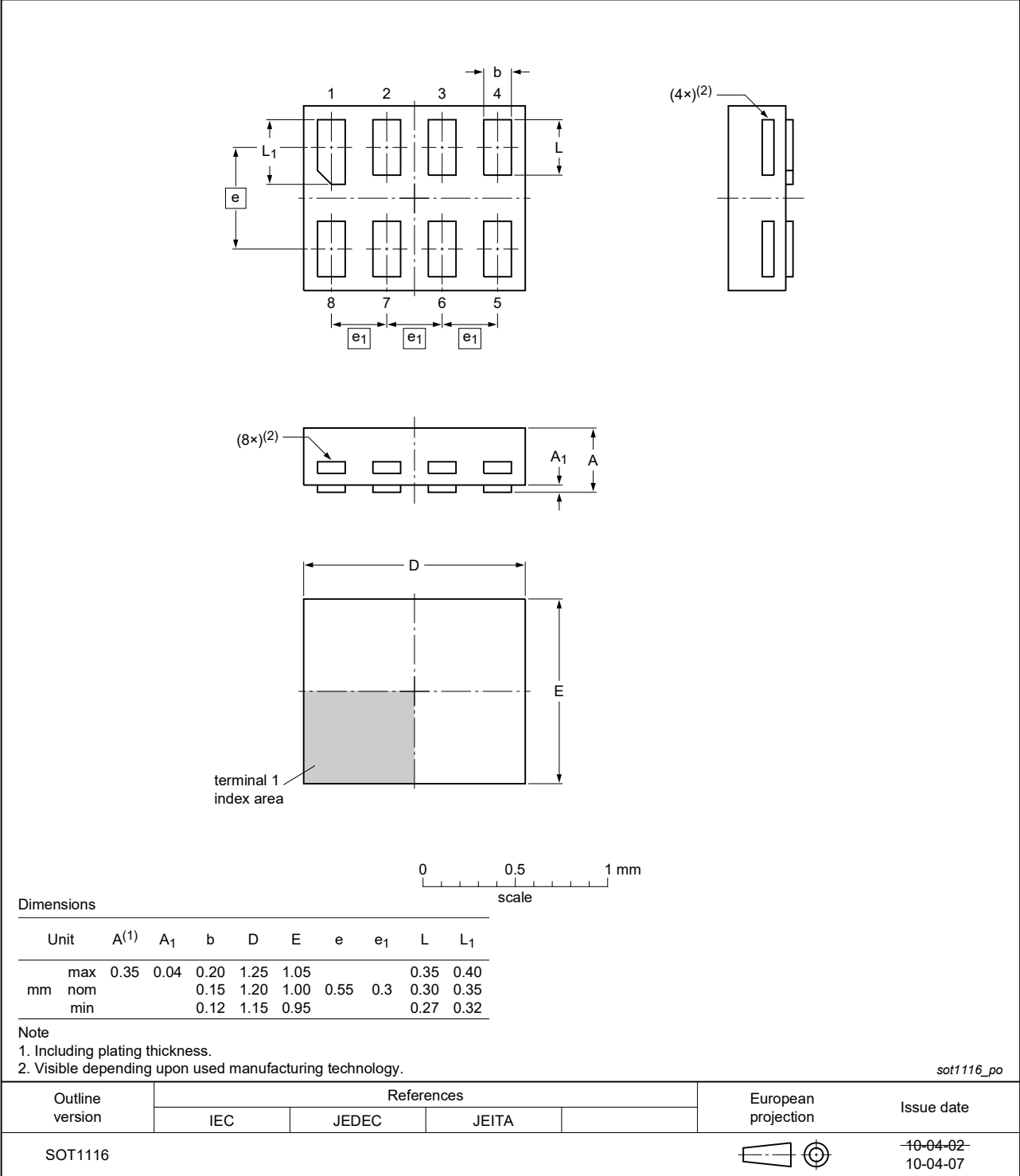
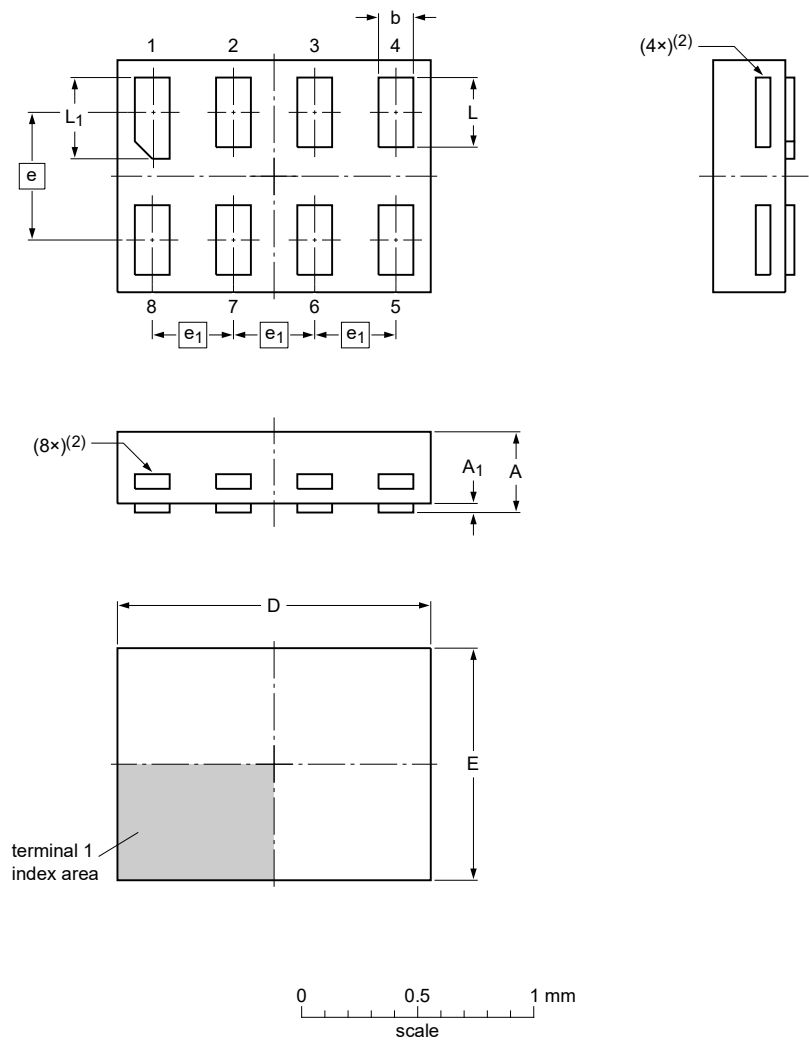


Fig. 12. Package outline SOT1116 (XSON8)

XSON8: extremely thin small outline package; no leads;  
8 terminals; body 1.35 x 1.0 x 0.35 mm

SOT1203



Dimensions

| Unit | A <sup>(1)</sup> | A <sub>1</sub> | b    | D    | E    | e    | e <sub>1</sub> | L    | L <sub>1</sub> |
|------|------------------|----------------|------|------|------|------|----------------|------|----------------|
| mm   | max              | 0.35           | 0.04 | 0.20 | 1.40 | 1.05 |                | 0.35 | 0.40           |
|      | nom              |                |      | 0.15 | 1.35 | 1.00 | 0.55           | 0.30 | 0.35           |
|      | min              |                |      | 0.12 | 1.30 | 0.95 |                | 0.27 | 0.32           |

Note  
1. Including plating thickness.  
2. Visible depending upon used manufacturing technology.

sot1203\_po

| Outline version | References |       |       |  | European projection                                                                   | Issue date                      |
|-----------------|------------|-------|-------|--|---------------------------------------------------------------------------------------|---------------------------------|
|                 | IEC        | JEDEC | JEITA |  |                                                                                       |                                 |
| SOT1203         |            |       |       |  |  | <del>10-04-02</del><br>10-04-06 |

Fig. 13. Package outline SOT1203 (XSON8)

## 13. Abbreviations

Table 11. Abbreviations

| Acronym | Description                             |
|---------|-----------------------------------------|
| CMOS    | Complementary Metal-Oxide Semiconductor |
| DUT     | Device Under Test                       |
| ESD     | ElectroStatic Discharge                 |
| HBM     | Human Body Model                        |
| TTL     | Transistor-Transistor Logic             |

## 14. Revision history

Table 12. Revision history

| Document ID    | Release date                                                                                                                                                                                                                                                                                   | Data sheet status  | Change notice | Supersedes     |
|----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|---------------|----------------|
| 74LVC3G07 v.15 | 20230824                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.14 |
| Modifications: | <ul style="list-style-type: none"> <li><a href="#">Section 2</a>: ESD specification updated according to the latest JEDEC standard.</li> </ul>                                                                                                                                                 |                    |               |                |
| 74LVC3G07 v.14 | 20210128                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.13 |
| Modifications: | <ul style="list-style-type: none"> <li>Type number 74LVC3G07GM (SOT902-2 / XQFN8) removed.</li> <li><a href="#">Table 5</a>: Derating values for <math>P_{tot}</math> total power dissipation updated.</li> </ul>                                                                              |                    |               |                |
| 74LVC3G07 v.13 | 20181023                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.12 |
| Modifications: | <ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type number 74LVC3G07GD (SOT996-2) removed.</li> </ul> |                    |               |                |
| 74LVC3G07 v.12 | 20161215                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.11 |
| Modifications: | <ul style="list-style-type: none"> <li><a href="#">Table 7</a>: The maximum limits for leakage current and supply current have changed.</li> </ul>                                                                                                                                             |                    |               |                |
| 74LVC3G07 v.11 | 20130409                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.10 |
| Modifications: | <ul style="list-style-type: none"> <li>For type number 74LVC3G07GD XSON8U has changed to XSON8.</li> </ul>                                                                                                                                                                                     |                    |               |                |
| 74LVC3G07 v.10 | 20120627                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.9  |
| Modifications: | <ul style="list-style-type: none"> <li>For type number 74LVC3G07GM the SOT code has changed to SOT902-2.</li> </ul>                                                                                                                                                                            |                    |               |                |
| 74LVC3G07 v.9  | 20111123                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.8  |
| Modifications: | <ul style="list-style-type: none"> <li>Legal pages updated.</li> </ul>                                                                                                                                                                                                                         |                    |               |                |
| 74LVC3G07 v.8  | 20111019                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.7  |
| 74LVC3G07 v.7  | 20100809                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.6  |
| 74LVC3G07 v.6  | 20080616                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.5  |
| 74LVC3G07 v.5  | 20080219                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.4  |
| 74LVC3G07 v.4  | 20070521                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.3  |
| 74LVC3G07 v.3  | 20050201                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.2  |
| 74LVC3G07 v.2  | 20041027                                                                                                                                                                                                                                                                                       | Product data sheet | -             | 74LVC3G07 v.1  |
| 74LVC3G07 v.1  | 20040608                                                                                                                                                                                                                                                                                       | Product data sheet | -             | -              |

## 15. Legal information

### Data sheet status

| Document status [1][2]         | Product status [3] | Definition                                                                            |
|--------------------------------|--------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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For sales office addresses, please send an email to: [salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)

Date of release: 24 August 2023