



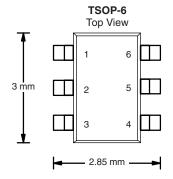
P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
	0.042 at V _{GS} = - 4.5 V	± 5.6		
- 8	0.060 at V _{GS} = - 2.5 V	± 4.7		
	0.080 at V _{GS} = - 1.8 V	± 2.9		

FEATURES

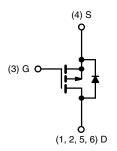
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs
- 1.8 V Rated
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3445DV-T1-E3 (Lead (Pb)-free)

Si3445DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 8	V	
Gate-Source Voltage		V _{GS}	± 8	V	
Continuous Drain Current (T,I = 150 °C) ^{a, b}	T _A = 25 °C	I_	± 5.6		
Continuous Drain Current (1 _J = 150 °C)	T _A = 70 °C	- I _D	± 4.5	_	
Pulsed Drain Current		I _{DM}	± 20	Α	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	- 1.7		
Mariana Barra Biratania h	T _A = 25 °C	P _D	2.0	W	
Maximum Power Dissipation ^{a, b}	T _A = 70 °C	' D	1.3	- vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian una lungation to Ambienta	t ≤ 5 s	R _{thJA}		62.5	°C/W
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	106		O/VV

Notes:

a. Surface Mounted on FR4 board.

b. $t \le 5$ s.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•		•	•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.45		- 1.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = -8 V, V _{GS} = 0 V	- 1		- 1		
	I _{DSS}	$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 5	- μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 15			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -5.6 \text{ A}$		0.034	0.042		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -4.7 \text{ A}$		0.048	0.060	Ω	
		$V_{GS} = -1.8 \text{ V}, I_D = -2.0 \text{ A}$		0.062	0.080		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 5.6 A		15		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.7	- 1.2	V	
Dynamic ^b	•		•	•			
Total Gate Charge	Qg			15	25		
Gate-Source Charge	Q_{gs}	$V_{DS} = -4 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -5.6 \text{ A}$		3		nC	
Gate-Drain Charge	Q_{gd}			2			
Turn-On Delay Time	t _{d(on)}			20	40		
Rise Time	t _r	V_{DD} = - 4 V, R_L = 4 Ω		50	100		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		110	220	ns	
Fall Time	t _f			60	120		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		60	100		

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

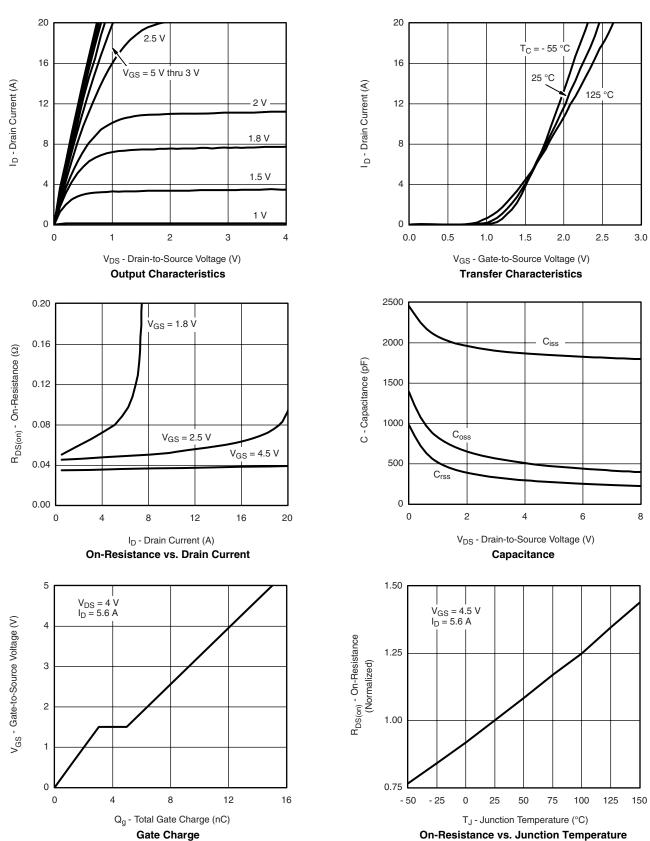
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.





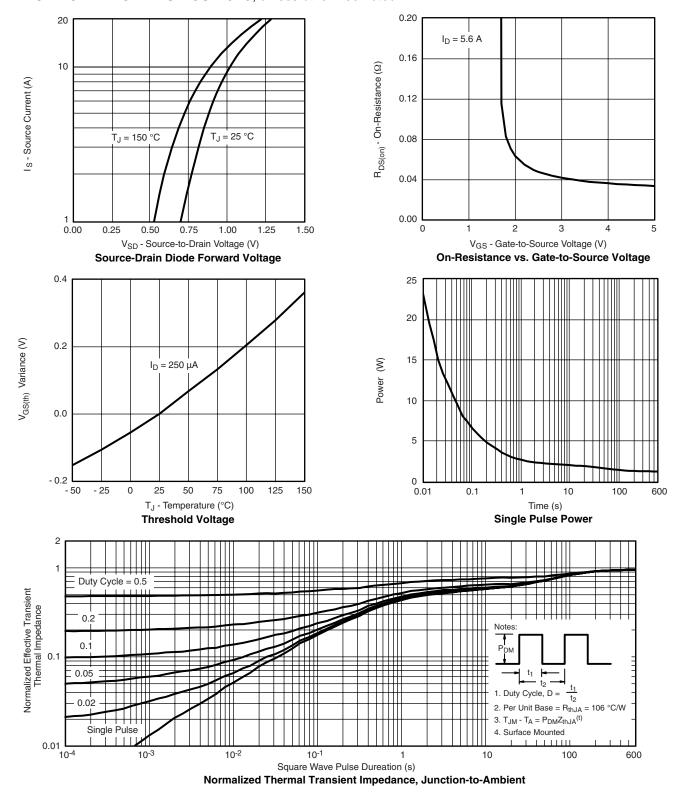
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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