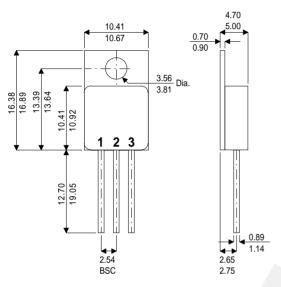


LT1086-220M LT1086-05-220M LT1086-12-220M

**MILITARY VERSION** 

## TO-220 Isolated Metal Package



Pinouts						
Pin	Fixed	Adjustable				
1	GROUND	ADJUST				
2	OUTPUT	OUTPUT				
3	INPUT	INPUT				
	Case is ISOLAT	ED				

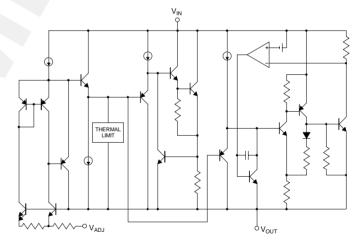
Ordering Information					
LT1086-220M	Positive Adjustable Regulator				
LT1086-05-220M	Fixed +5V Regulator				
LT1086-12-220M	Fixed +12V Regulator				

# LOW DROPOUT 1.5 AMP POSITIVE **VOLTAGE REGULATOR**

### **FEATURES**

- Low Dropout Performance
- Fixed or Adjustable Voltages
- Fixed Output Voltages of 5V & 12V
- Output Current 1.5A
- Line Regulation 0.015% / V Typical.
- Load Regulation 0.1% Typical.
- Military Temperature Range (-55 to +150°C)

## **BLOCK DIAGRAM**



## **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{V_{IN}}$	Input Voltage *			30V
$P_{D}$	Power Dissipation			Internally limited
V <sub>IN</sub>	Operating Input Voltage	Adjustable 5 Volt 12 Volt		25V 20V 25V
$T_{J}$	Operating Junction Temperature Range		Control Power	−55 to 150°C −55 to 200°C
$T_{STG}$	Storage Temperature Range			−65 to 150°C
$T_{LEAD}$	Lead Temperature (for 10 sec.)			300°C

<sup>\*</sup> Although the devices' maximum operating voltage is limited (20V for the 5V device, and 25V for the 12V and adjustable devices) the devices are guaranteed to withstand transient input voltages up to 30V. For input voltages greater than the maximum operating input voltage, some degradation of specifications will occur. For 5V and 12V devices operating at input-output voltage differentials >15V, a minimum external load of 5mA is required to maintain regulation.

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LT1086-220M LT1086-05-220M LT1086-12-220M

**MILITARY VERSION** 

## DESCRIPTION

The LT1086 is designed to provide 1.5A with higher efficiency than currently available devices. All internal circuitry is designed to operate down to 1V input to output differential and the dropout voltage is fully specified as a function of load current. Dropout is guaranteed at a maximum of 1.5V at maximum output current, decreasing at lower load currents. On-chip trimming adjusts the reference output voltage to 1%. Current limit is also trimmed, minimising the stress on both the regulator and power source circuitry under overload conditions.

The LT1086 is pin compatible with older 3 terminal adjustable regulators. A 10µF output capacitor is required on these new devices; however, this is usually included in most regulator designs.

Unlike PNP regulators, where up to 10% of the output current is wasted as quiescent current, the LT1086 quiescent current flows into the load, increasing efficiency.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>1</sub> = 25°C unless otherwise stated)

	Parameter	Output	Test Conditions		Min.	Typ	Max.	Unit
Vo	Output Voltage	5	$V_{IN} = 6.5 \text{ to } 20V$ $T_{J} = -55 \text{ to } 150^{\circ}\text{C}$	I <sub>O</sub> = 0 to 1.5A	4.90	5	5.10	Onit
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Output Voltage	12	$V_{IN} = 13.5V \text{ to } 25V$ $T_{J} = -55 \text{ to } 150^{\circ}\text{C}$	I <sub>O</sub> = 0 to 1.5A	11.76	12	12.24	V
V <sub>REF</sub>	Reference Voltage	ADJ.	$V_{IN} - V_{OUT} = 1.5 \text{ to } 15V$ $T_{J} = -55 \text{ to } 150^{\circ}\text{C}$	I <sub>O</sub> = 10mA to 1.5A	1.225	1.25	1.270	
REG <sub>(LINE)</sub>		5	V <sub>IN</sub> = 6.5 to 20V			0.5	10	
			I <sub>O</sub> = 0	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		1	10	mV
	Line Regulation	12	V <sub>IN</sub> = 13.5 to 25V			1	25	- %
			$I_{O} = 0$	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		2	25	
		ADJ.	$V_{IN} - V_{OUT} = 1.5 \text{ to } 15V$			0.015	0.2	
			I <sub>LOAD</sub> = 10mA	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		0.035	0.2	
		5	V <sub>IN</sub> = 8V			5	20	- mV - %
			$I_{O} = 0$ to 1.5A	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		10	35	
REG <sub>(LOAD)</sub>	Load Regulation	12	V <sub>IN</sub> = 15V			12	36	
			$I_{O} = 0$ to 1.5A	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		24	72	
	See notes 1,2	ADJ.	$V_{IN} - V_{OUT} = 3V$			0.1	0.3	
			I <sub>O</sub> = 10mA to 1.5A	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		0.2	0.4	
V <sub>D</sub>		5	$\Delta V_{OUT} = 50 \text{mV}$ $I_{OUT} = 1.5 \text{A}$	T <sub>J</sub> = -55 to 150°C		1.3	1.5	
	Dropout Voltage	12	$\Delta V_{OUT} = 120 \text{mV}$ $I_{OUT} = 1.5 \text{A}$	T <sub>J</sub> = -55 to 150°C		1.3	1.5	V
	See note 3	ADJ.	$\Delta V_{REF} = 1\%$ $I_{OUT} = 1.5A$	T <sub>J</sub> = -55 to 150°C		1.3	1.5	

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**MILITARY VERSION** 

## **ELECTRICAL CHARACTERISTICS** $(T_J = -55 \text{ to } +150 ^{\circ}\text{C} \text{ unless otherwise stated})$

		Output	I					
	Parameter	Voltage	Test Conditions		Min.	Тур.	Max.	Unit
I <sub>CL</sub>	Current Limit		$V_{IN} - V_{OUT} = 5V$	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$	1.5	1.6	2.5	A
	Odirent Linit		$V_{IN} - V_{OUT} = 25V$	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$	0.075	0.15		
1	Ouioscopt Current	5	$V_{IN} \le 20V$	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		5	10	
IQ	Quiescent Current	12	$V_{IN} \le 25V$	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		5	10	mA
	Minimum Load Current <sup>4</sup>	ADJ.	$V_{IN} - V_{OUT} = 25V$	$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$		5	10	
REG <sub>(THERM)</sub>	Thermal Regulation		$T_P = 30 ms$	T <sub>A</sub> = 25°C		0.008	0.04	%/W
	Ripple Rejection	5	$T_J = -55 \text{ to } 150^{\circ}\text{C}$	V <sub>IN</sub> = 8V	60	68		
l <sub>D</sub>		12	C <sub>OUT</sub> = 25μF Tantalum	V <sub>IN</sub> = 15V	54	60		dB
R <sub>A</sub>		ADJ.	f = 120Hz	$V_{IN} - V_{OUT} = 3V$	60 75	75		ub
			I <sub>O</sub> = 1.5A	$C_{ADJ} = 25\mu F$	60	75		
1	Adjust Din Current			,		55		
I <sub>PIN</sub> Adjust Pin Current				$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$			120	μΑ
AI	Adjust Pin Current Change		$V_{IN} - V_{OUT} = 1.5 \text{ to } 15V$			0.2	5	
$\Delta I_{PIN}$			$I_{O} = 10 \text{mA to } 1.5 \text{A}$	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		0.2		μΑ
T <sub>S</sub>	Temperature Stability		$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$			0.5		%
	Long Term Stability		T <sub>A</sub> = 125°C	T = 1000 Hrs		0.3	1	%
V <sub>N</sub>	RMS Output Noise		f = 10Hz to 10kHz	T <sub>A</sub> = 25°C		0.003		%
$R_{\theta JC}$	Thermal Resistance Junction – Case			Control Circuitry	TBA			°C/W
				Power Transistor		TBA		

#### Notes:

- 1. See thermal regulation specifications for changes in output voltage due to heating effects. Load and line regulation are measured at a constant junction temperature by low duty cycle pulse testing. Load regulation is measured at the output lead  $\approx \frac{1}{8}$ " from the package.
- 2. Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the input output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input output voltage range.
- 3. Dropout voltage is specified over the full output current range of the device.
- 4. Minimum load current is defined as the minimum output current required to maintain regulation. At 25V input-output differential the device is guaranteed to regulate if the output current is greater than 10mA.

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