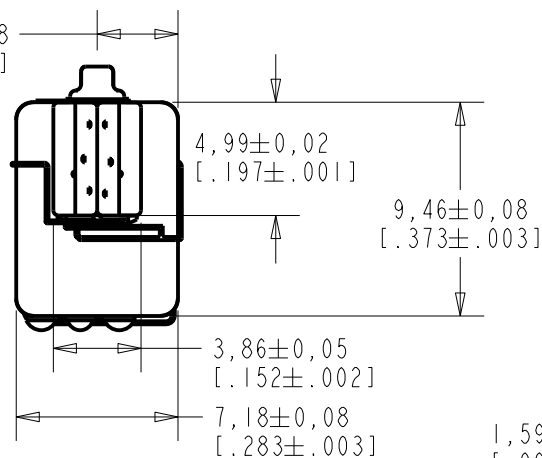


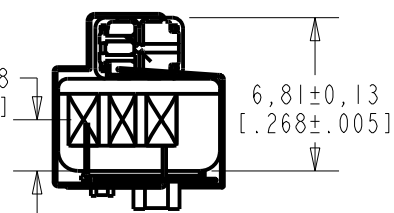
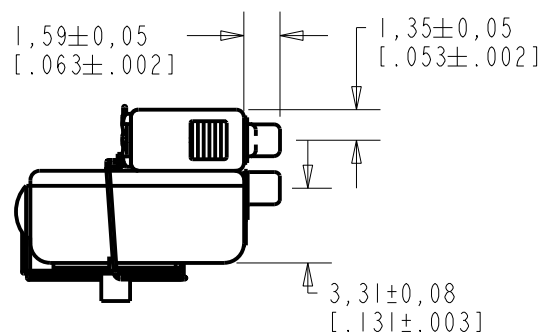
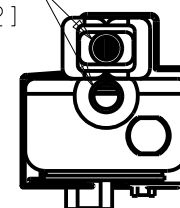
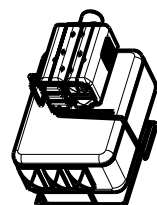
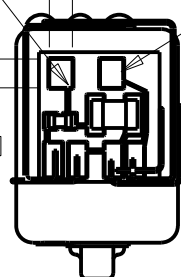
GK-31732-000

SHT 1.1

 $3,61 \pm 0,08$
[.142 ± .003]

NOTES:

A POSITIVE GOING VOLTAGE AT THE POSITIVE
TERMINAL RELATIVE TO THE NEGATIVE TERMINAL
CAUSES A DECREASE IN PRESSURE AT THE
SOUND OUTLETS.

 $2,25 \pm 0,08$
[.089 ± .003] $1,59 \pm 0,05$
[.063 ± .002] $\varnothing 1,41 \pm 0,05$
[.056 ± .002]TERMINAL
POSITIVE $1,02 \pm 0,10$
[.040 ± .004]TERMINAL
NEGATIVE $1,36 \pm 0,10$
[.054 ± .004]

SCALE 2:1

NOMINAL WEIGHT
1.26 GRAM

DIMENSIONS IN MILLIMETERS [INCHES]

KNOWLES ELECTRONICS
ITASCA, ILLINOIS U.S.A.

Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
			Active		A
A	C40101062	8-2-12			
SCALE: 3:1			DR. BY DATE		
DO NOT SCALE DRAWING			LW 8-2-12		
TITLE: RECEIVER OUTLINE DRAWING			CK. BY DATE		
			GJP 8-23-12		
			APP. BY DATE		
GK-31732-000 SHT 1.1			GJP 8-23-12		

KE111ASIZE.FRM

Rev: B

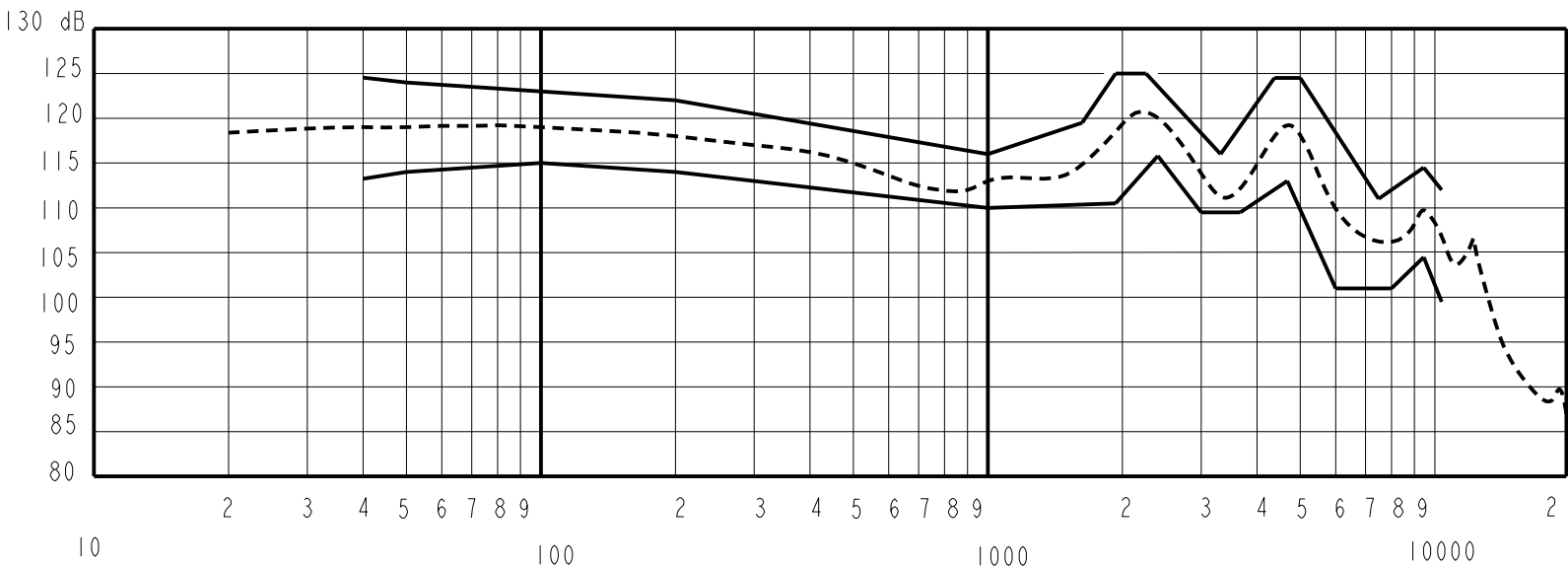
UNDAMPED MAGNETIC BALANCED RECEIVERS INTENDED FOR USE AS THE HIGH AND LOW FREQUENCY DRIVERS IN A HI-FI SYSTEM. THE GK RECEIVER COMBINES A TWFK AND C1 RECEIVERS, COUPLED THROUGH A CIRCUIT TO PROVIDE THE RESPONSE SHAPE.

NO DAMPING

CK-31732-000
SHEET 2.1

SENSITIVITY IN dB RELATIVE TO 20μPa FOR
CONDITIONS SHOWN BLEW

CONSTANT VOLTAGE DRIVE CONDITIONS (WITH VENT BLOCKED).



ACOUSTICAL

FREQUENCY IN HERTZ

SENSITIVITY
DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITION IN TABLE 3.
NOMINAL SENSITIVITY AT 1kHz IS dB RELATIVE TO 20 μPa. ALL OTHER VALUES
IN dB RELATIVE TO THE SENSITIVITY AT 1kHz.

FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
50	+1.0	+6.0	+11.0
100	+2.0	+6.0	+10.0
200	+1.0	+5.0	+9.0
1000	-3.0	113.0	+3.0
1700	---	---	+6.5
1950	-2.5	+5.5	+12.0
2300	+2.8	---	---
2450	---	---	+12.0
3000	-5.5	---	---
3350	---	---	+3.0
3700	-5.5	---	---
4400	---	---	+11.5
4700	+0.0	---	---
5000	---	---	+11.5
6000	-12.0	---	---
7500	---	---	-2.0
8000	-12.0	---	---
9750	-8.5	---	+1.5
10500	-13.5	---	-1.0

TABLE 1

TOTAL HARMONIC DISTORTION
DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (V rms)	DC BIAS (mA)	LIMIT (%)
20-8000	0.13	0	4

TABLE 2

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.13 V rms, 0mA DC BIAS
SOURCE IMPEDANCE	<1 Ohm
TUBING	TUBELESS
COUPLER CAVITY	IEC 60318-4

TABLE 3

ELECTRICAL

DC RESISTANCE	24.1 Ohms ± 10%
IMPEDANCE @ 500 Hz	23.0 Ohms ± 20%
IMPEDANCE @ 1kHz	12.0 Ohms ± 20%

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

MECHANICAL

PORT LOCATION: 12S
SOLDER TYPE: SAC305 (LEAD FREE)

TEMPERATURE:
OPERATING: SENSITIVITY AT 1kHz WILL NOT VARY
BY MORE THAN +1/-3 dB FROM
0°C TO 63°C
STORAGE: -40°C TO 63°C

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ITASCA, ILLINOIS U.S.A.

Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
			Active		A
A	C40101062	8-2-12			
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION			DR. BY LW	DATE 8-2-12	
TITLE: RECEIVER PERFORMANCE SPECIFICATION			CK. BY GJP	DATE 8-23-12	
			APP. BY GJP	DATE 8-23-12	
			CK-31732-000		
			SHT 2.1		