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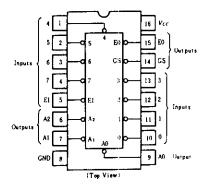
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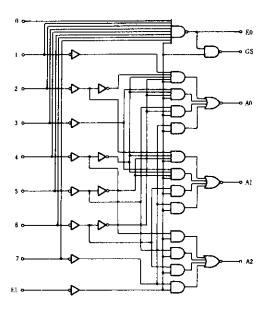
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The HD74LS148 encodes eight data lines to three-line (4-2-1) binary (octal). Cascading circuitry (enable input EI and enable output EO) has been provided to allow octal expansion without the need for external circuitry. The data inputs and outputs are active at the low logic level.

EPIN ARRANGEMENT



■BLOCK DIAGRAM



EFUNCTION TABLE

				Inputs							Outputs		
El	0	1	2	3	4	5	6	7	A2	A1	A0	GS	EO
Н	×	×	×	×	×	×	×	×	н	Н	Н	Н	Н
L	Н	Н	Н	Н	Н	н	Н	Н	Н	H	H	Н	L
L	×	×	×	×	×	×	×	L	L	L	L	L	Н
L	×	×	×	×	×	×	L	Н	L	L	Н	L	H
L	×	×	×	×	×	L	Н	Н	L	H	L	L	Н
L	×	×	×	×	L	Н	H	Н	L	H	Н	L	H
L	×	×	×	L	Н	Н	H	Н	Н	L	L	L	H
L	×	×	L	Н	Н	Н	Н	Н	Н	L	H	L	H
L	×	L	Н	Н	Н	Н	H	Н	Н	H	L	L	Н
L	L	Н	Н	н	Н	Н	H	Н	Н	Н	H	L	Н

H; high level, L; low level, X; irrelevant

ELECTRICAL CHARACTERISTICS ($Ta = -20 \sim +75^{\circ}C$)

Ite	m	Symbol	Test Condition	S	min	typ*	max	Unit
Input voltage		Vin			2.0	_	-	V
		VIL					0.8	V
Output voltage		Voн	$V_{CC} = 4.75V$, $V_{IH} = 2V$, $V_{IL} = 0.8V$. Ioн = -400μA	2.7		-	v
		Vol	$V_{CC} = 4.75 \text{V}, V_{IH} = 2 \text{V},$	IoL = 4mA			0.4	v
			$V_{IL}=0.8V$	IoL = 8mA	_	_	0.5	
	1~7 Inputs	,	$V_{CC} = 5.25 \text{V}, V_I = 2.7 \text{V}$	Ī	_		40	μA
	Other inputs	Iгн	VCC=5.25V, V1=2.1V	-	-	20	μη.	
	1~7 Inputs		$V_{CC} = 5.25 \text{V}, V_I = 0.4 \text{V}$	-	1	-0.8	mA	
Input current	Other inputs	Itt	VCC=5.25V, V1=0.4V	_	-	-0.4		
	1-7 Inputs	Iı	$V_{CC} = 5.25 \text{V}, V_{I} = 7 \text{V}$			_	0.2	mA
	Other inputs	п	VCC-5.25V, VI-1V				0.1	mA
Short-circuit output current		<i>l</i> os	$V_{CC}=5.25V$		-20	-	-100	mA
Supply current **		<i>Icc</i>	Vcc=5.25V Condition 1 Condition 2		-	12	20	
					-	10	17	mA
Input clamp volta	ıge	Vik	$V_{CC} = 4.75 \text{V}, I_{IN} = -18 \text{mA}$		_		1.5	v

^{*} VCC=5V, Ta=25°C

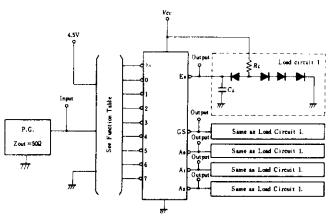
^{**} The condition 1 is measured with inputs 7 and El grounded, other inputs and outputs open, the condition 2 is measured with all inputs and outputs open.

ESWITCHING CHARACTERISTICS ($V_{CC} = 5V$, $T_a = 25^{\circ}C$)

Item	Symbol	Inputs	Outputs	Output Waveforms	Test Conditions	min	typ	max	Unit
	tplн	0~7	Ao, A1	In-phase		-	14	18	
	tphi.		A ₂	Output		_	15	25	ns
	tplh	0-7	Ao, A1	Out-of-phase	$C_L = 15 extstyle{pF}$ $R_L = 2 extstyle{k}\Omega$		20	36	
	tphi.		A ₂	Output		_	16	29	ns
	<i>tpl</i> H	0~7	EO	Out-of-phase			7	18	ns
	lPHL			Output		ļ	25	40	
D	<i>tpl</i> H	0~7	GS	In phase		-	35	55	ns
Propagation delay time	tPHL			Output			9	21	
	<i>t</i> PLH	EI	A ₀ , A ₁ or A ₂	In-phase		_	16	25	ns
	t _{PHL}			Output		-	12	25	
	tplн	EI	GS	In-phase		-	12	17	ns
	tPHL			Output			14	36	
	tplH .	EI	EO	In phase			12	21	ns
	tphl			Output		: - :	23	35	

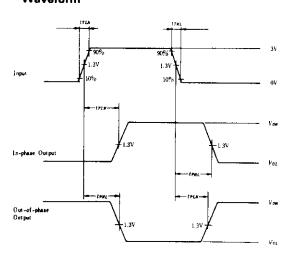
TESTING METHOD

1) Test Circuit



Notes) 1. C_L includes probe and jig capacitance. 2. All diodes are 1S2074 (H).

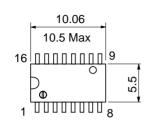
Waveform

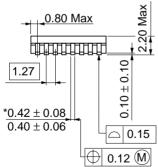


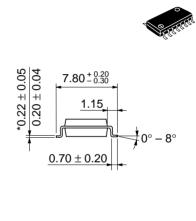
Input pulse; $t_{TLH} \leq 15 \, \mathrm{ns}$, $t_{THL} \leq 6 \, \mathrm{ns}$, $PRR = 1 \, \mathrm{MHz}$, duty cycle 50%.

Unit: mm 19.20 20.00 Max 16 7.40 Max 6.30 1.3 1.11 Max 7.62 5.06 Max 2.54 Min 0.51 Min $0.25^{+0.13}_{-0.05}$ 0.48 ± 0.10 2.54 ± 0.25 $0^{\circ} - 15^{\circ}$ Hitachi Code DP-16 **JEDEC** Conforms EIAJ Conforms Weight (reference value) 1.07 g

Unit: mm



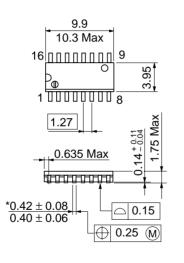


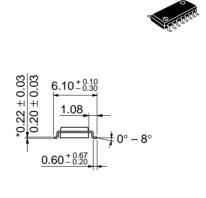


*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DA
JEDEC	_
EIAJ	Conforms
Weight (reference value)	0.24 g

Unit: mm





*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

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