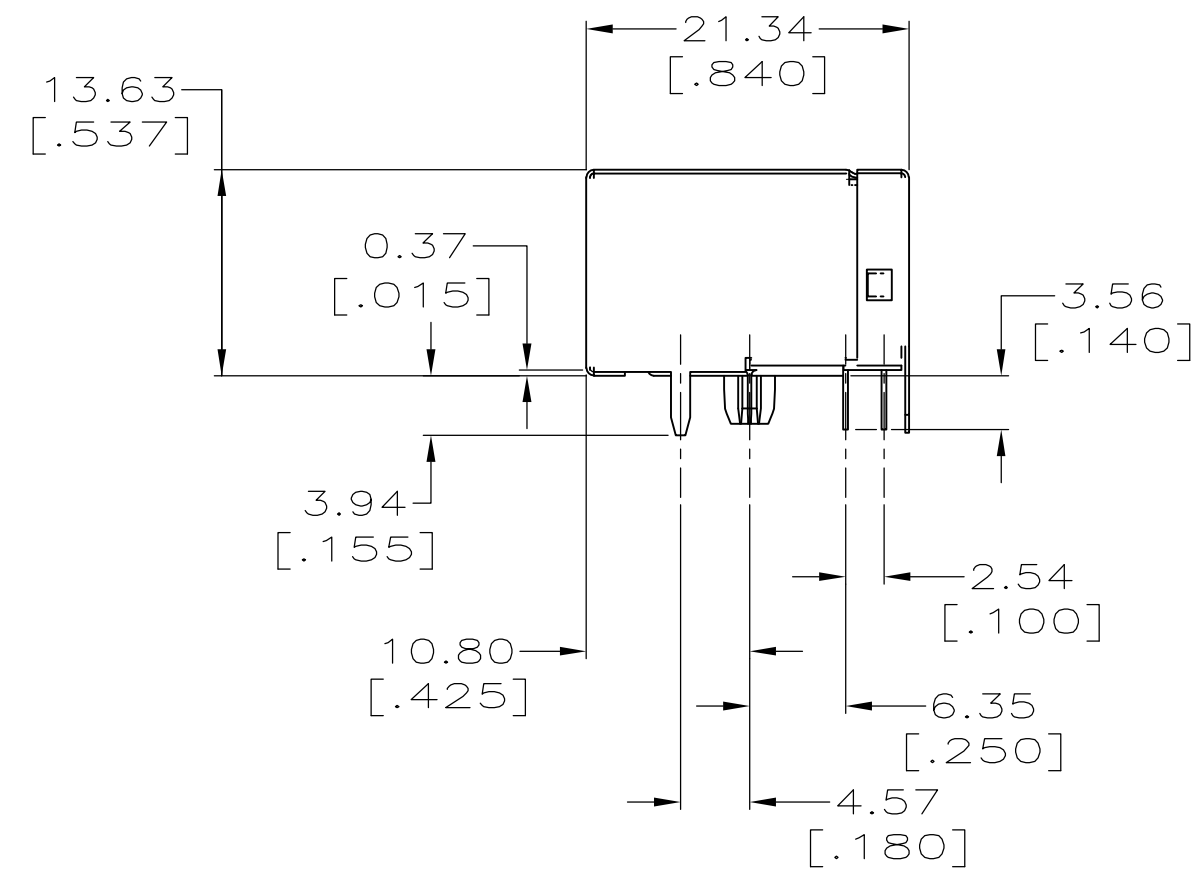
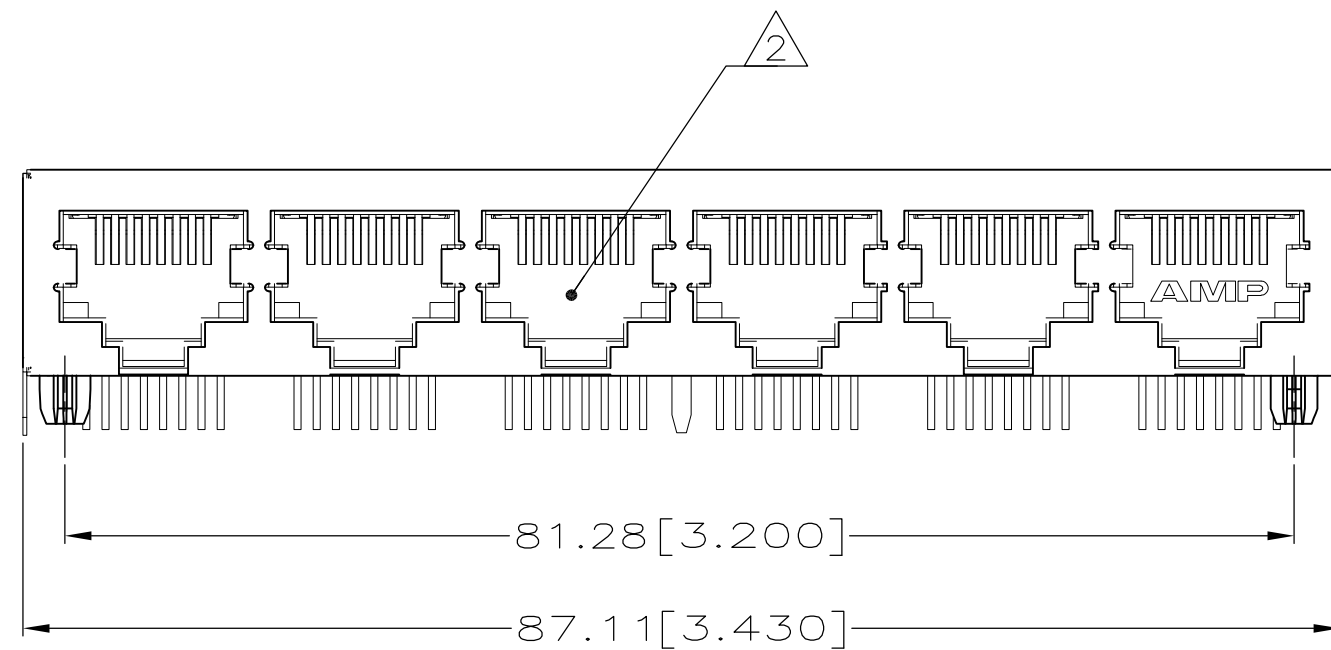


LOC	DIST	REVISIONS					
AA	22	P	LTR	DESCRIPTION	DATE	OWN	APVD
			A1	REVISED ECR-08-031628	04DEC08	AZ	SY
			A2	REVISED PER ECO-10-000444	19JAN10	KK	HMR



MATERIAL: HOUSING—PCT POLYESTER, BLACK,
IR COMPATIBLE, UL 94V-0.

TERMINALS—.013 [0.33] THICK PHOS BRONZE
PLATED WITH .000050 [1.27 μ m] THICK HARD
GOLD IN LOCALIZED AREA AND .000150 [3.81 μ m]
THICK MATTE TIN IN SOLDER AREA OVER .000050
[1.27 μ m] THICK NICKEL UNDERPLATE.

SHIELDS - 0.254 [0.010] THICK COPPER ALLOY
PLATED WITH 1.27 μ m [0.000050] MINIMUM NICKEL
AND 2.03 μ m [0.000080] MINIMUM HOT TIN DIP
ON PCB GROUND TABS.



JACK CAVITY COMFORMS TO FCC RULES AND
REGULATIONS, PART 68 SUBPART F.

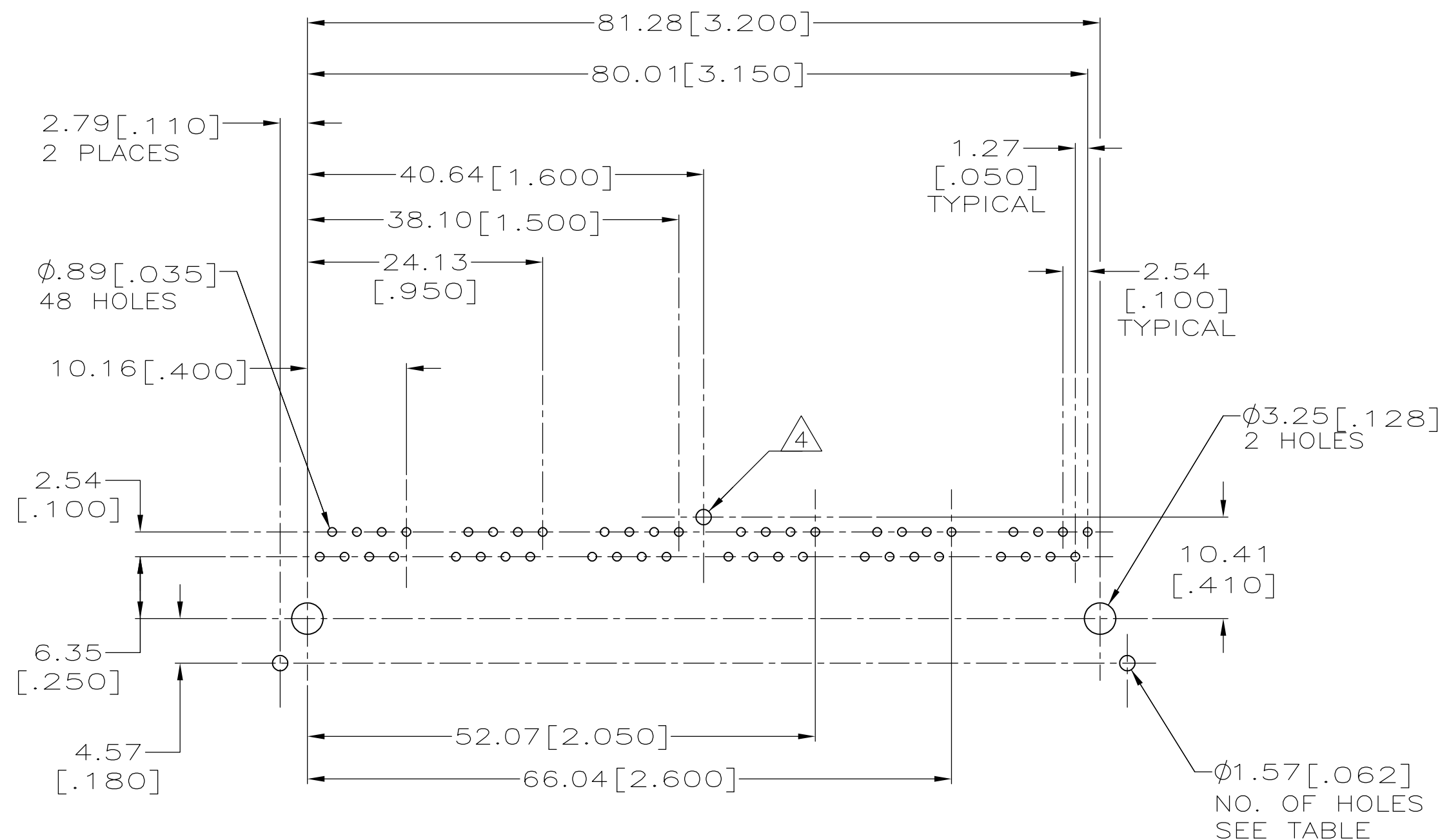
3. ALL DIMENSIONS SHOWN ARE NOMINAL UNLESS OTHERWISE SPECIFIED.



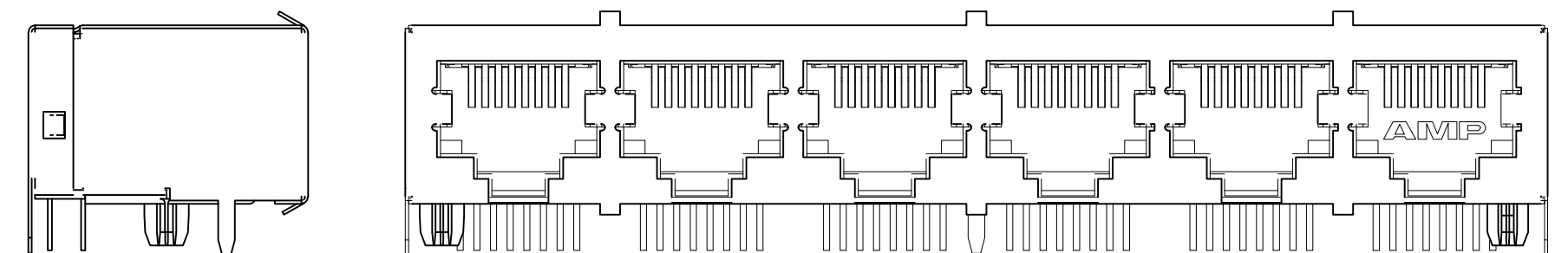
THIS HOLE IS REQUIRED FOR 5557572-1
AND 5557572-3.



OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER
D.RENAUD/D.SINISI

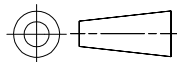


SUGGESTED PC BOARD LAYOUT
COMPONENT SIDE
SEE APPLICATION SPECIFICATION



VIEW APPLIES TO 5557572-3 ONLY

 OBSOLETE		3	PANEL GROUND SHIELD	5557572-3
		2	2 GROUND TABS	5557572-2
		3	3 GROUND TABS	5557572-1
	MATERIAL	NO. OF HOLES	DESCRIPTION	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN G. ATTADIA — DCOK5 CHK J. WESTMAN APVD S. FLICKINGER	15JUN2005 15JUN2005 15JUN2005	 Tyco Electronics Corporation Harrisburg, PA 17105-3608
DIMENSIONS: mm[inches]	TOLERANCES, UNLESS OTHERWISE SPECIFIED:	NAME MODULAR JACK ASSEMBLY, 6 PORT, 8 POSITION, RIGHT ANGLE, SHIELDED		
	0 PLC ± - 1 PLC ± - 2 PLC ± 0.25[.01] 3 PLC ± 0.13[.005] 4 PLC ± - ANGLES ± -	PRODUCT SPEC 108-1392 APPLICATION SPEC 114-2048	SIZE A2	CAGE CODE 00779
MATERIAL 	FINISH 	WEIGHT -	DRAWING NO C-5557572	RESTRICTED TO -
CUSTOMER DRAWING			SCALE 2:1	SHEET 1 OF 1
			REV A2	