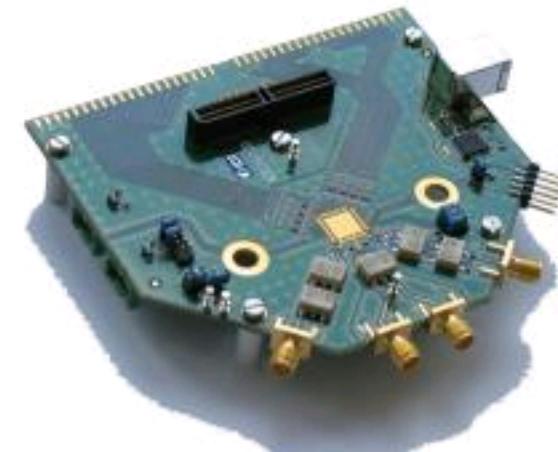


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ADC1212D125F2 ADC1212D125F2 demo board; LVDS/DDR version

IDT's dual-channel ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVDS/DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

Status: **Active New**

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Features

- SMA connector for clock and analog inputs signal
- LVDS/DDR output

Product Options

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Documents

	TITLE	TYPE	FORMAT	FILE SIZE	DATE
	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

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ADC1212D125HN	Dual 12-bit ADC; 125 Msps;

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