



- Used in SMPS Flyback Topology
- Designed for use with Power Integration Chip
- Reinforced Insulation (Triple Insulated Wire)
- UL 94V-0 Recognized Components
- UL 1446 Class F Insulation System
- Very Low Core Loss

Electrical Parameters @ 25° C

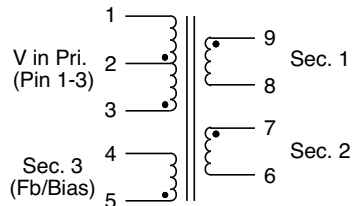
PCA Part Number	Chipset	Voltage (Vdc)					Primary OCL ($\mu\text{H} \pm 10\%$)	Current (Amp.)				Schematic
		V in Pin 1-3	V out 1	V out 2	V out 3	V out 4		Sec. 1	Sec. 2	Sec. 3	Sec. 4	
EPC3133-1	TNY253 (44 KHz)	120-375	7.5	N	N	---	5100	.173	N	N	---	A
EPC3133-2	TOP242P (132 KHz)	120-375	3.3	6.2	15	30	2100	1.5	0.4	.05	.005	B
EPC3133-3	TNY254 (44KHz)	120-375	12	N	15	---	3670	.25	N	.05	---	A
EPC3133-4	TNY266P (132 KHz)	120-375	5	N	N	---	1660	1.0	N	N	---	A
EPC3133-5	TNY267	120-375	3.3	N	15	---	2800	2.0	N	.05	---	A
EPC3133-6	TNY268 (132 KHz)	120-375	24	24	13	---	1800	0.17	0.17	.05	---	A

• Switching Frequency : 132 KHz/44 KHz • Isolation : 3750 Vrms •

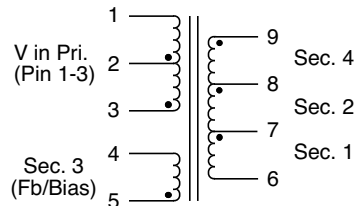
Dielectric Rating (Vdc)		
1 sec. 4500 or 60 sec. 3750	1 sec. 4500 or 60 sec. 3750	1 sec. 1500 or 60 sec. 1000
Pri. to all Sec. Wdg's	Between Sec. Wdg's to Core	Between Pri. Wdg & Bias

• Note : "N" means Not Required/No Connections •

Schematic A

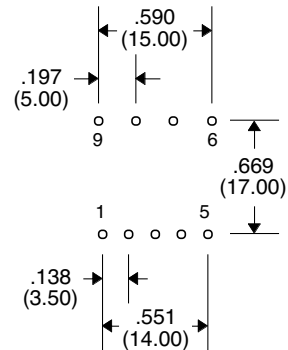
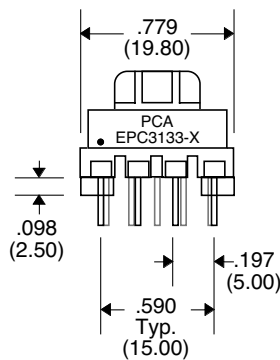
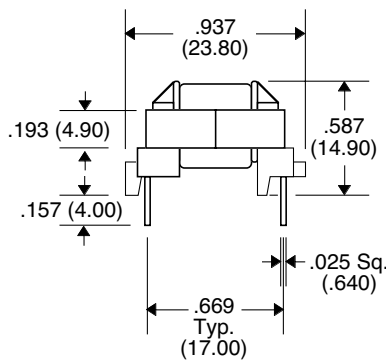


Schematic B



Shield is optional
Pin #2 is not connected in circuit

Package



Unless Otherwise Specified Dimensions are in Inches /mm $\pm .010 / .25$