

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK4037**470 MHz Band Amplifier Applications**

(Note) The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment

- Output power: $P_o = 36.5\text{dBmW}$ (typ)
- Gain: $G_p = 11.5\text{dB}$ (typ)
- Drain Efficiency: $\eta_D = 60.0\%$ (typ)

Absolute Maximum Ratings (Ta = 25°C)

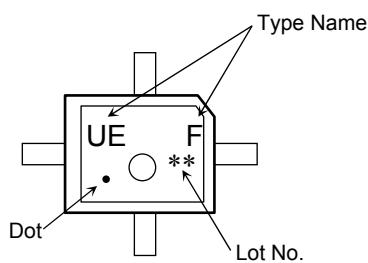
Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	12	V
Gate-source voltage	V_{GSS} (Note 1)	3	V
Drain current	I_D	3	A
Power dissipation	P_D (Note 2)	20	W
Channel temperature	T_{ch}	150	°C
Storage temperature range	T_{stg}	-45~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Operating Ranges: 0~3V

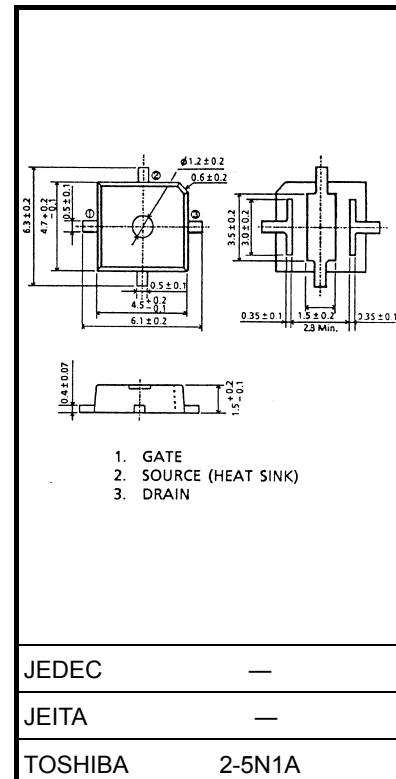
Note 2: $T_c = 25\text{°C}$ (When mounted on a 0.8 mm glass epoxy PCB)

Marking

Caution: This device is sensitive to electrostatic discharge.

Please make enough tool and equipment earthed when you handle.

Unit: mm



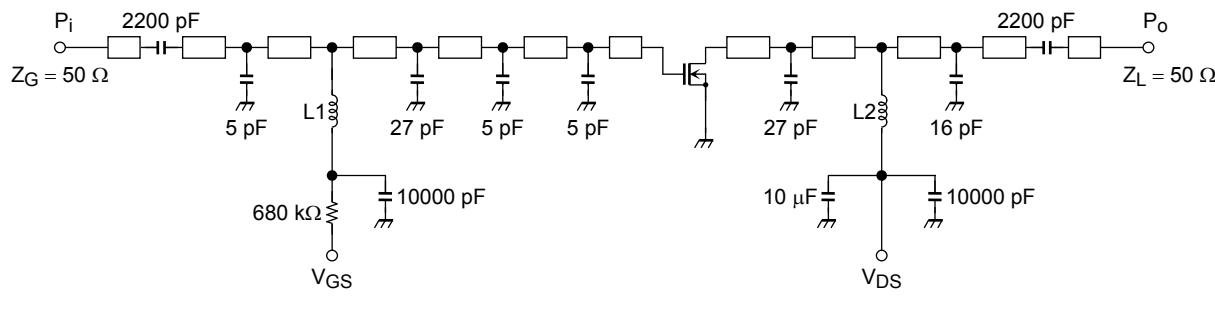
Weight: 0.08 g (typ.)

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output power	P _O	V _{DS} = 6.0 V, I _{idle} = 250 mA (V _{GS} = adjust) f = 470 MHz, P _i = 25dBmW Z _G = Z _L = 50 Ω	35.5	36.5	—	dBmW
Drain efficiency	η _D		55.0	60.0	—	%
Power gain	G _p		10.5	11.5	—	dB
Threshold voltage	V _{th}	V _{DS} = 6.0 V, I _D = 0.5 mA	—	1.0	1.5	V
Drain cut-off current	I _{DSS}	V _{DS} = 12 V, V _{GS} = 0 V	—	—	10	μA
Gate-source leakage current	I _{GSS}	V _{GS} = 3V, V _{DS} = 0 V	—	—	5	μA
Load mismatch	(Note 3)	— V _{DS} = 6.0 V, f = 470 MHz, P _i = 25dBmW, P _O = 36.5dBmW (V _{GS} = adjust) VSWR LOAD 10:1 all phase	No degradation			—

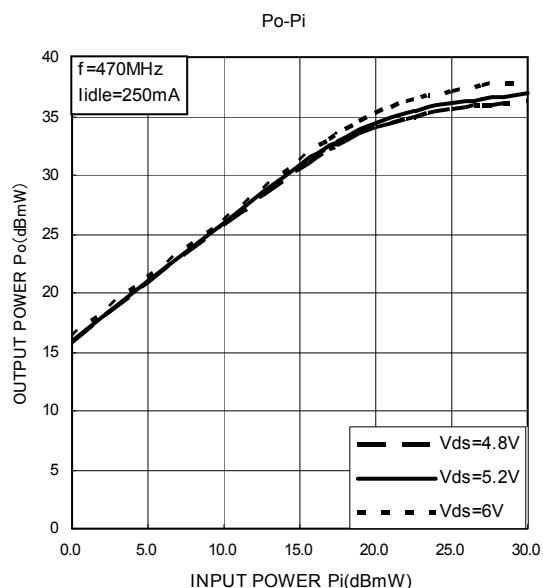
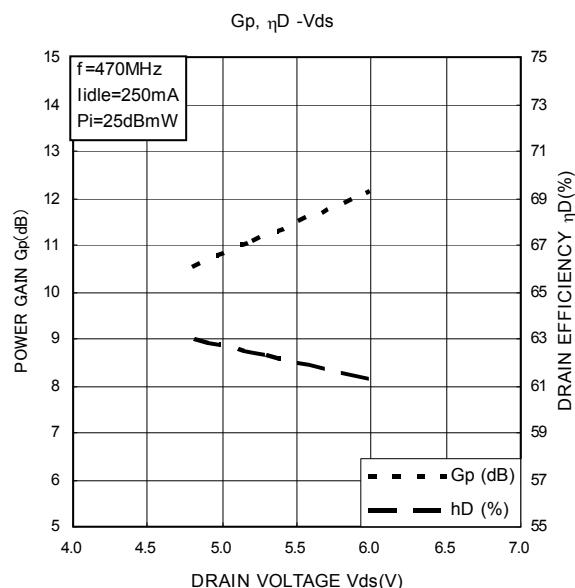
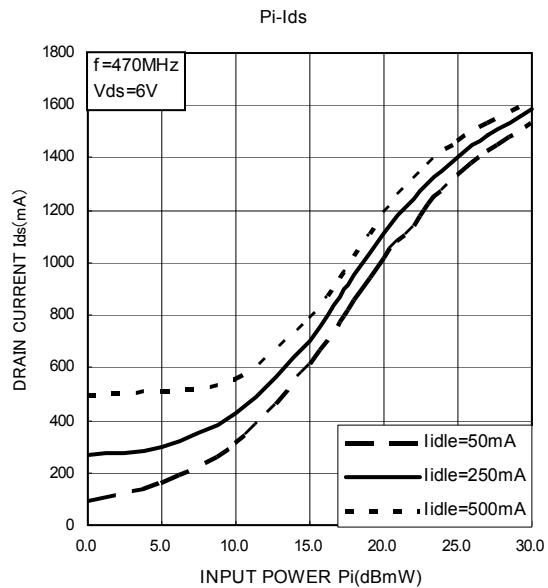
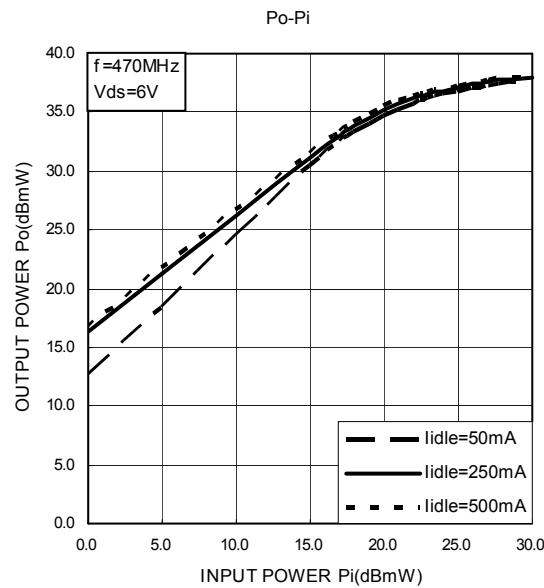
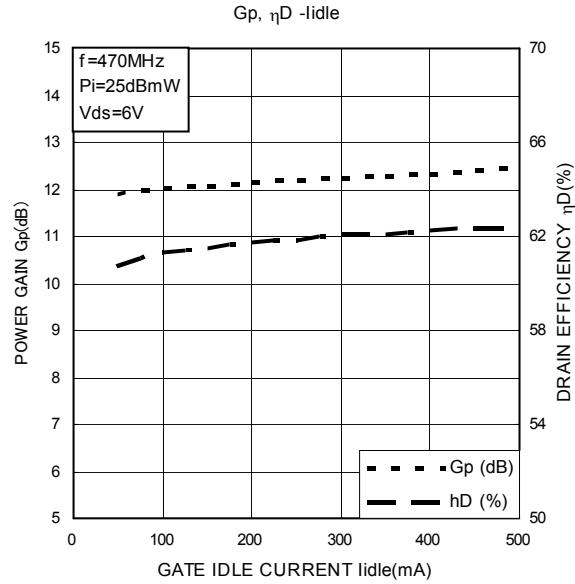
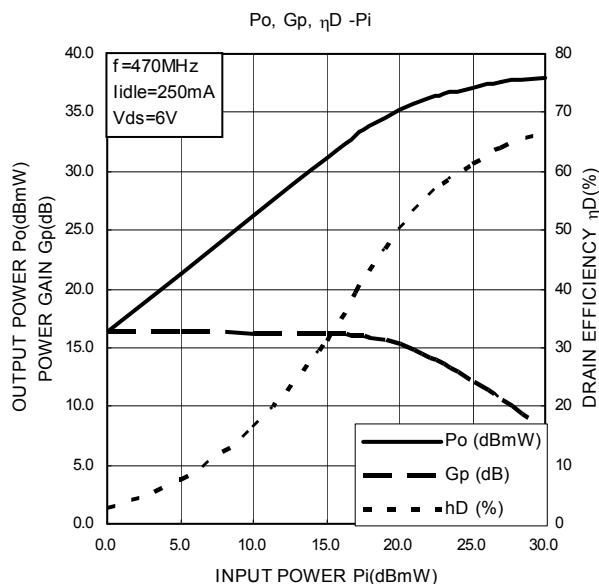
Note 3: These characteristic values are measured using measurement tools specified by Toshiba.

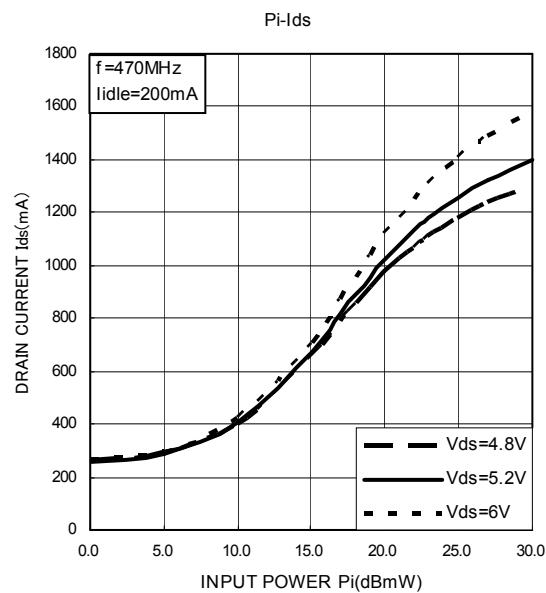
Test Circuit



L1: φ0.6 mm enamel wire, 5.8ID, 8T
L2: φ0.6 mm enamel wire, 5.8ID, 8T

Line: 2mm





Note 4: These are only typical curves and devices are not necessarily guaranteed at these curves.

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
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