TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC9243P, TC9243F

# INFRARED REMOTE CONTROL SIGNAL TRANSMISSION LSI

The TC9243P, TC9243F are infrared remote control signal transmission LSI which is suited for remote control of audio system, TV, VTR, CD player, etc.

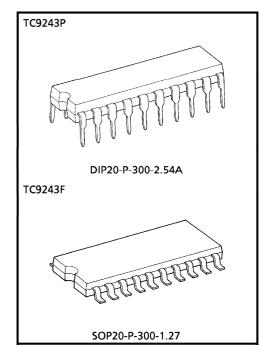
#### **FEATURES**

 Wide range of operating supply voltages, enabling low voltage operation.

$$V_{DD} = 2.0 \sim 4.0 \text{ V}$$

- 32 functions are basically available.
   Up to 112 instructions (28 x 4) can be output because multiple keying is possible.
- Interference with other equipment is prevented because 7 bits out of 8 bits of system code are presettable.
- With transmission display output pin.
- Low current dissipation. :  $I_{OD} \le 1\mu A$  (at time of standby)
- Packages in two types; DIP and flat types are available.

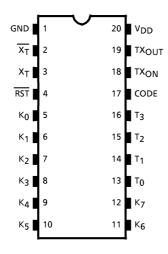
DIP20 : TC9243P SOP20 : TC9243F



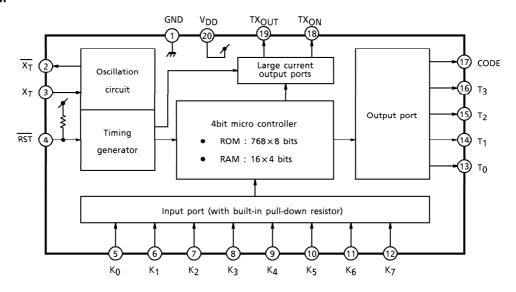
Weight

DIP20-P-300-2.54A : 1.4g (Typ.) SOP20-P-300-1.27 : 0.48g (Typ.)

# PIN CONNECTION



# **BLOCK DIAGRAM**



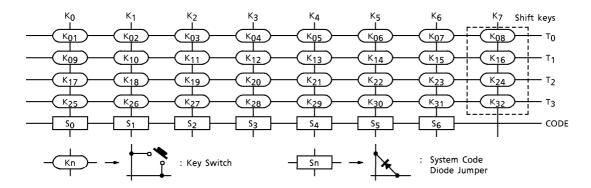
# **PIN FUNCTION**

PIN No.	SYMBOL	PIN NAME	FUNCTION AND OPERATION				
1	GND	Power Terminal	Applies supply voltage. ( $V_{DD} = 2.0 \sim 4.0 \text{V}$ )				
20	V <sub>DD</sub>	- Power Terminal					
2	<del>XT</del>		Input/output terminals for the ceramic				
3	XT	Oscillator Terminal	oscillators, with built-in amplifier circuit and feedback resistor.				
4	RST	Reset Input	When this pin is set at "L" level, the inside is initialized.  With a built-in pull-up resistor.				
5~12	K <sub>0</sub> ~K <sub>7</sub>	Key Inputs	The input terminals for Key Matrix.  Each of all the pins has a built-in pull-down resistor.				
13~16	T <sub>0</sub> ~T <sub>3</sub>	Key Scan Output	The key matrix scan output terminals. CMOS output.				
17	CODE	Code Scan Output	The scan output terminals for code setting. Pch open drain output.				
18	TXON	Transmission Display Output	Transmission display LED driving output terminal.				
19	TX <sub>OUT</sub>	Transmission Output	Infrared LED driving output terminal.				

#### **OPERATIONS**

#### 1. Key matrix

The TC9243P, TC9243F enables the setting of maximum 32keys in combination of  $K_0 \sim K_7$  and  $K_0 \sim K_7$  and  $K_0 \sim K_6$  and CODE keys.



 K<sub>08</sub>, 16, 24 and 32 (the shift key) keys can be pushed simultaneously with other keys (the normal keys).

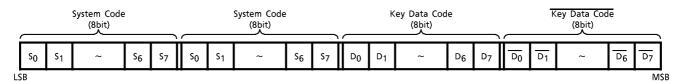
However, the simultaneous keying of the shift keys and that of the normal keys are prohibited.

• The system code setting is made by the diode jumper between "CODE" lines and "K<sub>0</sub>~K<sub>6</sub>" lines. With diode jumper, data code will become "1".

However, if the setting of "CODE" and " $K_0 \sim K_6$ " keys is only at one point, the keys are connectable directly without using the diode jumper.

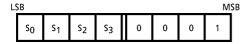
Further, "S7" key is fixed at "1" and cannot be changed.

#### 2. Data format



(Note) "80H~8FH" out of the system codes are free codes.

These codes are freely usable but they might have been used by other equipment and therefore, there is the possibility of occurrence of interference.



Other system codes have been customized and therefore, their general use is prohibited. TOSHIBA will assume no responsibility for interference and other problems which may result from use of other system coded.

# 3. Key data code

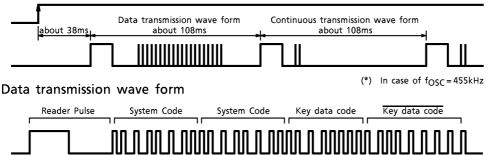
KEY No.	Tn	Kn	D <sub>0</sub>	D <sub>1</sub>	D <sub>2</sub>	D <sub>3</sub>	D <sub>4</sub>	D <sub>5</sub>	D <sub>6</sub>	D <sub>7</sub>			
K <sub>01</sub>	T <sub>0</sub>	K <sub>0</sub>	1	0	0	0	0		·				
K <sub>02</sub>		K <sub>1</sub>	0	1	0	0	0		Shift key data				
K <sub>03</sub>		K <sub>2</sub>	1	1	0	0	0	Shift k					
K <sub>04</sub>		T <sub>0</sub>	К3	0	0	1	0	0					
K <sub>05</sub>			'0	К4	1	0	1	0	0	<ul> <li>"000" except dual keying</li> </ul>			
K <sub>06</sub>			K <sub>5</sub>	0	1	1	0	0					
K <sub>07</sub>		К6	1	1	1	0	0						
K <sub>08</sub>		K <sub>7</sub>		key data " except du	ual keying.	1	0	0					
K <sub>09</sub>		K <sub>0</sub>	1	0	0	1	0		•	•			
K <sub>10</sub>		K <sub>1</sub>	0	1	0	1	0	1	Shift key data				
K <sub>11</sub>		K <sub>2</sub>	1	1	0	1	0	Chiff k					
K <sub>12</sub>	<b> </b>	К3	0	0	1	1	0		=				
K <sub>13</sub>	T <sub>1</sub>	К4	1	0	1	1	0	000	"000" except dual keying				
K <sub>14</sub>		K <sub>5</sub>	0	1	1	1	0	1					
K <sub>15</sub>		К6	1	1	1	1	0						
K <sub>16</sub>		K <sub>7</sub>		key data " except du	ual keying.			1 1 0					
K <sub>17</sub>		K <sub>0</sub>	1	0	0	0	1		Chiff how data				
K <sub>18</sub>		K <sub>1</sub>	0	1	0	0	1						
K <sub>19</sub>		K <sub>2</sub>	1	1	0	0	1	م دام:دید اد					
K <sub>20</sub>	 	К3	0	0	1	0	1		Shift key data				
K <sub>21</sub>	T <sub>2</sub>	K <sub>4</sub>	1	0	1	0	1	• 000	except dual keying				
K <sub>22</sub>		K <sub>5</sub>	0	1	1	0	1						
K <sub>23</sub>		K <sub>6</sub>	1	1	1	0	1						
K <sub>24</sub>		K <sub>7</sub>		key data " except du	ual keying.		1	0	1				
K <sub>25</sub>		K <sub>0</sub>	1	0	0	1	1						
K <sub>26</sub>		K <sub>1</sub>	0	1	0	1	1	1	Shift key data				
K <sub>27</sub>		K <sub>2</sub>	1	1	0	1	1	a chicu l					
K <sub>28</sub>	Т3	K <sub>3</sub>	0	0	1	1	1						
K <sub>29</sub>		K <sub>4</sub>	1	0	1	1	1	1 · · · · · · · · · · · · · · · · · · ·	O" except dual keying.				
K <sub>30</sub>		K <sub>5</sub>	0	1	1	1	1	1					
K <sub>31</sub>		K <sub>6</sub>	1	1	1	1	1						
K <sub>32</sub>		K <sub>7</sub>		key data " except du	ual keying.			1	1	1			

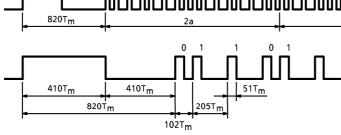
 $\bullet \quad \text{Normal key} \, : \, \mathsf{K}_{01} {\sim} \mathsf{K}_{07}, \, \mathsf{K}_{09} {\sim} \mathsf{K}_{15}, \, \mathsf{K}_{17} {\sim} \mathsf{K}_{23}, \, \mathsf{K}_{25} {\sim} \mathsf{K}_{31}$ 

• Shift key : K<sub>08</sub>, K<sub>16</sub>, K<sub>24</sub>, K<sub>32</sub>

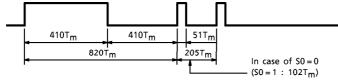
# 4. TXOUT output wave form

#### Key ON

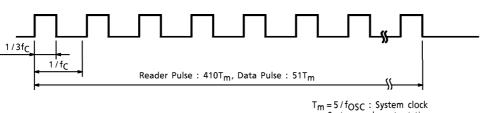




#### Continuous transmission wave form



# Carrier wave form



 $T_m = 5 / f_{OSC}$ : System clock a: System code output time  $f_C = f_{OSC} / 12$ 

When oscillation frequency is 455kHz, signal is output after pulse modulated by 37.9kHz of duty 1/3, which is a 1/12 division by the carrier generation circuit.

(Caution) In preparing a receiving software, strictly follow the following instructions :

- In case of system codes, the same code is transmitted twice and therefore, always decode these 2 codes and judge if they agree with each other.
- In case of key data codes, always decode a key data code and its reversed code and judge if they agree with each other.

# **MAXIMUM RATINGS** (Ta = $25^{\circ}$ C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	$V_{DD}$	-0.3~5.0	V
Input Voltage	VIN	$VSS - 0.3 \sim V_{DD} + 0.3$	V
Output Current	IOUT	<b>– 20</b>	mA
Power Dissipation	PD	350 (300) 💥	mW
Operating Temperature	T <sub>opr</sub>	<b>- 20∼75</b>	°C
Storage Temperature	T <sub>stg</sub>	<b>- 40∼125</b>	°C

<sup>\*</sup> The value shown in parentheses applies to the TC9243F.

# **ELECTRICAL CHARACTERISTICS**

Recommended operating conditions

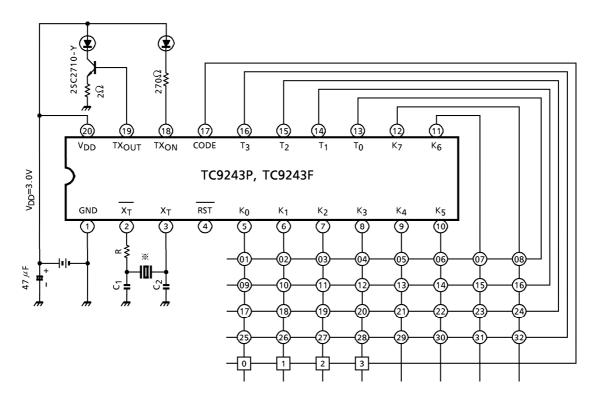
(Unless otherwise specified,  $V_{DD} = 3.0V$ ,  $Ta = 25^{\circ}C$ , For items with an asterisk %,  $Ta = -20 \sim 75^{\circ}C$ )

CHARACT	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT		
Operating Suppl	$V_{DD}$	_	_	2.0	_	4.0	V		
Oscillation Frequency 💥		fosc	_	_	400	_	800	kHz	
Input Voltage	"H" Level	V <sub>IH1</sub>	_	(Except RST)	V <sub>DD</sub> ×0.7		V <sub>DD</sub>	V	
	"L" Level	V <sub>IL1</sub>	_	(Except RST)	0	_	V <sub>DD</sub> ×0.3	<b>V</b>	
Input Voltage	"H" Level	V <sub>IH2</sub>	_	(RST)	V <sub>DD</sub> ×0.8		V <sub>DD</sub>	V	
	"L" Level	V <sub>IL2</sub>	_	(RST)	0	1	V <sub>DD</sub> ×0.2	<b>v</b>	

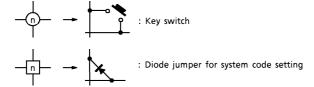
# DC characteristics (Unless otherwise specified, $V_{DD} = 3.0V$ , Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Operating Supply Current		lDD	_	f <sub>OSC</sub> = 455kHz	1	ı	1.0	mA	
Static Supply Current		lQD	_	At time of "Hold"	_	_	1.0	$\mu$ A	
Pull-down Resistor		R <sub>D</sub>	_	(K <sub>0</sub> ~K <sub>7</sub> )	100	_	400	kΩ	
Pull-up Resistor		RU	_	(RST)	25	_	100	kΩ	
Output Current	"H" Level	Іон	_	$(TX_{OUT})$ $V_{OH} = 1.5V$	- 10	_	_	mA	
	"L" Level	lOL	_	$(TX_{ON})$ $V_{OL} = 1.5V$	5	1	_		
Input Leak Current		ILI		$V_{IN} = V_{DD}$ , $V_{SS}$	- 1.0		1.0	$\mu$ A	

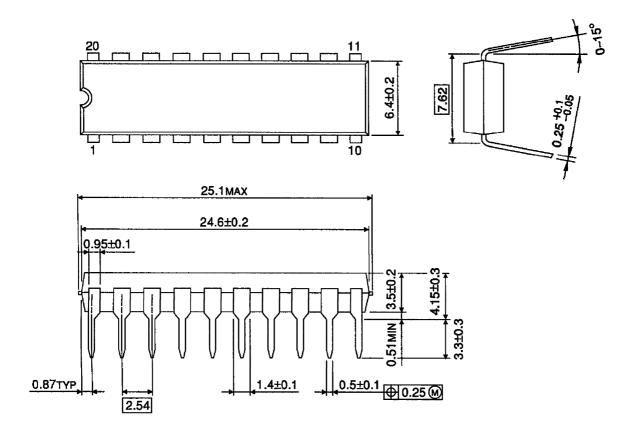
# **APPLICATION CIRCUIT**



% Ceramic Oscillator CSB455E (Murata Seisakusho)  $C_1=C_2=100 pF$   $R=6.8k\Omega$  FCR455K3 (TDK)  $C_1=C_2=220 pF$   $R=2.2k\Omega$  or equivalent



# **PACKAGE DIMENSIONS**

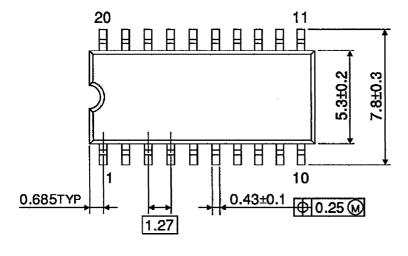


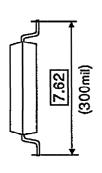
Weight: 1.4g (Typ.)

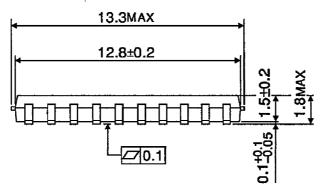
# **PACKAGE DIMENSIONS**

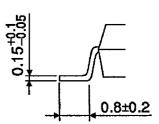
SOP20-P-300-1.27

Unit: mm









Weight: 0.48g (Typ.)

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