

BUL116D

MEDIUM VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

PRELIMINARY DATA

- INTEGRATED ANTIPARALLEL COLLECTOR- EMITTER DIODE
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED

APPLICATIONS:

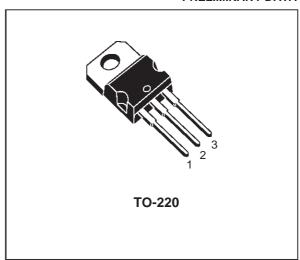
- COMPACT FLUORESCENT LAMPS UP TO 23 W AT 110 V A.C. MAINS
- FLYBACK AND FORWARD SINGLE TRANSISTOR LOW POWER CONVERTERS AT 110 V A.C. MAINS

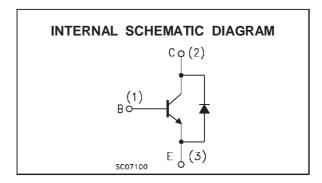
DESCRIPTION

The device is manufactured using Multi Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CES}	Collector-Emitter Voltage (V _{BE} = 0)	400	V
V _{CEO}	Collector-Emitter Voltage (I _B = 0)	200	V
V _{EBO}	Emitter-Base Voltage (I _C = 0)	9	V
Ic	Collector Current	5	А
I _{CM}	Collector Peak Current (t _p < 5 ms)	10	А
lв	Base Current	2	А
I_{BM}	Base Peak Current (t _p < 5 ms)	4	А
P _{tot}	Total Dissipation at T _c = 25 °C	60	W
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

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THERMAL DATA

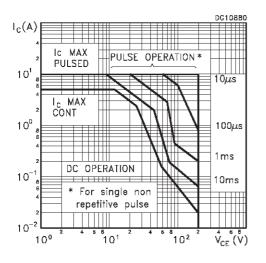
R	R _{thj-case}	Thermal Res	istance	Junction-Case	Max	2.08	°C/W
F	R _{thj-amb}	Thermal Res	sistance	Junction-Ambient	Max	62.5	°C/W

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

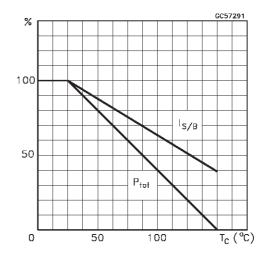
Symbol Parameter		Test Co	onditions	Min.	Min. Typ. Max.		Unit
I _{CES}	Collector Cut-off Current (V _{BE} = 0)	V _{CE} = 400 V V _{CE} = 400 V	T _j = 125 °C			100 500	μΑ μΑ
V _{EBO}	Emitter-Base Voltage (I _C = 0)	I _E = 10 mA		9			V
V _{CEO(sus)} *	Collector-Emitter Sustaining Voltage (I _B = 0)	I _C = 100 mA	L = 25 mH	200			V
I _{CEO}	Collector-Emitter Leakage Current	V _{CE} = 200 V				250	μА
V _{CE(sat)} *	Collector-Emitter Saturation Voltage	I _C = 0.5 A I _C = 1 A I _C = 3 A I _C = 5 A	$I_B = 50 \text{ mA}$ $I_B = 0.1 \text{ A}$ $I_B = 0.6 \text{ A}$ $I_B = 1 \text{ A}$			0.25 0.4 0.7 1.2	V V V
V _{BE(sat)} *	Base-Emitter Saturation Voltage	I _C = 1 A I _C = 5 A	I _B = 0.1 A I _B = 1 A			1.1 1.5	V V
h _{FE} *	DC Current Gain	I _C = 10 mA I _C = 5 A	V _{CE} = 5 V V _{CE} = 5 V	10 8		20	
t _r t _f t _s	RESISTIVE LOAD Rise Time Fall Time Storage Time	$V_{CC} = 125 \text{ V}$ $I_{B1} = 0.4 \text{ A}$ $t_p = 300 \mu\text{s}$	$I_C = 2 A$ $I_{B2} = -0.4 A$ (see figure 2)		0.2 0.2 1.4	0.4	μs μs μs
t _s	INDUCTIVE LOAD Storage Time Fall Time	I _C = 2 A V _{BE} = -5 V V _{clamp} = 180 V	$I_{B1} = 0.4 \text{ A}$ $L = 500 \mu\text{H}$ (see figure 1)		0.5 0.10		μs μs
V _F	Diode Forward Voltage	I _C = 2 A				1.5	V

^{*} Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %

Safe Operating Area

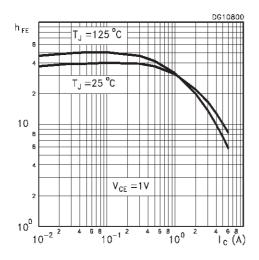


Derating Curve

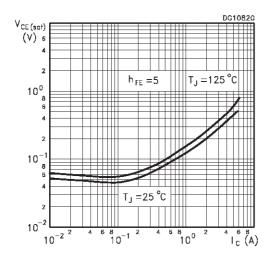


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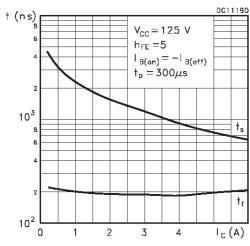
DC Current Gain



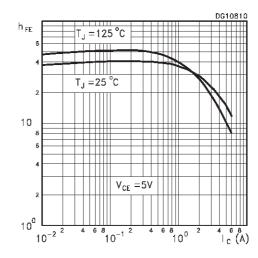
Collector-Emitter Saturation Voltage



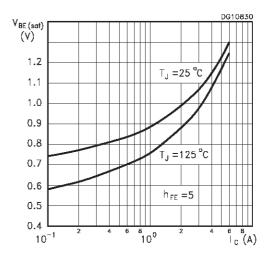
Switching Time Resistive Load



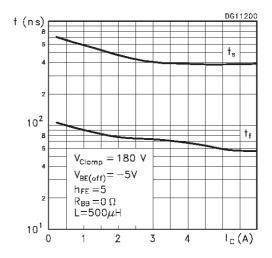
DC Current Gain



Base-Emitter Saturation Voltage



Switching Time Inductive Load



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Reverse Biased SOA

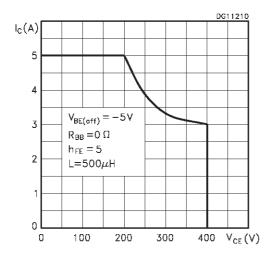


Figure 1: Inductive Load Switching Test Circuit.

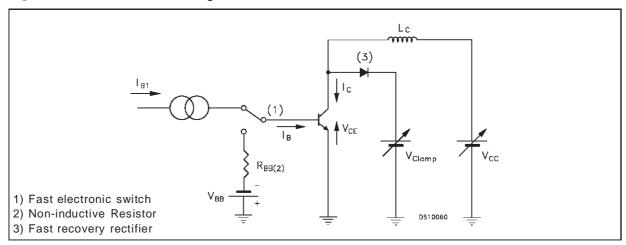
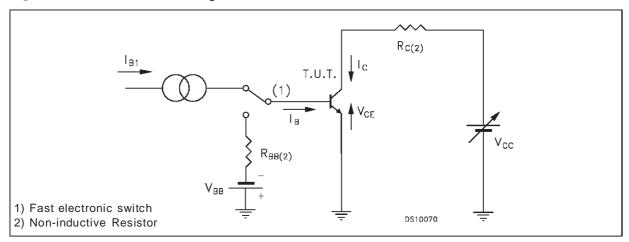


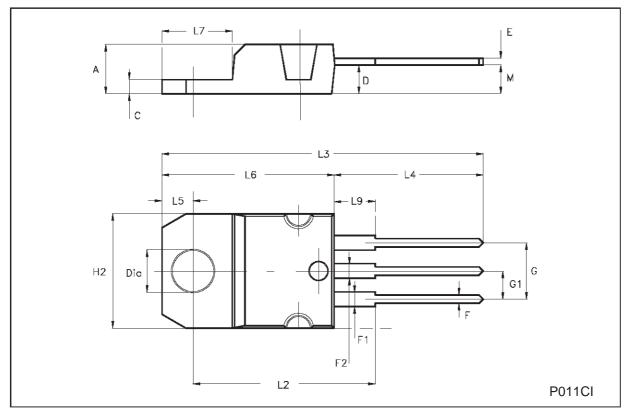
Figure 2: Resistive Load Switching Test Circuit.



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TO-220 MECHANICAL DATA

DIM.	mm			inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А	4.40		4.60	0.173		0.181	
С	1.23		1.32	0.048		0.052	
D	2.40		2.72	0.094		0.107	
Е	0.49		0.70	0.019		0.027	
F	0.61		0.88	0.024		0.034	
F1	1.14		1.70	0.044		0.067	
F2	1.14		1.70	0.044		0.067	
G	4.95		5.15	0.194		0.202	
G1	2.40		2.70	0.094		0.106	
H2	10.00		10.40	0.394		0.409	
L2		16.40			0.645		
L4	13.00		14.00	0.511		0.551	
L5	2.65		2.95	0.104		0.116	
L6	15.25		15.75	0.600		0.620	
L7	6.20		6.60	0.244		0.260	
L9	3.50		3.93	0.137		0.154	
М		2.60			0.102		
DIA.	3.75		3.85	0.147		0.151	



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