

Fixed Negative Voltage Regulators

Mfr.'s Type				Output Current (A)	Output Voltage (V)	Max. Input Voltage* (V)	No. of Leads	Temperature Range (°C)
Metal Can	SOIC	TO-220	TO-92					
—	—	LM7905CT	—	1.5	−5.0	−35	T3	0 to +125
—	—	LM7912CT	—	1.5	−12.0	−35	T3	0 to +125
—	—	LM7915CT	—	1.5	−15.0	−35	T3	0 to +125
LM320K-15†	—	LM320T-15	—	1.5	−15.0	−25	K2, T3	0 to +125
—	—	LM320T-12	—	1.5	−12.0	−35	T3	0 to +125
—	—	LM320T-5.0	—	1.5	−5.0	−35	T3	0 to +125
—	—	LM79M05CT	—	0.5	−5.0	−30	T3	0 to +125
—	—	LM79M12CT	—	0.5	−12.0	−35	T3	0 to +125
—	—	LM79M15CT	—	0.5	−15.0	−35	T3	0 to +125
LM120H-5.0	—	—	—	0.5	−5.0	−25	H3	−55 to +150
LM320H-5.0	—	—	—	0.5	−5.0	−25	H3	0 to +125
—	LM79L05ACM	—	LM79L05ACZ	0.1	−5.0	−35	M8, Z3	0 to +125
—	—	—	LM79L12ACZ	0.1	−12.0	−35	Z3	0 to +125
—	LM79L15ACM	—	—	0.1	−15.0	−35	M8	0 to +125
—	—	—	LM320LZ-12	0.1	−12.0	−35	Z3	0 to +125
—	—	—	LM320LZ-5.0	0.1	−5.0	−35	Z3	0 to +125

*Minimum Input Voltage for Fixed Negative Voltage Regulators. †TO-3 Package.

Precision Motion Control Circuits

Mfr.'s Type	Features	Speed (MHz)	No. of Leads	Temperature Range (°C)
PDIP				
LM628N-6	32-Bit Position, Velocity and Acceleration Registers; Position and Velocity Modes; 16-Bit PID Filter with Programmable Coefficients; 8- or 12-Bit DAC Output Data; Quadrature Incremental Encoder Interface; 8-Bit Asynchronous Host Interface	6	N28	−40 to +85
LM629N-6	32-Bit Position, Velocity and Acceleration Registers; Position and Velocity Modes; 16-Bit PID Filter with Programmable Coefficients; 8-Bit PWM Sign/Magnitude Output Data; Quadrature Incremental Encoder Interface; 8-Bit Asynchronous Host Interface	6	N28	−40 to +85
LM629N-8	32-Bit Position, Velocity and Acceleration Registers; Position and Velocity Modes; 16-Bit PID Filter with Programmable Coefficients; 8-Bit PWM Sign/Magnitude Output Data; Quadrature Incremental Encoder Interface; 8-Bit Asynchronous Host Interface	8	N28	−40 to +85

Data Transmission Interface Line Drivers, Receivers and Transceivers

TIA/EIA-232 (RS-232)

Mfr.'s Type		Description	No. of Tx	No. of Rx	No. of Leads	Temperature Range (°C)
SOIC	MDIP					
—	DS1488N	Quad Line Driver	4	0	14	0 to +70
DS14C88M	DS14C88N	Quad CMOS Line Driver	4	0	14	0 to +70
DS1489AM	DS1489AN	Quad Line Receiver	0	4	14	0 to +70
—	DS1489N	Quad Line Receiver	0	4	14	0 to +70
—	DS14C89AN	Quad CMOS Line Receiver	0	4	14	0 to +70
DS14C232CM	DS14C232CN	Dual Driver/Receiver	2	2	16	0 to +70

13

TIA/EIA-423 (RS-423)

—	DS9636ACN	Dual Programmable Slew Rate Driver	2	0	8	0 to +70
—	DS3631N	Quad Tri-State Line Driver	4	0	16	0 to +70

TIA/EIA-422 Drivers (RS-422) and TIA/EIA-422/423 Receivers (RS-422/423)

DS26LS31CM	DS26LS31CN	Quad High Speed Differential Line Driver	4	0	16	0 to +70
DS3487M	DS3487N	Quad Tri-State Line Driver	4	0	16	0 to +70
—	DS9638CN	Dual High Speed Differential Line Driver	2	0	8	0 to +70
DS26C32ATM	—	Quad CMOS Differential Line Receiver	0	4	16	−40 to +85
—	DS26LS32ACN	Quad Differential Line Receiver	0	4	16	0 to +70
—	DS26LS32CN	Quad Differential Line Receiver	0	4	16	0 to +70
DS3486M	DS3486N	Quad Differential Line Receiver	0	4	16	0 to +70
—	DS88C20N	Dual CMOS Compatible Differential Line Receiver	0	2	14	0 to +70
DS9637ACM	DS9637ACN	Dual Differential Line Receiver	0	2	8	0 to +70
—	DS8921AN	Differential Line Driver and Receiver Pair	1	1	8	0 to +70
DS8921M	DS8921N	Differential Line Driver and Receiver Pair	1	1	8	0 to +70
—	DS8922AN	Dual Tri-State Differential Line Driver and Receiver Pair	2	2	16	0 to +70
—	DS8922N	Dual Tri-State Differential Line Driver and Receiver Pair	2	2	16	0 to +70
DS8923AM	DS8923AN	Dual Tri-State Differential Line Driver and Receiver Pair	2	2	16	0 to +70

TIA/EIA-485 (RS-485)

—	DS96172CN	Quad Differential Line Driver	4	0	16	0 to +70
—	DS96174CN	Quad Differential Line Driver	4	0	16	0 to +70
—	DS96175CN	Quad Differential Line Receiver	0	4	16	0 to +70
DS3695AM	—	Multipoint Transceiver	1	1	8	—
—	DS3695N	Multipoint Transceiver	1	1	8	0 to +70
—	DS3695TN	Multipoint Transceiver	1	1	8	−40 to +85
—	DS3696N	Multipoint Transceiver	1	1	8	0 to +70
DS75176BM	DS75176BN	Multipoint Transceiver	1	1	8	0 to +70
—	DS96176CN	Differential Bus Transceiver	1	1	8	0 to +70