

CD4000M/CD4000C Dual 3-Input NOR Gate Plus Inverter

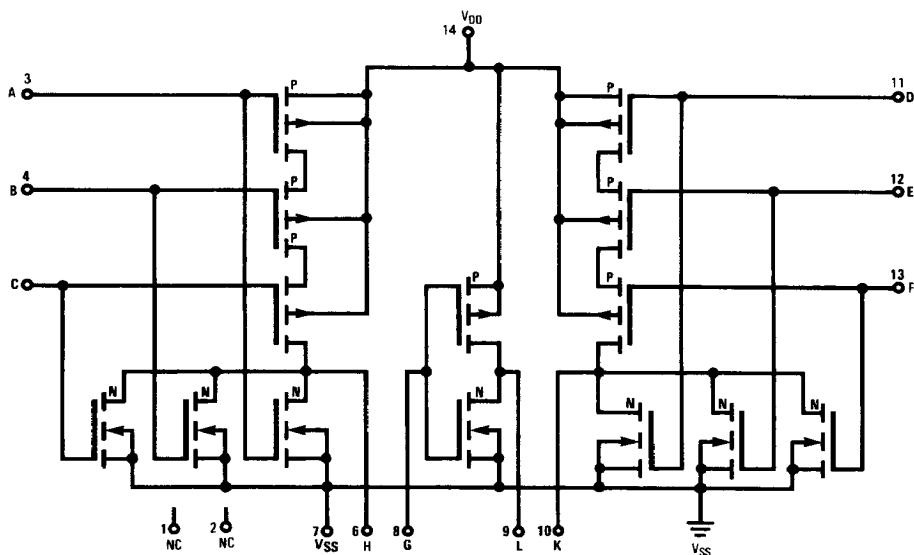
General Description

The CD4000M/CD4000C is a monolithic complementary MOS (CMOS) dual 2-input NOR gate plus an inverter. N- and P-channel enhancement mode transistors provide a symmetrical circuit with output swings essentially equal to the supply voltage. This results in high noise immunity over a wide supply voltage range. No DC power other than that caused by leakage current is consumed during static conditions. All inputs are protected against static discharge.

Features

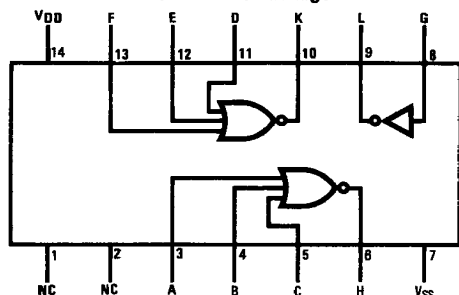
- Wide supply voltage range 3.0V to 15V
- Low power 10 nW (typ.)
- High noise immunity 0.45 V_{DD} (typ.)

Schematic and Connection Diagrams



TL/F/5937-1

Dual-In-Line Package



Top View

TL/F/5937-2

Order Number CD4000*

*Please look into Section 8, Appendix D for availability of various package types.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Voltage at any Pin $V_{SS} - 0.3V$ to $V_{DD} + 0.3V$

Operating Temperature Range (T_A)

CD4000M

$-55^{\circ}C$ to $+125^{\circ}C$

CD4000C

$-40^{\circ}C$ to $+85^{\circ}C$

Storage Temperature Range (T_S)

$-65^{\circ}C$ to $+150^{\circ}C$

Power Dissipation (P_D)

Dual-In-Line

700 mW

Small Outline

500 mW

Operating V_{DD} Range

$V_{SS} + 3V$ to $V_{SS} + 15V$

Lead Temperature (T_L)

(Soldering, 10 seconds)

$260^{\circ}C$

DC Electrical Characteristics CD4000M (Note 2)

Symbol	Parameter	Conditions	$-55^{\circ}C$		$+25^{\circ}C$			$+125^{\circ}C$		Units
			Min	Max	Min	Typ	Max	Min	Max	
I_{DD}	Quiescent Device Current	$V_{DD} = 5V$ $V_{DD} = 10V$		0.05 0.1			0.05 0.1		3 6	μA μA
V_{OL}	Low Level Output Voltage	$V_{DD} = 5V$ $V_{DD} = 10V$		0.05 0.05			0.05 0.05		0.05 0.05	V V
V_{OH}	High Level Output Voltage	$V_{DD} = 5V$ $V_{DD} = 10V$	4.95 9.95		4.95 9.95			4.95 9.95		V V
V_{NL}	Noise Immunity (Note 3)	$V_{DD} = 5V, V_O = 1.4V$ or $3.6V$ $V_{DD} = 10V, V_O = 2.8V$ or $7.2V$	1.5 3.0		1.5 3.0			1.4 2.9		V V
V_{NH}	Noise Immunity (Note 3)	$V_{DD} = 5V, V_O = 1.4V$ or $3.6V$ $V_{DD} = 10V, V_O = 2.8V$ or $7.2V$	1.4 2.9		1.5 3.0			1.5 3.0		V V
I_{DN}	Low Level Output Current (Note 4)	$V_{DD} = 5V, V_O = 0.4V$ $V_{DD} = 10V, V_O = 0.5V$	0.5 1.1		0.4 0.9			0.28 0.65		mA mA
I_{DP}	High Level Output Current (Note 4)	$V_{DD} = 5V, V_O = 2.5V$ $V_{DD} = 10V, V_O = 9.5V$	-0.62 -0.62		-0.5 -0.5			-0.35 -0.35		mA mA
I_{IN}	Input Current	$V_{DD} = 15V, V_{IN} = 0V$ $V_{DD} = 15V, V_{IN} = 15V$	-1.0	1.0	-0.1	-10^{-5} 10^{-5}	0.1	-1.0	1.0	μA μA

AC Electrical Characteristics* CD4000M $T_A = 25^{\circ}C$, $C_L = 50$ pF, unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_{PHL}	Propagation Delay Time, High to Low Level	$V_{DD} = 5V$ $V_{DD} = 10V$		40 20	50 40	ns ns
t_{PLH}	Propagation Delay Time, Low to High Level	$V_{DD} = 5V$ $V_{DD} = 10V$		50 25	95 45	ns ns
t_{THL}	Transition Time, High to Low Level	$V_{DD} = 5V$ $V_{DD} = 10V$		50 20	125 70	ns ns
t_{TLH}	Transition Time, Low to High Level	$V_{DD} = 5V$ $V_{DD} = 10V$		70 35	175 75	ns ns
C_i	Input Capacitance	Any Input		5		pF
C_{PD}	Power Dissipation Capacitance	(Note 5)		35		pF

*AC Parameters are guaranteed by DC correlated testing.

DC Electrical Characteristics CD4000C (Note 2)

Symbol	Parameter	Conditions	- 40°C		+ 25°C			+ 85°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
I_{DD}	Quiescent Device Current	$V_{DD} = 5V$ $V_{DD} = 10V$		0.05 5			0.05 5		15 30	μA μA
V_{OL}	Low Level Output Voltage	$V_{DD} = 5V$ $V_{DD} = 10V$		0.05 0.05			0.05 0.05		0.05 0.05	V V
V_{OH}	High Level Output Voltage	$V_{DD} = 5V$ $V_{DD} = 10V$	4.95 9.95		4.95 9.95			4.95 9.95		V V
V_{NL}	Noise Immunity (Note 3)	$V_{DD} = 5V, V_O = 1.4V$ or $3.6V$ $V_{DD} = 10V, V_O = 2.8V$ or $7.2V$	1.5 3.0		1.5 3.0			1.4 2.9		V V
V_{NH}	Noise Immunity (Note 3)	$V_{DD} = 5V, V_O = 1.4V$ or $3.6V$ $V_{DD} = 10V, V_O = 2.8V$ or $7.2V$	1.4 2.9		1.5 3.0			1.5 3.0		V V
I_{DL}	Low Level Output Current (Note 4)	$V_{DD} = 5V, V_O = 0.4V$ $V_{DD} = 10V, V_O = 0.5V$	0.35 0.72		0.3 0.6			0.24 0.48		mA mA
I_{DP}	High Level Output Current (Note 4)	$V_{DD} = 5V, V_O = 2.5V$ $V_{DD} = 10V, V_O = 9.5V$	-0.35 -0.3		-0.3 -0.25			-0.24 -0.2		mA mA
I_{IN}	Input Current	$V_{DD} = 15V, V_{IN} = 0V$ $V_{DD} = 15V, V_{IN} = 15V$	-0.3		-0.3	-10^{-5} 10^{-5}		-1.0		μA μA

AC Electrical Characteristics* CD4000C $T_A = 25^\circ C$, $C_L = 50$ pF, unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_{PHL}	Propagation Delay Time, High to Low Level	$V_{DD} = 5V$ $V_{DD} = 10V$		40 20	80 55	ns ns
t_{PLH}	Propagation Delay Time Low to High Level	$V_{DD} = 5V$ $V_{DD} = 10V$		50 25	120 65	ns ns
t_{THL}	Transition Time, High to Low Level	$V_{DD} = 5V$ $V_{DD} = 10V$		50 20	200 115	ns ns
t_{TLH}	Transition Time, Low to High Level	$V_{DD} = 5V$ $V_{DD} = 10V$		70 35	300 125	ns ns
C_i	Input Capacitance	Any Input		5		pF
C_{PD}	Power Dissipation Capacitance	(Note 5)		35		pF

*AC Parameters are guaranteed by DC correlated testing.

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: $V_{SS} = 0V$ unless otherwise specified.

Note 3: For the NOR gates V_{NH} and V_{NL} are tested at each input while all other inputs are at V_{SS} .

Note 4: I_{DN} and I_{DP} are tested one output at a time.

Note 5: C_{PD} determines the no load AC power consumption of any CMOS device. For explanation see 54C/74C Family Characteristics Application Note, AN-90.