

## Product Summary

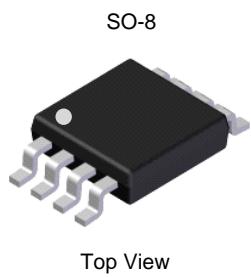
- Continuous Drain Source Voltage: 60V
- On-State Resistance: 200mΩ
- Nominal Load Current ( $V_{IN} = 5V$ ): 2.0A
- Clamping Energy: 120mJ

## Description

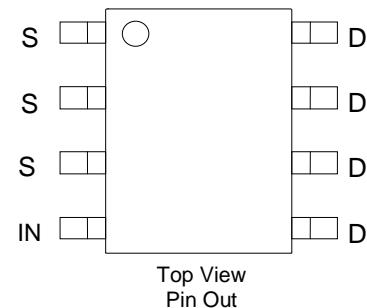
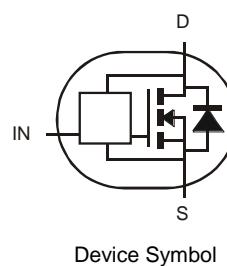
The ZXMS6005N8 is a self-protected low side IntelliFET™ MOSFET with logic level input. It integrates over-temperature, overcurrent, overvoltage (active clamp) and ESD protected logic level functionality. The ZXMS6005N8 is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.

## Applications

- Especially Suited for Loads with a High In-Rush Current such as Lamps and Motors
- All Types of Resistive, Inductive and Capacitive Loads in Switching Applications
- μC Compatible Power Switch for 12V and 24V DC Applications
- Replaces Electromechanical Relays and Discrete Circuits
- Linear Mode Capability—the current-limiting protection circuitry is designed to deactivate at low  $V_{DS}$  to minimize on-state power dissipation. The maximum DC operating current is therefore determined by the thermal capability of the package/board combination, rather than by the protection circuitry. This does not compromise the product's ability to self-protect at low  $V_{DS}$ .



Top View



Top View  
Pin Out

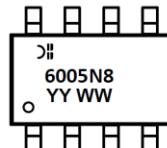
## Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXMS6005N8-13	6005N8	13	12	2,500 Units

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

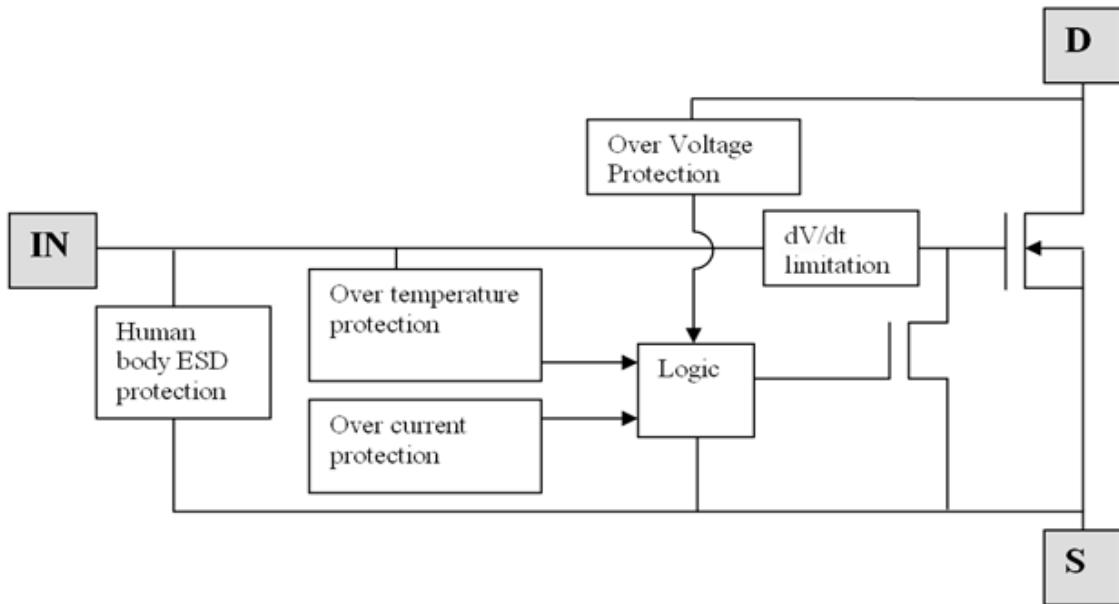
## Marking Information



Top View

○ = Manufacturer's Marking  
 6005N8 = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY: Year  
 WW: Week: 01~52;  
 52 represents 52 and 53 week

## Functional Block Diagram



## Absolute Maximum Ratings (@ $T_A = +25^\circ\text{C}$ , unless otherwise stated.)

Characteristic	Symbol	Value	Unit
Continuous Drain-Source Voltage	$V_{DS}$	60	V
Drain-Source Voltage for Short Circuit Protection	$V_{DS(\text{SC})}$	24	V
Continuous Input Voltage	$V_{IN}$	-0.5 to +6	V
Continuous Input Current @ $-0.2\text{V} \leq V_{IN} \leq 6\text{V}$	$I_{IN}$	No Limit	
Continuous Input Current @ $V_{IN} < -0.2\text{V}$ or $V_{IN} > 6\text{V}$		$ I_{IN}  \leq 2$	mA
Pulsed Drain Current @ $V_{IN} = 3.3\text{V}$	$I_{DM}$	5	A
Pulsed Drain Current @ $V_{IN} = 5\text{V}$	$I_{DM}$	6	A
Continuous Source Current (Body Diode) (Note 5)	$I_S$	2.5	A
Pulsed Source Current (Body Diode)	$I_{SM}$	10	A
Unclamped Single Pulse Inductive Energy, $T_J = +25^\circ\text{C}$ , $I_D = 0.5\text{A}$ , $V_{DD} = 24\text{V}$	$E_{AS}$	120	mJ
Electrostatic Discharge (Human Body Model)	$V_{HBM}$	4,000	V
Charged Device Model	$V_{CDM}$	1,000	V

## Recommended Operating Conditions

The ZXMS6005N8 is optimized to use with  $\mu\text{C}$  operating from 3.3V and 5V supplies.

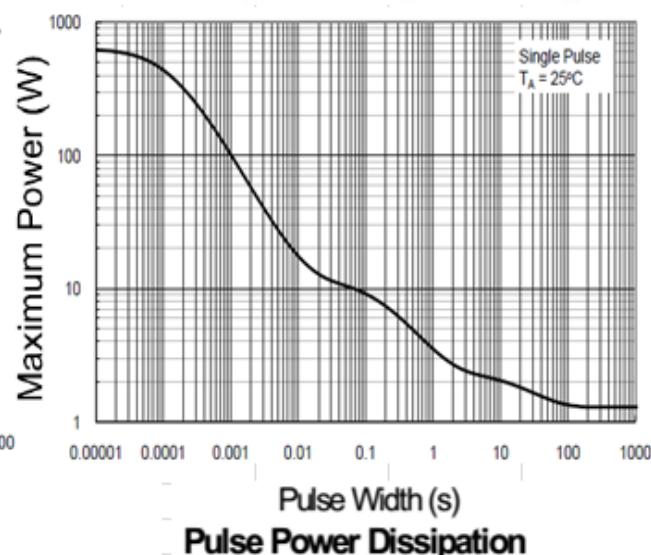
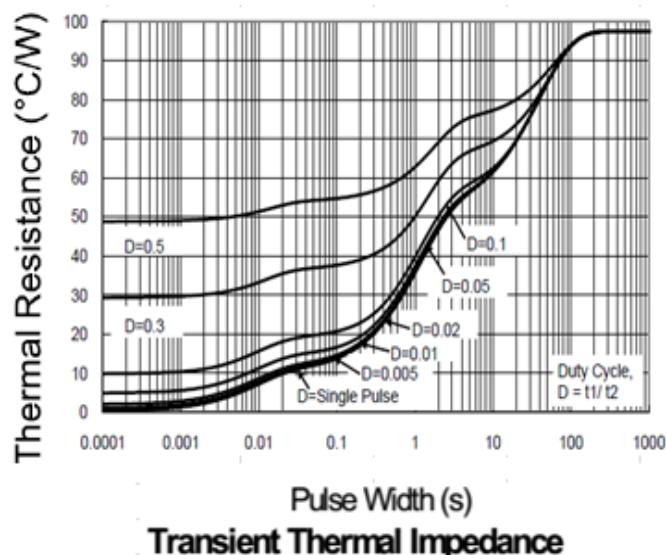
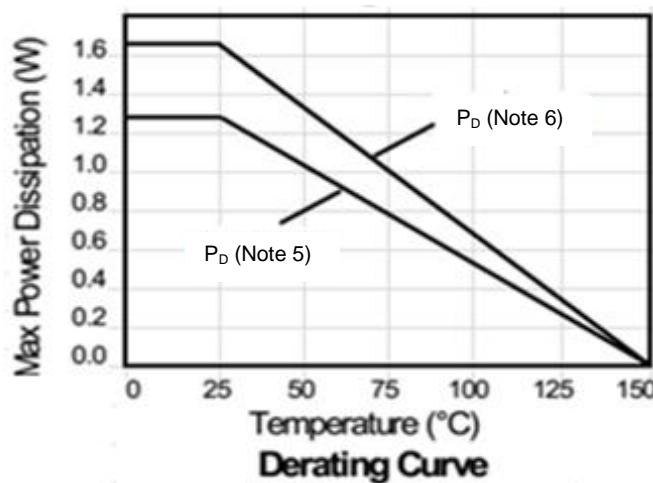
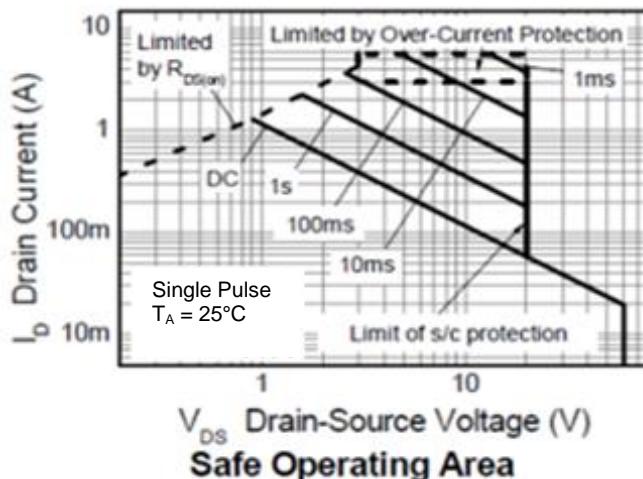
Characteristic	Symbol	Min	Max	Unit
Input Voltage Range	$V_{IN}$	0	5.5	V
Ambient Temperature Range	$T_A$	-40	+125	°C
High Level Input Voltage for MOSFET to be On	$V_{IH}$	3	5.5	V
Low Level Input Voltage for MOSFET to be Off	$V_{IL}$	0	0.7	V
Peripheral Supply Voltage (Voltage to which Load is Referred)	$V_P$	0	24	V

**Thermal Resistance** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation @ $T_A = +25^\circ\text{C}$ (Note 5)	$P_D$	1.28	W
Linear Derating Factor		10	$\text{mW}/^\circ\text{C}$
Power Dissipation @ $T_A = +25^\circ\text{C}$ (Note 6)	$P_D$	1.65	W
Linear Derating Factor		12.4	$\text{mW}/^\circ\text{C}$
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	98	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	76	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Case (Note 7)	$R_{\theta JC}$	12	$^\circ\text{C}/\text{W}$
Operating Temperature Range	$T_J$	-40 to +150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55 to +150	$^\circ\text{C}$

Notes:

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Thermal resistance between junction and the mounting surfaces of drain and source pins.



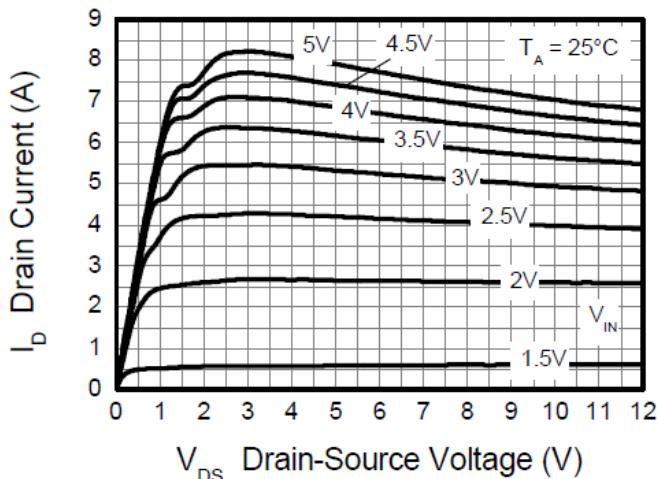
**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise stated.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>Static Characteristics</b>						
Drain-Source Clamp Voltage	$V_{DS(AZ)}$	60	65	70	V	$I_D = 10\text{mA}$
Off-State Drain Current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 12\text{V}, V_{IN} = 0\text{V}$
		—	—	2		$V_{DS} = 36\text{V}, V_{IN} = 0\text{V}$
Input Threshold Voltage	$V_{IN(TH)}$	0.7	1	1.5	V	$V_{DS} = V_{GS}, I_D = 1\text{mA}$
Input Current	$I_{IN}$	—	60	100	$\mu\text{A}$	$V_{IN} = 3\text{V}$
		—	120	200		$V_{IN} = 5\text{V}$
Input Current while Overtemperature Active	—	—	—	300	$\mu\text{A}$	$V_{IN} = 5\text{V}$
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	—	170	250	$\text{m}\Omega$	$V_{IN} = 3\text{V}, I_D = 1.0\text{A}$
		—	150	200		$V_{IN} = 5\text{V}, I_D = 1.0\text{A}$
Continuous Drain Current (Note 5)	$I_D$	1.4	—	—	A	$V_{IN} = 3\text{V}, T_A = +25^\circ\text{C}$
		1.6	—	—		$V_{IN} = 5\text{V}, T_A = +25^\circ\text{C}$
		1.9	—	—		$V_{IN} = 3\text{V}, T_A = +25^\circ\text{C}$
		2.0	—	—		$V_{IN} = 5\text{V}, T_A = +25^\circ\text{C}$
Current Limit (Note 8)	$I_{D(LIM)}$	2.2	5	—	A	$V_{IN} = 3\text{V}$
		3.3	7	—		$V_{IN} = 5\text{V}$
<b>Dynamic Characteristics</b>						
Turn-On Delay Time	$t_{D(ON)}$	—	5	—	$\mu\text{s}$	$V_{DD} = 12\text{V}, I_D = 0.5\text{A}, V_{GS} = 5\text{V}$
Rise Time	$t_R$	—	14	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	34	—		
Fall Time	$t_F$	—	19	—		
<b>Overtemperature Protection</b>						
Thermal Overload Trip Temperature (Note 9)	$T_{JT}$	+150	+175	—	°C	—
Thermal Hysteresis (Note 9)	$\Delta T_{JT}$	—	+10	—	°C	—

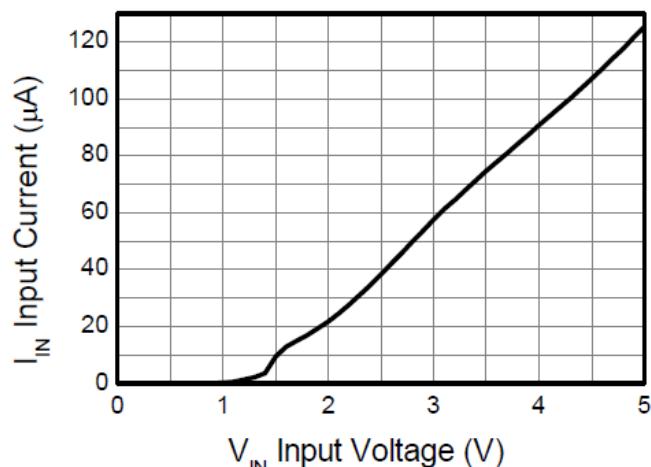
Notes: 8. The drain current is restricted only when the device is in saturation (see Typical Output Characteristic graph). This allows the device to be used in the fully on state without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unnecessary.

9. Overtemperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal operating range, so this part is not designed to withstand overtemperature for extended periods.

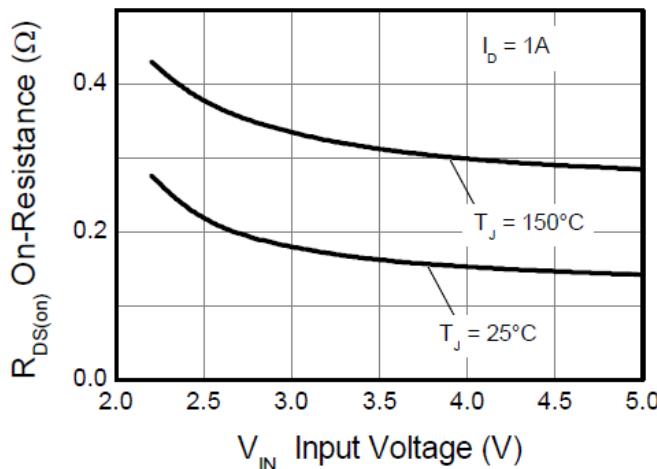
## Typical Characteristics



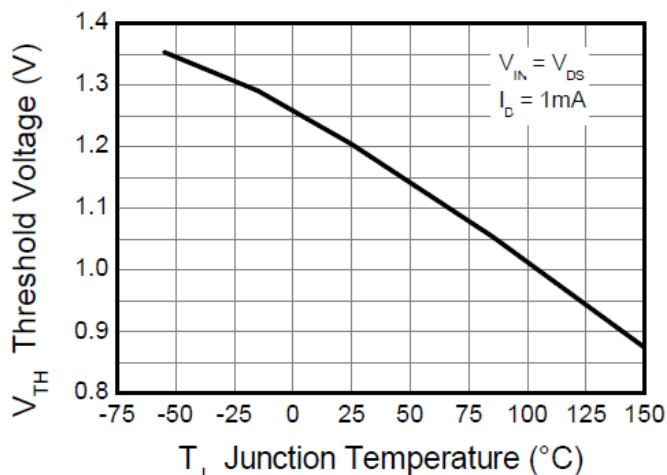
**Typical Output Characteristic**



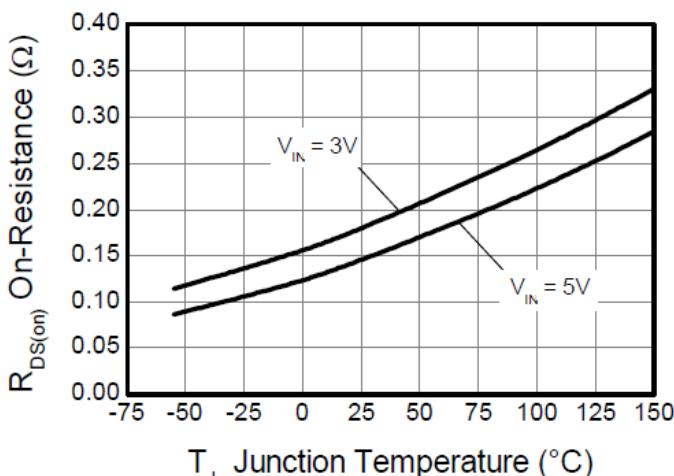
**Input Current vs Input Voltage**



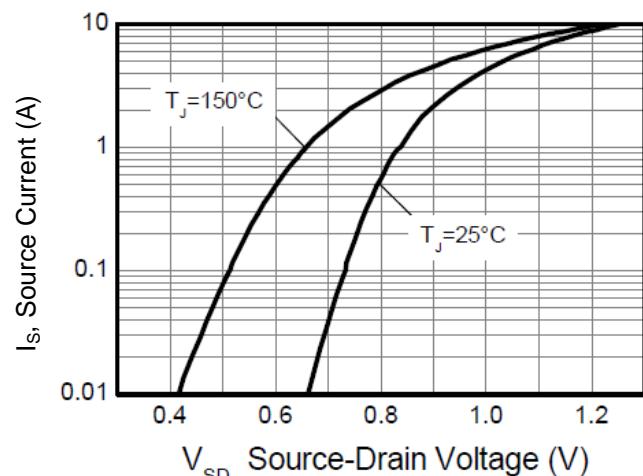
**On-Resistance vs Input Voltage**



**Threshold Voltage vs Temperature**

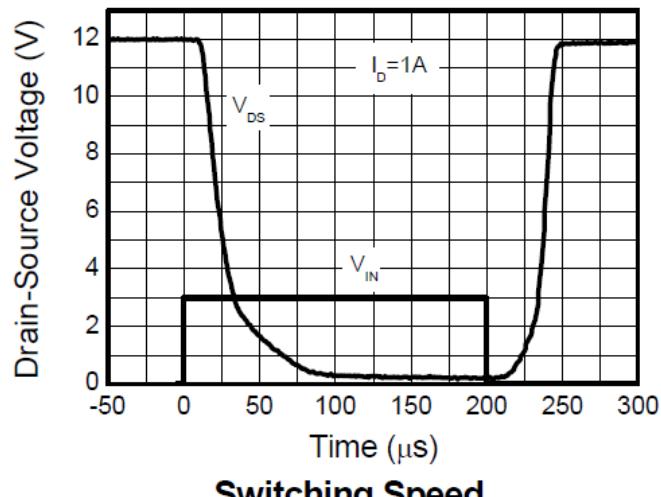


**On-Resistance vs Temperature**

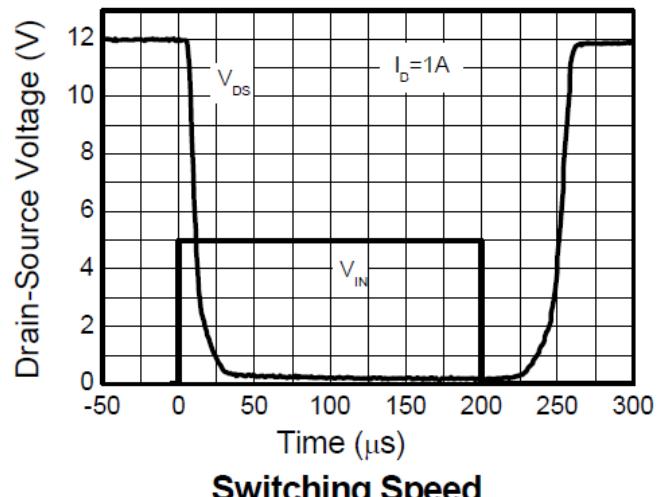


**Reverse Diode Characteristic**

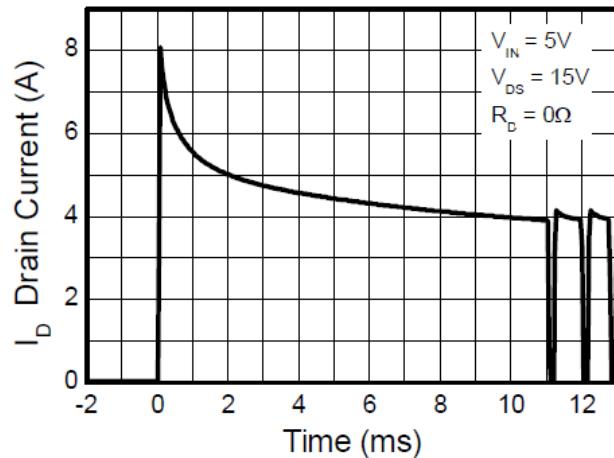
**Typical Characteristics (Cont.)**



**Switching Speed**



**Switching Speed**

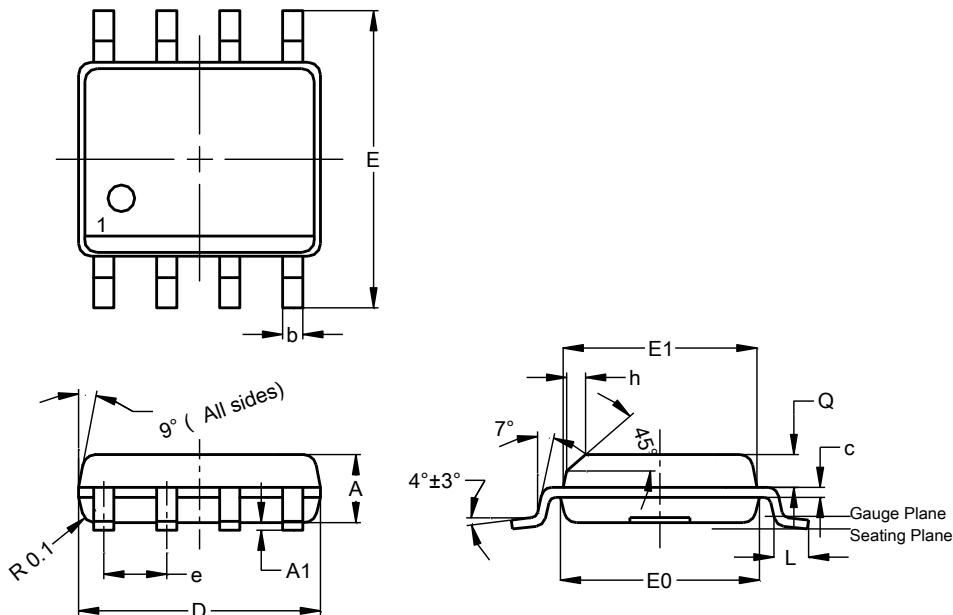


**Typical Short Circuit Protection**

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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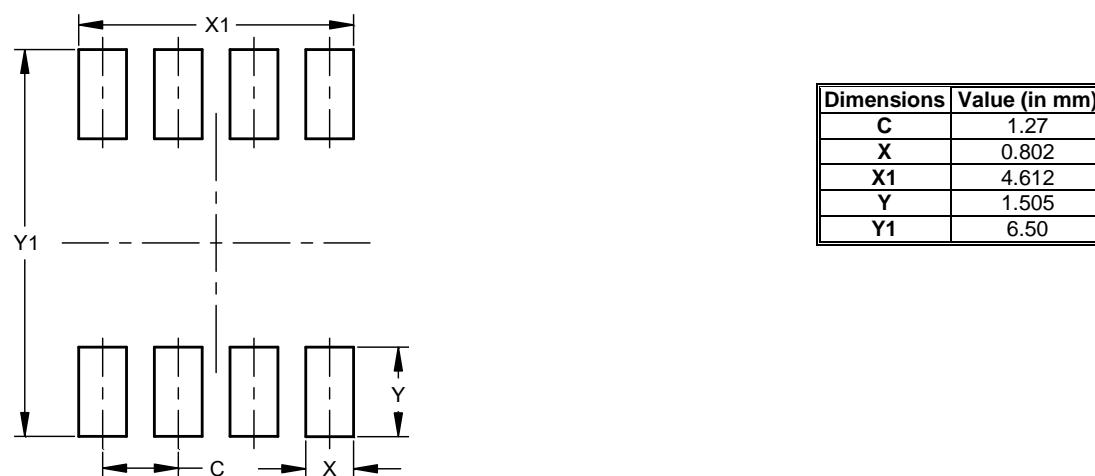
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Dim	Min	Max	Typ
<b>A</b>	1.40	1.50	1.45
<b>A1</b>	0.10	0.20	0.15
<b>b</b>	0.30	0.50	0.40
<b>c</b>	0.15	0.25	0.20
<b>D</b>	4.85	4.95	4.90
<b>E</b>	5.90	6.10	6.00
<b>E1</b>	3.80	3.90	3.85
<b>E0</b>	3.85	3.95	3.90
<b>e</b>	--	--	1.27
<b>h</b>	-	--	0.35
<b>L</b>	0.62	0.82	0.72
<b>Q</b>	0.60	0.70	0.65

All Dimensions in mm

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



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