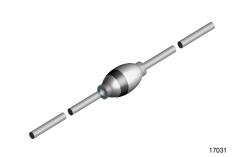


## **Vishay Semiconductors**

## **Standard Sinterglass Diode**

#### **Features**

- High temperature metallurgically bonded constructed rectifiers
- · Cavity-free glass passivated junction
- · Hermetically sealed package
- 2.0 ampere operation at T<sub>amb</sub> = 75 °C with no thermal runaway



#### **Mechanical Data**

Case: DO-204AP Sintered glass case

**Terminals:** Solder plated axial leads, solderable per Mill-STD-750, Method 2026

Polarity: Color band denotes cathode end

Mounting Position: Any Weight: approx. 560 mg

#### **Parts Table**

Part	Type differentiation	Package
G2A	V <sub>RRM</sub> = 50 V	DO-204AP(G-1)
G2B	V <sub>RRM</sub> = 100 V	DO-204AP(G-1)
G2D	V <sub>RRM</sub> = 200 V	DO-204AP(G-1)
G2G	V <sub>RRM</sub> = 400 V	DO-204AP(G-1)
G2J	V <sub>RRM</sub> = 600 V	DO-204AP(G-1)
G2K	V <sub>RRM</sub> = 800 V	DO-204AP(G-1)
G2M	V <sub>RRM</sub> = 1000 V	DO-204AP(G-1)

#### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
Reverse voltage = Repetitive peak reverse voltage	see electrical characteristics	G2A	$V_R = V_{RRM}$	50	V
		G2B	$V_R = V_{RRM}$	100	V
		G2D	$V_R = V_{RRM}$	200	V
		G2G	$V_R = V_{RRM}$	400	٧
		G2J	$V_R = V_{RRM}$	600	V
		G2K	$V_R = V_{RRM}$	800	V
		G2M	$V_R = V_{RRM}$	1000	V
Maximum average forward rectified current	0.375 " (9.5 mm) lead length at $T_{amb}$ = 75 °C		I <sub>F(AV)</sub>	2.0	Α

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## **Vishay Semiconductors**



Parameter	Test condition	Part	Symbol	Value	Unit
Peak forward surge current	8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)		I <sub>FSM</sub>	50	Α
Maximum full load reverse current	full cycle average 0.375 " (9.5 mm) lead length at T <sub>amb</sub> = 100 °C		I <sub>R(AV)</sub>	100	μА
Operating junction and storage temperature range			T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 175	°C

#### **Maximum Thermal Resistance**

 $T_{amb}$  = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol Value		Unit
Typical thermal resistance 1)		$R_{ hetaJA}$	55	K/W

<sup>1)</sup> Thermal resistance from junction to ambient at 0.375 " (9.5 mm) lead length, P.C.B. mounted

#### **Electrical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Maximum instantaneous	I <sub>F</sub> = 2 A	G2A	$V_{F}$			1.2	V
forward voltage							
		G2B	$V_{F}$			1.2	V
		G2D	$V_{F}$			1.1	V
		G2G	$V_{F}$			1.1	V
		G2J	$V_{F}$			1.1	V
		G2K	V <sub>F</sub>			1.1	V
		G2M	$V_{F}$			1.1	V
Maximum reverse current	V <sub>R</sub> = V <sub>RRM</sub> , T <sub>amb</sub> = 25 °C		I <sub>R</sub>			1.0	μА
	V <sub>R</sub> = V <sub>RRM</sub> , T <sub>amb</sub> = 150 °C		I <sub>R</sub>			100	μΑ
Typical reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		t <sub>rr</sub>		1.5		μs
Typical junction capacitance	V <sub>R</sub> = 4.0 V, f = 1 MHz		C <sub>j</sub>		15		pF

## **Typical Characteristics** (T<sub>amb</sub> = 25 °C unless otherwise specified)

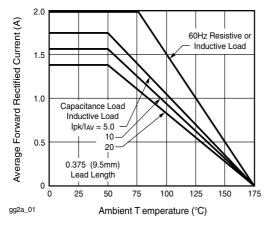


Figure 1. Forward Current Derating Curve

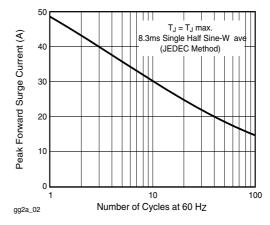


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current



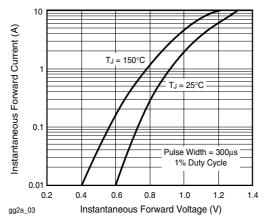


Figure 3. Typical Instantaneous Forward Characteristics

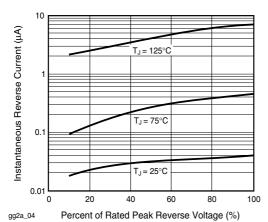


Figure 4. Typical Reverse Characteristics

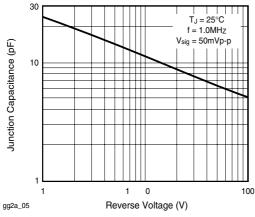
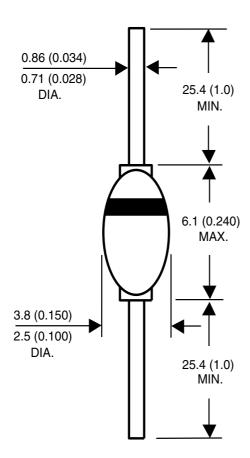


Figure 5. Typical Junction Capacitance

## **Vishay Semiconductors**

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## **Package Dimensions in mm (Inches)**



17030

## G2A/B/D/G/J/K/M



### Vishay Semiconductors

#### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

#### We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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