

10BASE-T Isolation Modules

16 Pin SOIC Package



Product Features:

- Standard series of 16 Pin SOIC Isolation Modules to meet the isolation and impedance matching requirements of IEEE 802.3 for 10BASE-T
- 100% Compliance Testing
- 1500Vrms Hi-Pot
- Patented Open Frame Construction
- UL60950, IEC60950 and EN60950 recognized

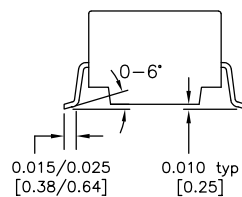
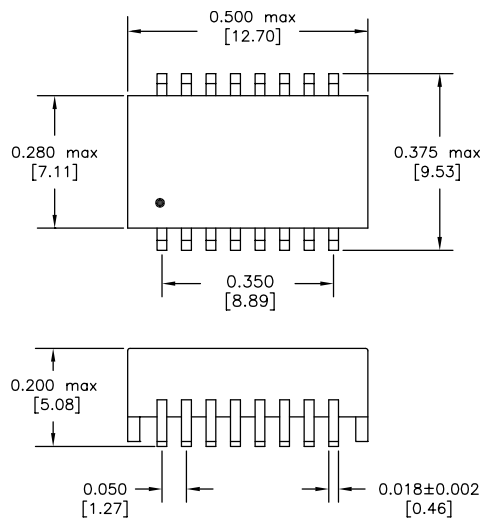
Part Number	Turns Ratio $\pm 3\%$ (1-3:16-14) / (6-8:11-9)	PRI OCL (μH)	PRI/SEC Cw/w (pF max)	PRI LL (μH max)	PRI DCR (Ω max)	Schematic	Temp Range
TG04-1006N1RL	(1CT:1CT) / (1CT:1CT)	100 Typ	9	0.4	0.3	A	0 to +70°C
TG04-2006N1RL	(1CT:1CT) / (1CT:1CT)	200 Typ	15	0.3	0.4	A	-40 to +85°C
TG41-2006NRL	(1CT:1CT) / (1CT:2.5CT)	200/35 Min	15	0.4	0.5	B	-40 to +85°C
TG42-1406N1RL	(1CT:1CT) / (1CT:1.41CT)	140 Typ	15	0.4	0.3	A	0 to +70°C
TG43-1406NRL	(1CT:1CT) / (1CT:1.41CT)	140 Typ	15	0.4	0.3	B	0 to +70°C ¹
TG62-1006N1RL	(2CT:1CT) / (1CT:1CT)	150 Min	10	0.6	0.35	A	0 to +70°C
TG66-1006NRL	(2CT:1CT) / (1CT:1CT)	150 Min	10	0.9	0.35	B	0 to +70°C
TG74-1406N1RL	(1CT:1CT) / (1CT:2CT)	140/25 Typ	15	0.4	0.3	A	-40 to +85°C
TG75-1406NRL	(1CT:1CT) / (1CT:2CT)	140/25 Typ	15	0.4	0.3	B	-40 to +85°C
TG81-1006NRL	(1CT:1CT) / (1CT:1CT)	100 Typ	9	0.3	0.3	B	0 to +70°C
TG84-2006N1RL	(1CT:2CT) / (1CT:1CT)	50/200 Min	11	0.15	0.5	A	-40 to +85°C
TG86-2006NRL	(1CT:2CT) / (1CT:1CT)	50/200 Min	11	0.3	0.5	B	-40 to +85°C
TG92-2006N1RL	(1CT:1CT) / (1CT:2.5CT)	200/35 Min	15	0.4	0.5	A	-40 to +85°C

Notes:

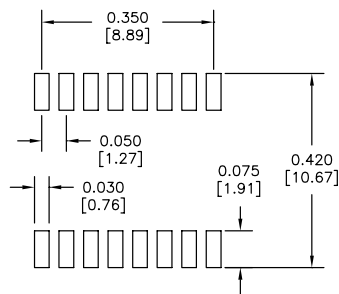
1. TG43-1406NRL can be operated in -40 to +85°C environment when using the Cirrus Logic CS8900A-IQ (5V) Transceiver
2. Please contact the factory or representative for individual datasheets or additional information
3. Contact factory for "LF" lead free availability

10BASE-T Isolation Modules

Package N/N1

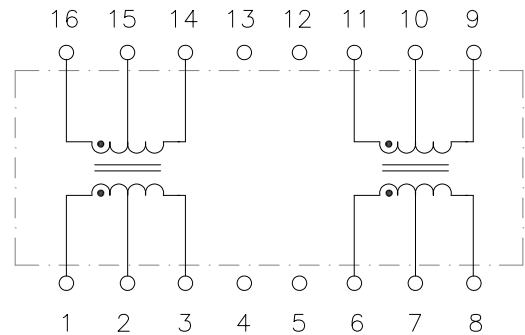


Dimensions are in inches [mm]
Co-Planarity: 0.004 [0.10]

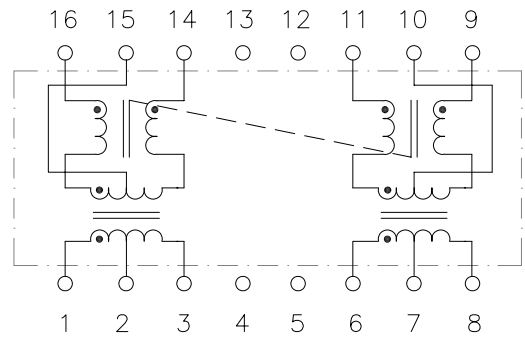


Recommended
Solder Pad Layout

Schematic A



Schematic B



US Patent Numbers

5,656,985 6,297,721 B1
6,297,720 B1 6,320,489 B1
6,344,785 B1 6,662,431 B1

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