

Silicon Switching Diode

Rev. V1

Features

- Available in JAN, JANTX, and JANTXV per MIL-PRF-19500/144
- Metallurgically Bonded
- Hermetically Sealed
- Double Plug Construction



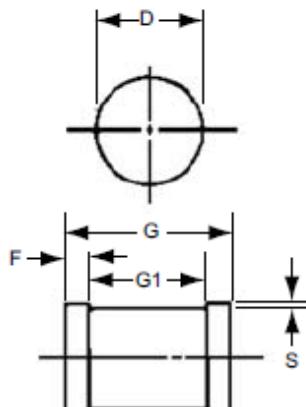
Maximum Ratings

Operating & Storage Temperature: -55°C to +175°C
 Operating Current: 200 mA @ $T_A = +75^\circ\text{C}$
 Derating: 2 mA DC/°C above $T_L = +75^\circ\text{C}$ @ $L = 3/8"$
 Surge Current A: 2 A (pk) $t_P = 8.3$ ms, $V_{RM} = 0$
 Surge Current B: 4 A (pk) $t_P = 1$ μs , $V_{RM} = 0$

Electrical Specifications @ +25°C (unless otherwise Specified)

V_{BR} @ 5 μA	V_{RWM}	I_0	V_F1 @ $I_F = 10$ mA	V_F2 @ $I_F = 10$ mA $T_A = 150^\circ\text{C}$	t_{rr}	I_{R1} @ 50 V dc	I_{R2} @ 50 V $T_A = 150^\circ\text{C}$	Capacitance @ 0 V
Volts	Volts (pk)	mA	V dc	V dc	n sec	μA	μA	pF
75	50	200	0.8	0.7	4	0.1	100	2

Outline



MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX
D	1.60	1.70	0.063	0.067
F	0.41	0.55	0.016	0.022
G	3.30	3.70	.130	.146
G1	2.54 REF.		.100 REF.	
S	0.03 MIN.		.001 MIN.	

LEADED DESIGN DATA

CASE: DO – 213AA, Hermetically sealed glass case.
 (MELF, SOD-80, LL34)

LEAD FINISH: Tin / Lead

THERMAL RESISTANCE: ($R_{\Theta JEC}$): 100 °C/W maximum

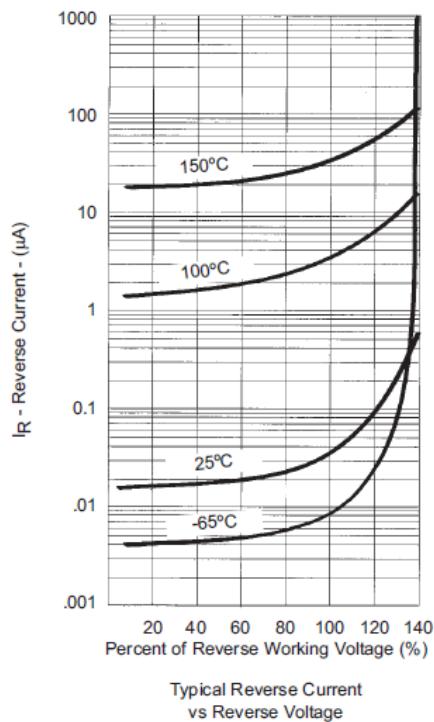
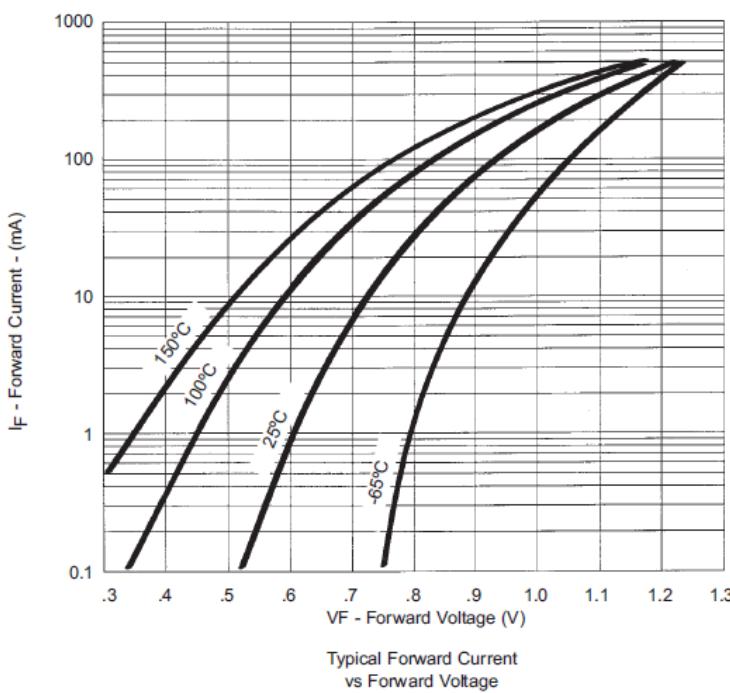
THERMAL IMPEDANCE: ($Z_{\Theta JX}$): 70 °C/W maximum

POLARITY: Cathode end is banded.

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Graphs



NOTE: All temperatures shown on graphs are junction temperatures.

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