

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HCU04AFN

Hex Inverter

The TC74HCU04A is a high speed CMOS INVERTER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

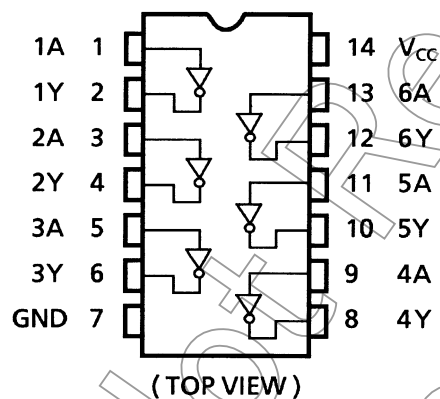
Since the internal circuit is composed of a single stage inverter, it can be used in analog applications such as crystal oscillators.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

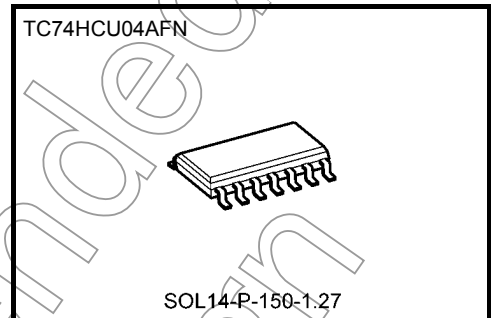
Features

- High speed: $t_{pd} = 4 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 1 \mu\text{A (max)}$ at $T_a = 25^\circ\text{C}$
- High noise immunity: $V_{NIH} = V_{NIH} = 10\% V_{CC} \text{ (min)}$
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 4 \text{ mA (min)}$
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range: $V_{CC} \text{ (opr)} = 2 \text{ to } 6 \text{ V}$
- Pin and function compatible with 74LS04

Pin Assignment

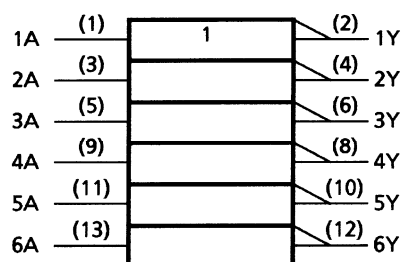


Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight
SOL14-P-150-1.27 : 0.12 g (typ.)

IEC Logic Symbol



Truth Table

A	Y
L	H
H	L

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to 7	V
DC input voltage	V_{IN}	-0.5 to $V_{CC} + 0.5$	V
DC output voltage	V_{OUT}	-0.5 to $V_{CC} + 0.5$	V
Input diode current	I_{IK}	± 20	mA
Output diode current	I_{OK}	± 20	mA
DC output current	I_{OUT}	± 25	mA
DC V_{CC} /ground current	I_{CC}	± 50	mA
Power dissipation	P_D	180	mW
Storage temperature	T_{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2 to 6	V
Input voltage	V_{IN}	0 to V_{CC}	V
Output voltage	V_{OUT}	0 to V_{CC}	V
Operating temperature	T_{opr}	-40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = –40 to 85°C		Unit	
				V _{CC} (V)	Min	Typ.	Max	Min		Max
High-level input voltage	V _{IH}	—		2.0 4.5 6.0	1.7 3.6 4.8	— — —	— — —	1.7 3.6 4.8	— — —	V
Low-level input voltage	V _{IL}	—		2.0 4.5 6.0	— — —	— — —	0.3 0.9 1.2	— — —	0.3 0.9 1.2	V
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = –20 μA	2.0	1.8	2.0	—	1.9	—	V
				4.5	4.0	4.5	—	4.0	—	
		V _{IN} = GND	I _{OH} = –4 mA I _{OH} = –5.2 mA	6.0	5.5	5.9	—	5.5	—	
				4.5	4.18	4.31	—	4.13	—	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 20 μA	2.0	—	0.0	0.2	—	0.2	V
				4.5	—	0.0	0.5	—	0.5	
		V _{IN} = V _{CC}	I _{OL} = 4 mA I _{OL} = 5.2 mA	6.0	—	0.1	0.5	—	0.5	
				4.5	—	0.17	0.26	—	0.33	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	—	—	±0.1	—	±1.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	—	—	1.0	—	10.0	μA

AC Characteristics (C_L = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output transition time	t _{TLH}	—	—	4	8	ns
	t _{THL}					
Propagation delay time	t _{pLH}	—	—	4	8	ns
	t _{pHL}					

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	Min	Typ.	Max	Min	Max
Output transition time	t _{TLH} t _{THL}	—	2.0	—	30	75	—	95
			4.5	—	8	15	—	19
			6.0	—	7	13	—	16
Propagation delay time	t _{pLH} t _{pHL}	—	2.0	—	18	60	—	75
			4.5	—	6	12	—	15
			6.0	—	5	10	—	13
Input capacitance	C _{IN}	—	—	—	9	15	—	15
Power dissipation capacitance	C _{PD} (Note)	—	—	—	13	—	—	—

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

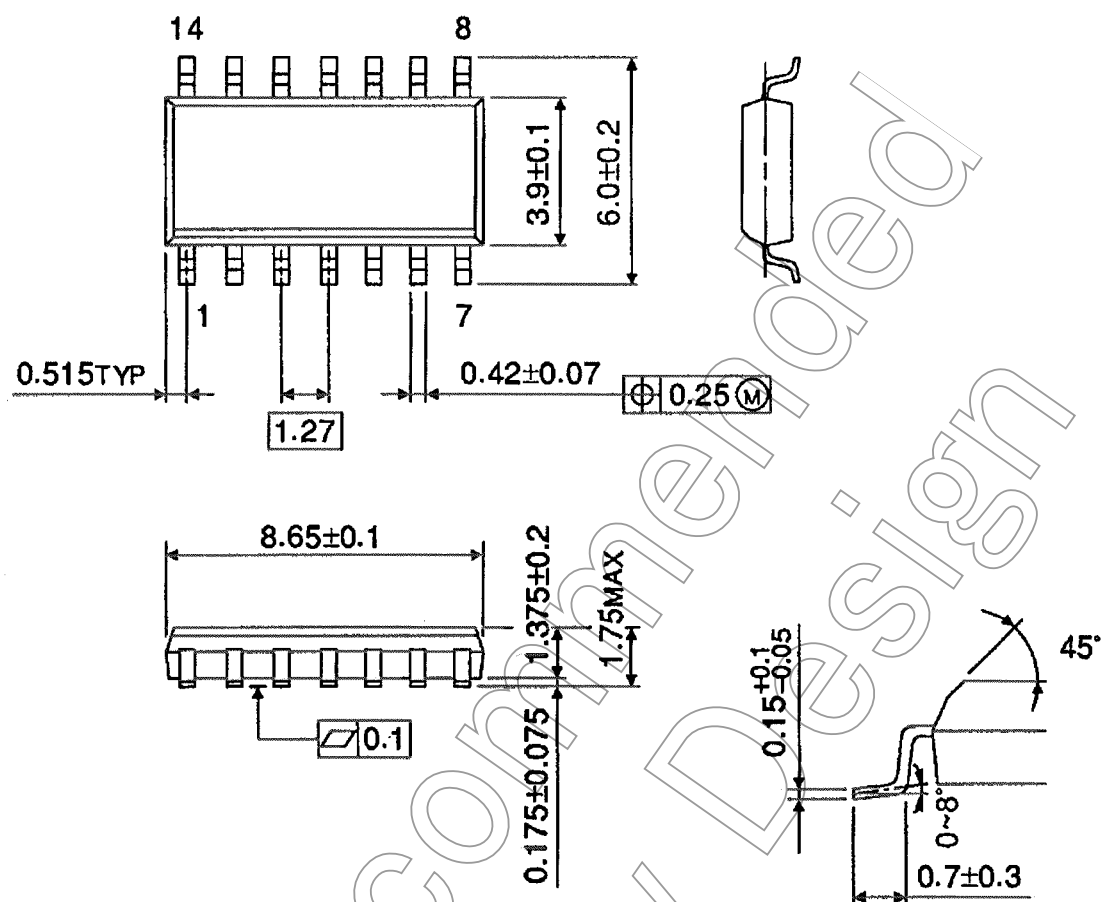
Average operating current can be obtained by the equation:

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$$

Package Dimensions (Note)

SOL14-P-150-1.27

Unit : mm



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

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