

# HAF2012(L),HAF2012(S)

## Silicon N Channel MOS FET Series Power Switching

# HITACHI

ADE-208-677A (Z)

2nd. Edition

July 2000

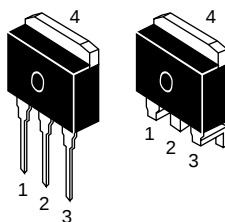
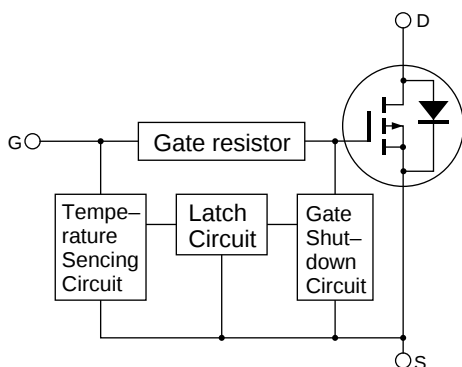
This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc.

### Features

- Logic level operation (4 to 6 V Gate drive)
- High endurance capability against to the short circuit
- Built-in the over temperature shut-down circuit
- Latch type shut-down operation (Need 0 voltage recovery)

### Outline

LDPAK



1. Gate
2. Drain
3. Source
4. Drain

Absolute Maximum Ratings (Ta = 25°C)

| Item                                   | Symbol                                 | Ratings     | Unit |
|----------------------------------------|----------------------------------------|-------------|------|
| Drain to source voltage                | V <sub>DSS</sub>                       | 60          | V    |
| Gate to source voltage                 | V <sub>GSS</sub>                       | 16          | V    |
| Gate to source voltage                 | V <sub>GSS</sub>                       | −2.8        | V    |
| Drain current                          | I <sub>D</sub>                         | 20          | A    |
| Drain peak current                     | I <sub>D(pulse)</sub> <sup>Note1</sup> | 40          | A    |
| Body-drain diode reverse drain current | I <sub>DR</sub>                        | 20          | A    |
| Channel dissipation                    | P <sub>ch</sub> <sup>Note2</sup>       | 50          | W    |
| Channel temperature                    | T <sub>ch</sub>                        | 150         | °C   |
| Storage temperature                    | T <sub>stg</sub>                       | −55 to +150 | °C   |

Note: 1. PW ≤ 10μs, duty cycle ≤ 1 %  
2. Value at Ta = 25°C

Typical Operation Characteristics

| Item                                  | Symbol               | Min | Typ  | Max | Unit | Test Conditions                |
|---------------------------------------|----------------------|-----|------|-----|------|--------------------------------|
| Input voltage                         | V <sub>IH</sub>      | 3.5 | —    | —   | V    |                                |
|                                       | V <sub>IL</sub>      | —   | —    | 1.2 | V    |                                |
| Input current<br>(Gate non shut down) | I <sub>IH1</sub>     | —   | —    | 100 | μA   | Vi = 8V, V <sub>DS</sub> = 0   |
|                                       | I <sub>IH2</sub>     | —   | —    | 50  | μA   | Vi = 3.5V, V <sub>DS</sub> = 0 |
|                                       | I <sub>IL</sub>      | —   | —    | 1   | μA   | Vi = 1.2V, V <sub>DS</sub> = 0 |
| Input current<br>(Gate shut down)     | I <sub>IH(sd)1</sub> | —   | 0.8  | —   | mA   | Vi = 8V, V <sub>DS</sub> = 0   |
|                                       | I <sub>IH(sd)2</sub> | —   | 0.35 | —   | mA   | Vi = 3.5V, V <sub>DS</sub> = 0 |
| Shut down temperature                 | T <sub>sd</sub>      | —   | 175  | —   | °C   | Channel temperature            |
| Gate operation voltage                | V <sub>OP</sub>      | 3.5 | —    | 13  | V    |                                |

## Electrical Characteristics (Ta = 25°C)

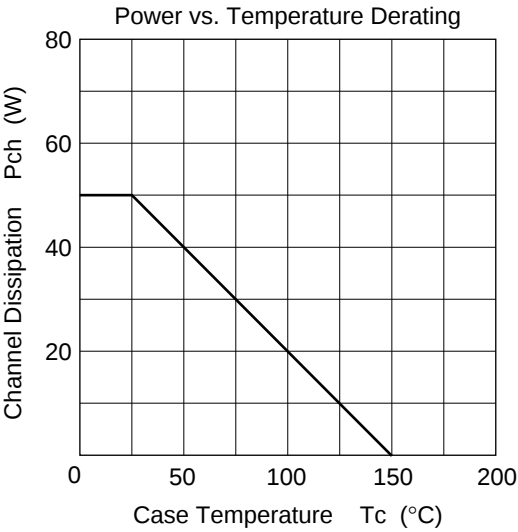
| Item                                       | Symbol        | Min  | Typ  | Max  | Unit       | Test Conditions                                  |
|--------------------------------------------|---------------|------|------|------|------------|--------------------------------------------------|
| Drain current                              | $I_{D1}$      | 10   | —    | —    | A          | $V_{GS} = 3.5V, V_{DS} = 2V$                     |
| Drain current                              | $I_{D2}$      | —    | —    | 10   | mA         | $V_{GS} = 1.2V, V_{DS} = 2V$                     |
| Drain to source breakdown voltage          | $V_{(BR)DSS}$ | 60   | —    | —    | V          | $I_D = 10mA, V_{GS} = 0$                         |
| Gate to source breakdown voltage           | $V_{(BR)GSS}$ | 16   | —    | —    | V          | $I_G = 100\mu A, V_{DS} = 0$                     |
| Gate to source breakdown voltage           | $V_{(BR)GSS}$ | -2.8 | —    | —    | V          | $I_G = -100\mu A, V_{DS} = 0$                    |
| Gate to source leak current                | $I_{GSS1}$    | —    | —    | 100  | $\mu A$    | $V_{GS} = 8V, V_{DS} = 0$                        |
|                                            | $I_{GSS2}$    | —    | —    | 50   | $\mu A$    | $V_{GS} = 3.5V, V_{DS} = 0$                      |
|                                            | $I_{GSS3}$    | —    | —    | 1    | $\mu A$    | $V_{GS} = 1.2V, V_{DS} = 0$                      |
|                                            | $I_{GSS4}$    | —    | —    | -100 | $\mu A$    | $V_{GS} = -2.4V, V_{DS} = 0$                     |
| Input current (shut down)                  | $I_{GS(op)1}$ | —    | 0.8  | —    | mA         | $V_{GS} = 8V, V_{DS} = 0$                        |
|                                            | $I_{GS(op)2}$ | —    | 0.35 | —    | mA         | $V_{GS} = 3.5V, V_{DS} = 0$                      |
| Zero gate voltage drain current            | $I_{DSS}$     | —    | —    | 250  | $\mu A$    | $V_{DS} = 50V, V_{GS} = 0$                       |
| Gate to source cutoff voltage              | $V_{GS(off)}$ | 1.0  | —    | 2.25 | V          | $I_D = 1mA, V_{DS} = 10V$                        |
| Static drain to source on state resistance | $R_{DS(on)}$  | —    | 50   | 65   | m $\Omega$ | $I_D = 10A, V_{GS} = 4V$ <sup>Note3</sup>        |
| Static drain to source on state resistance | $R_{DS(on)}$  | —    | 30   | 43   | m $\Omega$ | $I_D = 10A, V_{GS} = 10V$ <sup>Note3</sup>       |
| Forward transfer admittance                | $ y_{fs} $    | 6    | 12   | —    | S          | $I_D = 10A, V_{DS} = 10V$ <sup>Note3</sup>       |
| Output capacitance                         | $C_{oss}$     | —    | 630  | —    | pF         | $V_{DS} = 10V, V_{GS} = 0$<br>$f = 1\text{ MHz}$ |
| Turn-on delay time                         | $t_{d(on)}$   | —    | 7.5  | —    | $\mu s$    | $I_D = 5A, V_{GS} = 5V$                          |
| Rise time                                  | $t_r$         | —    | 29   | —    | $\mu s$    | $R_L = 6\Omega$                                  |
| Turn-off delay time                        | $t_{d(off)}$  | —    | 34   | —    | $\mu s$    |                                                  |
| Fall time                                  | $t_f$         | —    | 26   | —    | $\mu s$    |                                                  |
| Body-drain diode forward voltage           | $V_{DF}$      | —    | 1.0  | —    | V          | $I_F = 20A, V_{GS} = 0$                          |
| Body-drain diode reverse recovery time     | $t_{rr}$      | —    | 110  | —    | ns         | $I_F = 20A, V_{GS} = 0$<br>$di_F/dt = 50A/\mu s$ |
| Over load shut down                        | $t_{os1}$     | —    | 1.8  | —    | ms         | $V_{GS} = 5V, V_{DD} = 12V$                      |
| operation time <sup>Note4</sup>            | $t_{os2}$     | —    | 0.7  | —    | ms         | $V_{GS} = 5V, V_{DD} = 24V$                      |

Note: 3. Pulse test

4. Include the junction temperature rise of the over loaded condition.

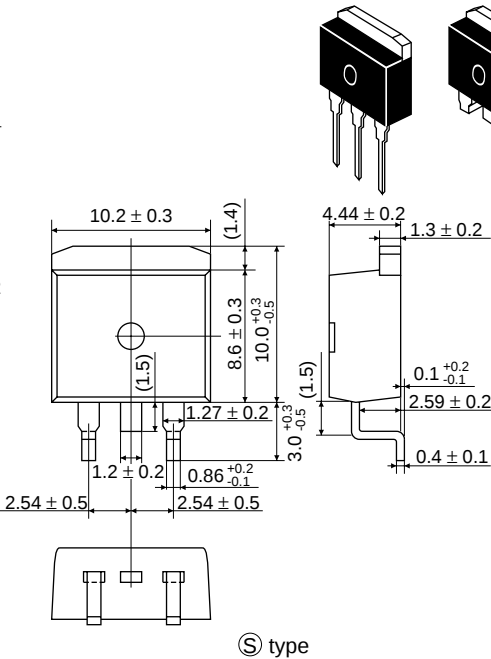
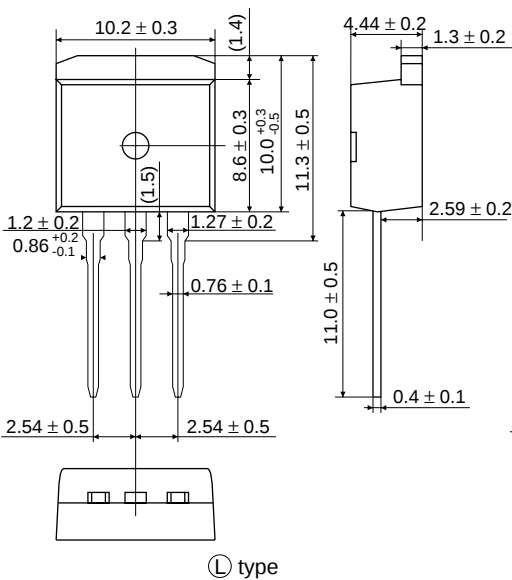
• See characteristic curve of HAF2001.

Main Characteristics



Package Dimensions

Unit: mm



|              |       |
|--------------|-------|
| Hitachi code | LDBAK |
| EIAJ         | -     |
| JEDEC        | -     |

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