

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

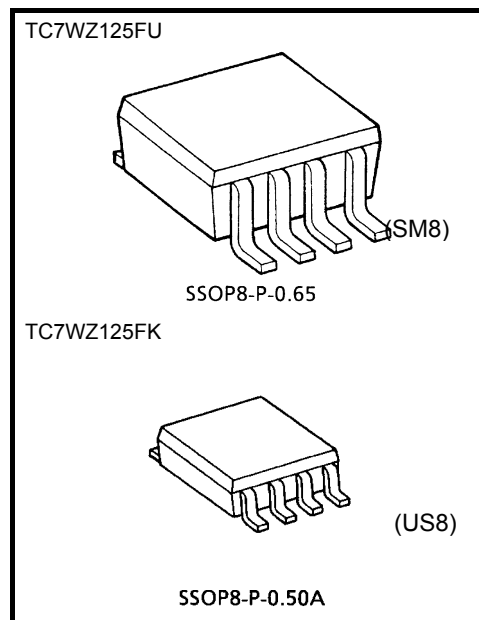
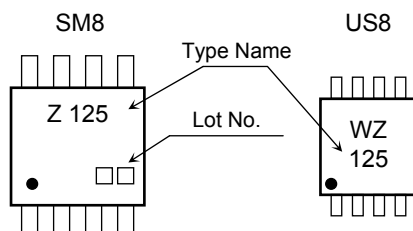
TC7WZ125FU, TC7WZ125FK

Dual Bus Buffer with 3-STATE Output

Features

- High output current : ± 24 mA (min) at $V_{CC} = 3$ V
- Super high speed operation : $t_{pd} = 2.6$ ns (typ.)
at $V_{CC} = 5$ V, 50 pF
- Operating voltage range : $V_{CC} (opr) = 1.65$ to 5.5 V
- 5.5-V tolerant inputs
- 5.5-V power down protection output
- Matches the performance of TC74LCX series when operated at 3.3-V V_{CC} .

Marking

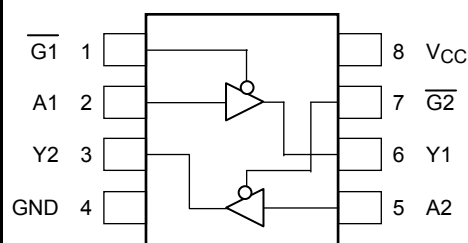


Weight
SSOP8-P-0.65 : 0.02 g (typ.)
SSOP8-P-0.50A : 0.01 g (typ.)

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	-0.5 to 6	V
DC input voltage	V_{IN}	-0.5 to 6	V
DC output voltage	V_{OUT}	-0.5 to 6 (Note 1)	V
		-0.5 to $V_{CC} + 0.5$ V (Note 2)	
Input diode current	I_{IK}	-20	mA
Output diode current	I_{OK}	-20 (Note 3)	mA
DC output current	I_{OUT}	± 50	mA
DC V_{CC} /ground current	I_{CC}	± 100	mA
Power dissipation	P_D	300 (SM8) 200 (US8)	mW
Storage temperature	T_{stg}	-65 to 150	$^\circ\text{C}$
Lead temperature (10s)	T_L	260	$^\circ\text{C}$

Pin Assignments (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0$ V

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

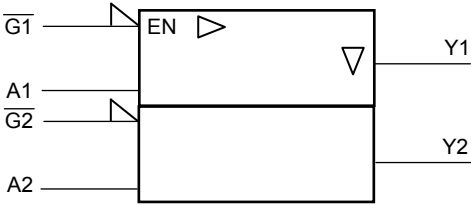
Note 3: $V_{OUT} < GND$

Truth Table

Inputs		Output
$\overline{G}$	A	Y
H	X	Z
L	L	L
L	H	H

X: Don't Care
Z: High Impedance

IEC Logic Symbol



Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	1.65 to 5.5	V
		1.5 to 5.5 (Note4)	
Input voltage	V_{IN}	0 to 5.5	V
Output voltage	V_{OUT}	0 to 5.5 (Note 5)	V
		0 to V_{CC} (Note 6)	
Operating temperature	T_{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 20 ($V_{CC} = 1.8\text{ V} \pm 0.15\text{ V}, 2.5\text{ V} \pm 0.2\text{ V}$)	ns/V
		0 to 10 ($V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$)	
		0 to 5 ($V_{CC} = 5.0\text{ V} \pm 0.5\text{ V}$)	

Note 4: Data retention only

Note 5: $V_{CC} = 0\text{ V}$

Note 6: High and Low state

Electrical Characteristics
DC Characteristics

Characteristics		Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit				
					VCC (V)	Min	Typ.	Max	Min		Max			
Input voltage	High level	VIH	—		1.65 to 1.95	VCC × 0.75	—	—	VCC × 0.75	—	V			
					2.3 to 5.5	VCC × 0.7	—	—	VCC × 0.7	—				
	Low level	VIL	—		1.65 to 1.95	—	—	VCC × 0.25	—	VCC × 0.25				
					2.3 to 5.5	—	—	VCC × 0.3	—	VCC × 0.3				
Output voltage	High level	VOH	VIN = VIH or VIL	IOH = −100 μA	1.65	1.55	1.65	—	1.55	—	V			
					2.3	2.2	2.3	—	2.2	—				
					3.0	2.9	3.0	—	2.9	—				
					4.5	4.4	4.5	—	4.4	—				
				IOH = −4 mA	1.65	1.29	1.52	—	1.29	—				
				IOH = −8 mA	2.3	1.9	2.15	—	1.9	—				
				IOH = −16 mA	3.0	2.4	2.8	—	2.4	—				
				IOH = −24 mA	3.0	2.3	2.68	—	2.3	—				
	IOH = −32 mA	4.5	3.8	4.2	—	3.8	—							
	Low level	VOL	VIN = VIH or VIL	IOL = 100 μA	1.65	—	0	0.1	—	0.1				
					2.3	—	0	0.1	—	0.1				
					3.0	—	0	0.1	—	0.1				
					4.5	—	0	0.1	—	0.1				
				IOL = 4 mA	1.65	—	0.08	0.24	—	0.24				
				IOL = 8 mA	2.3	—	0.1	0.3	—	0.3				
				IOL = 16 mA	3.0	—	0.15	0.4	—	0.4				
				IOL = 24 mA	3.0	—	0.22	0.55	—	0.55				
				IOL = 32 mA	4.5	—	0.22	0.55	—	0.55				
				Input leakage current		IIN	VIN = 5.5 V or GND		0 to 5.5	—		—	±1	—
3-state output off-state current				I0Z	VIN = VIH or VIL VOUT = 0 to 5.5V		1.65 to 5.5	—	—	±1	—	±10	μA	
Power off leakage current		I0FF	VIN or VOUT = 5.5 V		0.0	—	—	1	—	10	μA			
Quiescent supply current		I0CC	VIN = 5.5 V or GND		1.65 to 5.5	—	—	1	—	10	μA			

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition	Ta = 25°C				Ta = -40 to 85°C		Unit
			V _{CC} (V)	Min	Typ.	Max	Min	Max	
Propagation delay time	t_{pLH} t_{pHL}	$C_L = 15\text{ pF}, R_L = 1\text{M}\Omega$	1.8 ± 0.15	2.0	5.3	11.0	2.0	11.5	ns
			2.5 ± 0.2	0.8	3.4	7.5	0.8	8.0	
			3.3 ± 0.3	0.5	2.5	5.2	0.5	5.5	
			5.0 ± 0.5	0.5	2.1	4.5	0.5	4.8	
		$C_L = 50\text{ pF}, R_L = 500\Omega$	3.3 ± 0.3	1.5	3.2	5.7	1.5	6.0	
			5.0 ± 0.5	0.8	2.6	5.0	0.8	5.3	
Output enable time	t_{pZL} t_{pZH}	$C_L = 50\text{ pF}, R_L = 500\ \Omega$	1.8 ± 0.15	2.0	7.0	14.9	2.0	16.6	ns
			2.5 ± 0.2	1.5	4.6	8.5	1.5	9.0	
			3.3 ± 0.3	1.5	3.5	6.2	1.5	6.5	
			5.0 ± 0.5	0.8	2.8	5.5	0.8	5.8	
Output disable time	t_{pLZ} t_{pHZ}	$C_L = 50\text{ pF}, R_L = 500\ \Omega$	1.8 ± 0.15	2.0	5.4	11.8	2.0	12.7	ns
			2.5 ± 0.2	1.5	4.0	8.0	1.5	8.5	
			3.3 ± 0.3	1.0	3.5	5.7	1.0	6.0	
			5.0 ± 0.5	0.5	2.5	4.7	0.5	5.0	
Input capacitance	C _{IN}	—	0 to 5.5	—	4	—	—	—	pF
Output capacitance	C _{OUT}	—	0 to 5.5	—	4	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 7)	3.3	—	17	—	—	—	pF
			5.5	—	24	—	—	—	

Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

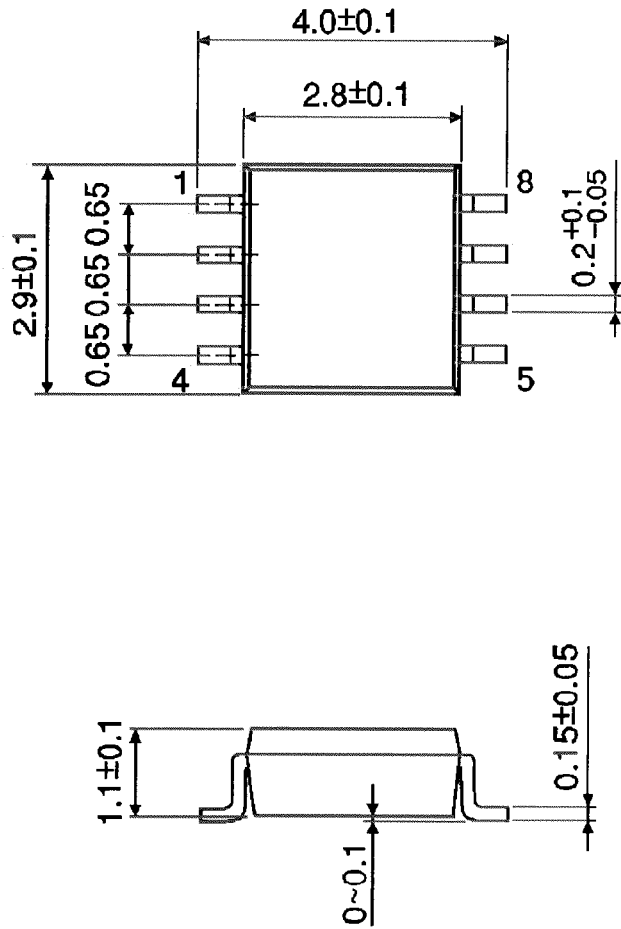
Average operating current can be obtained by the equation:

$$I_{CC(\text{opr.})} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$$

Package Dimensions

SSOP8-P-0.65

Unit : mm

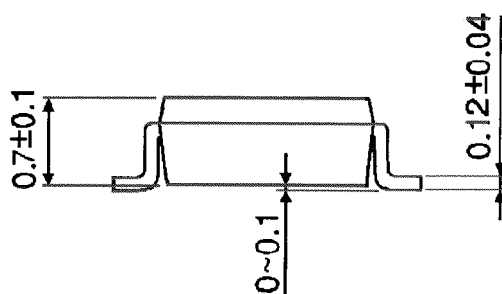
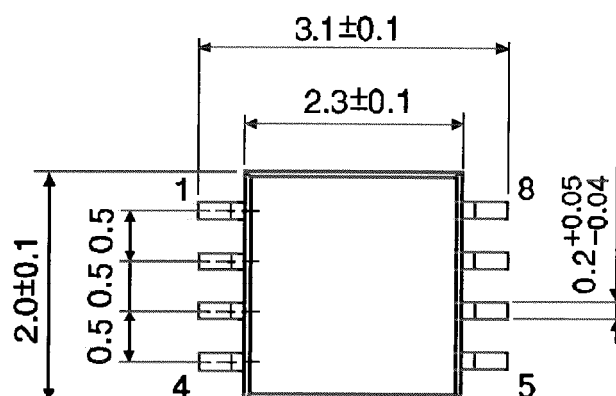


Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A

Unit : mm



Weight: 0.01 g (typ.)

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