

### SUMMARY

### Features

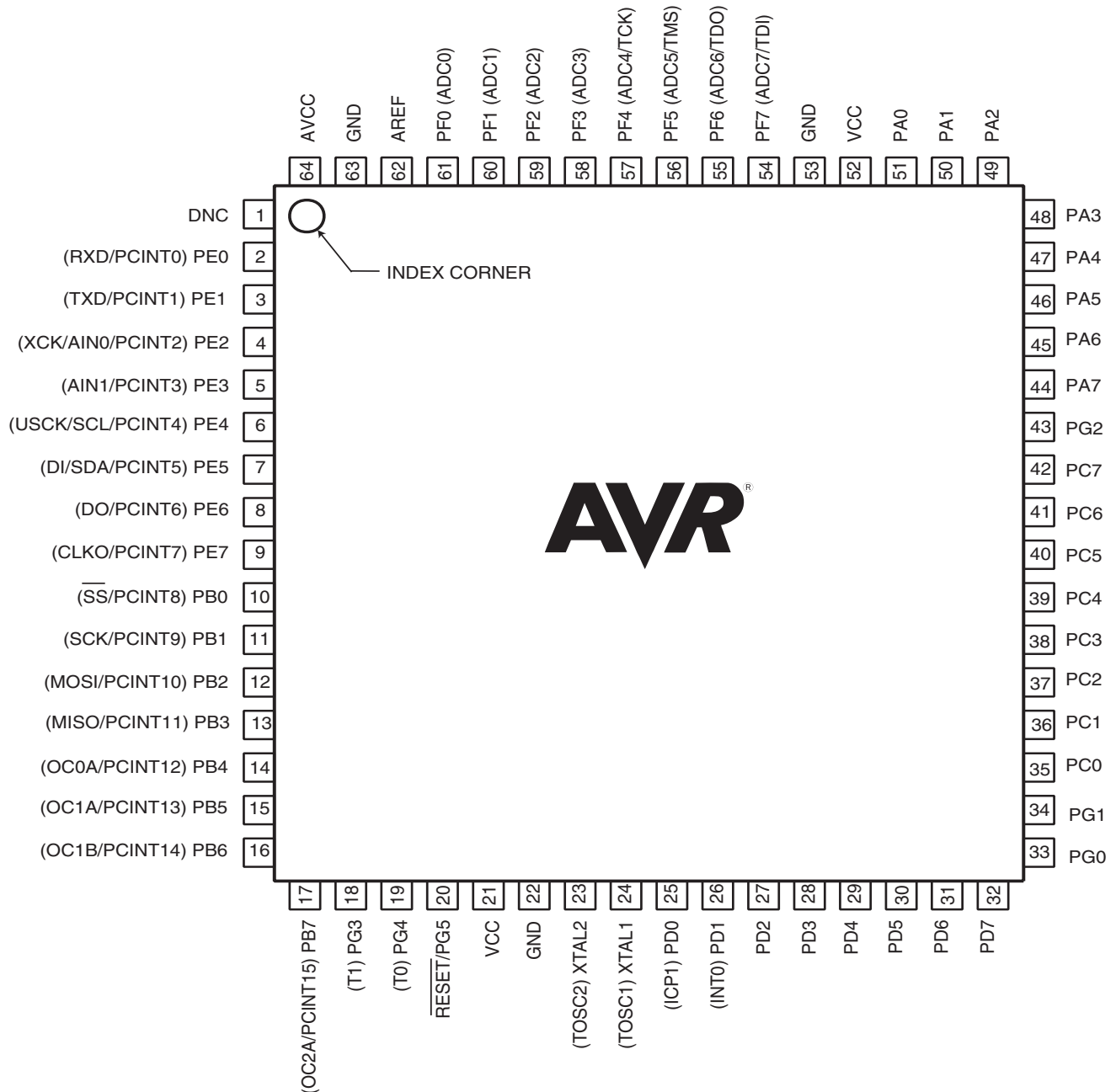
- High performance, low power Atmel® AVR® 8-Bit Microcontroller
- Advanced RISC architecture
  - 130 powerful instructions – most single clock cycle execution
  - 32 × 8 general purpose working registers
  - Fully static operation
  - Up to 16MIPS throughput at 16MHz (Atmel ATmega165PA/645P)
  - Up to 20MIPS throughput at 20MHz (Atmel ATmega165A/325A/325PA/645A/3250A/3250PA/6450A/6450P)
  - On-chip 2-cycle multiplier
- High endurance non-volatile memory segments
  - In-system self-programmable flash program memory
    - 16KBytes (ATmega165A/ATmega165PA)
    - 32KBytes (ATmega325A/ATmega325PA/ATmega3250A/ATmega3250PA)
    - 64KBytes (ATmega645A/ATmega645P/ATmega6450A/ATmega6450P)
  - EEPROM
    - 512Bytes (ATmega165A/ATmega165PA)
    - 1Kbytes (ATmega325A/ATmega325PA/ATmega3250A/ATmega3250PA)
    - 2Kbytes (ATmega645A/ATmega645P/ATmega6450A/ATmega6450P)
  - Internal SRAM
    - 1KBytes (ATmega165A/ATmega165PA)
    - 2KBytes (ATmega325A/ATmega325PA/ATmega3250A/ATmega3250PA)
    - 4KBytes (ATmega645A/ATmega645P/ATmega6450A/ATmega6450P)
  - Write/erase cycles: 10,000 flash/100,000 EEPROM
  - Data retention: 20 years at 85°C/100 years at 25°C <sup>(1)</sup>
  - Optional Boot Code Section with Independent Lock Bits
    - In-System Programming by On-chip Boot Program
    - True read-while-write operation
  - Programming lock for software security
- Atmel QTouch® library support
  - Capacitive touch buttons, sliders and wheels
  - Atmel QTouch and QMatrix acquisition
  - Up to 64 sense channels
- JTAG (IEEE std. 1149.1 compliant) interface
  - Boundary-scan capabilities according to the JTAG standard
  - Extensive on-chip debug support
  - Programming of Flash, EEPROM, Fuses, and Lock Bits through the JTAG Interface
- Peripheral Features
  - Two 8-bit Timer/Counters with Separate Prescaler and Compare Mode
  - One 16-bit Timer/Counter with Separate Prescaler, Compare Mode, and Capture Mode
  - Real time counter with separate oscillator
  - Four PWM channels
  - 8-channel, 10-bit ADC
  - Programmable serial USART
  - Master/Slave SPI Serial Interface
  - Universal Serial Interface with Start Condition detector
  - Programmable Watchdog Timer with separate on-chip oscillator
  - On-chip Analog Comparator
  - Interrupt and Wake-up on pin change
- Special microcontroller features
  - Power-on reset and programmable Brown-out detection
  - Internal calibrated oscillator
  - External and internal interrupt sources
  - Five sleep modes: Idle, ADC Noise Reduction, Power-save, Power-down and Standby
- I/O and packages
  - 54/69 programmable I/O lines
  - 64/100-lead TQFP, 64-pad QFN/MLF and 64-pad DRQFN
- Speed grade:
  - ATmega 165A/165PA/645A/645P: 0 - 16MHz @ 1.8 - 5.5V
  - ATmega325A/325PA/3250A/3250PA/6450A/6450P: 0 - 20MHz @ 1.8 - 5.5V
- Temperature range:
  - -40°C to 85°C industrial
- Ultra-low power consumption (picoPower® devices)
  - Active mode:
    - 1MHz, 1.8V: 215µA
    - 32kHz, 1.8V: 8µA (including oscillator)
  - Power-down mode: 0.1µA at 1.8V
  - Power-save mode: 0.6µA at 1.8V (Including 32kHz RTC)

Note: 1. Reliability Qualification results show that the projected data retention failure rate is much less than 1 PPM over 20 years at 85°C or 100 years at 25°C.

# 1. Pin configurations

## 1.1 Pinout - TQFP and QFN/MLF

**Figure 1-1.** 64A (TQFP) and 64M1 (QFN/MLF) pinout Atmel  
ATmega165A/ATmega165PA/ATmega325A/ATmega325PA/ATmega645A/ATmega645P.

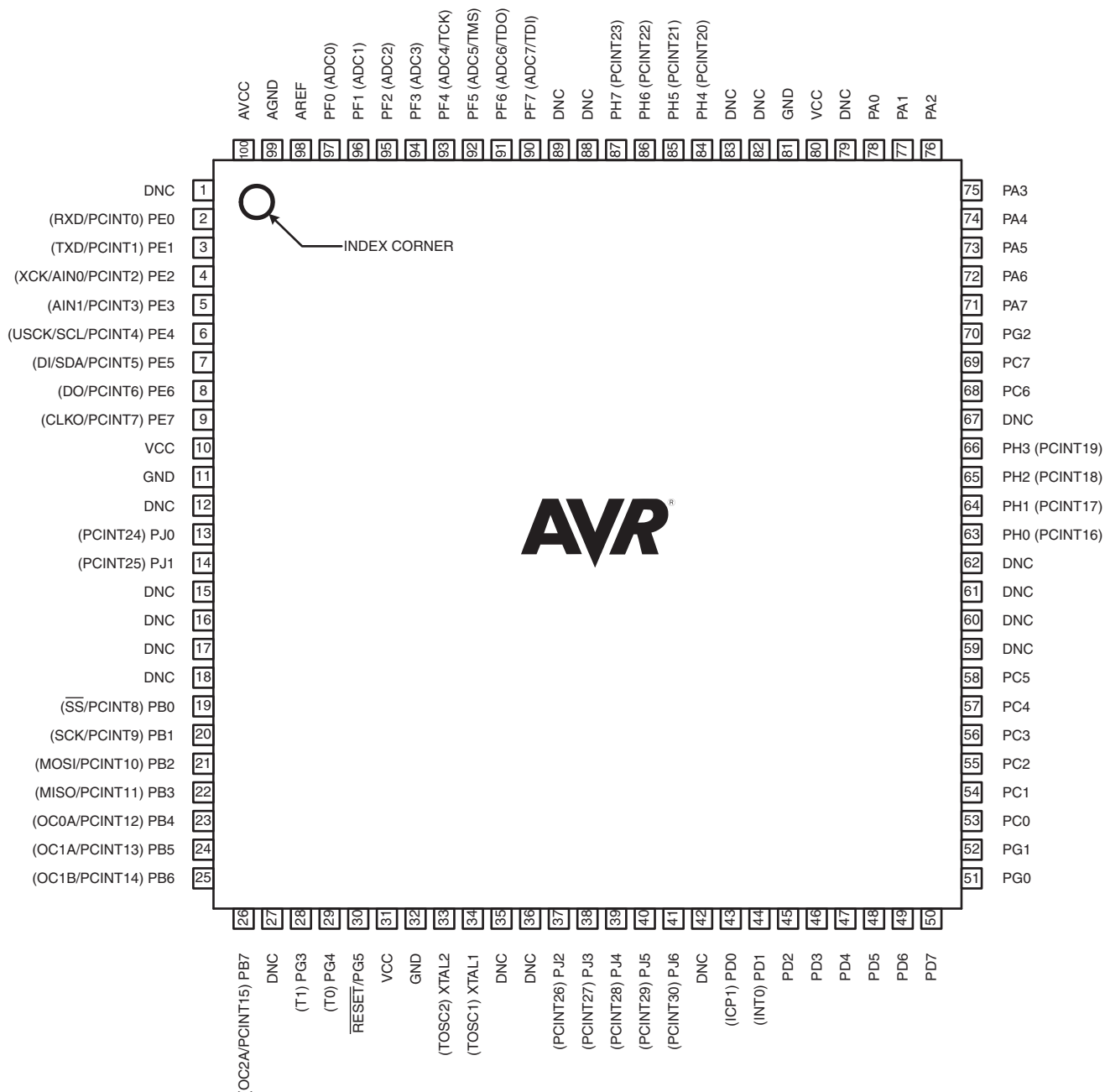


**Note:** The large center pad underneath the QFN/MLF packages is made of metal and internally connected to GND. It should be soldered or glued to the board to ensure good mechanical stability. If the center pad is left unconnected, the package might loosen from the board.

## 1.2 Pinout - 100A (TQFP)

Figure 1-2. Pinout Atmel ATmega3250A/ATmega3250PA/ATmega6450A/ATmega6450P.

### TQFP

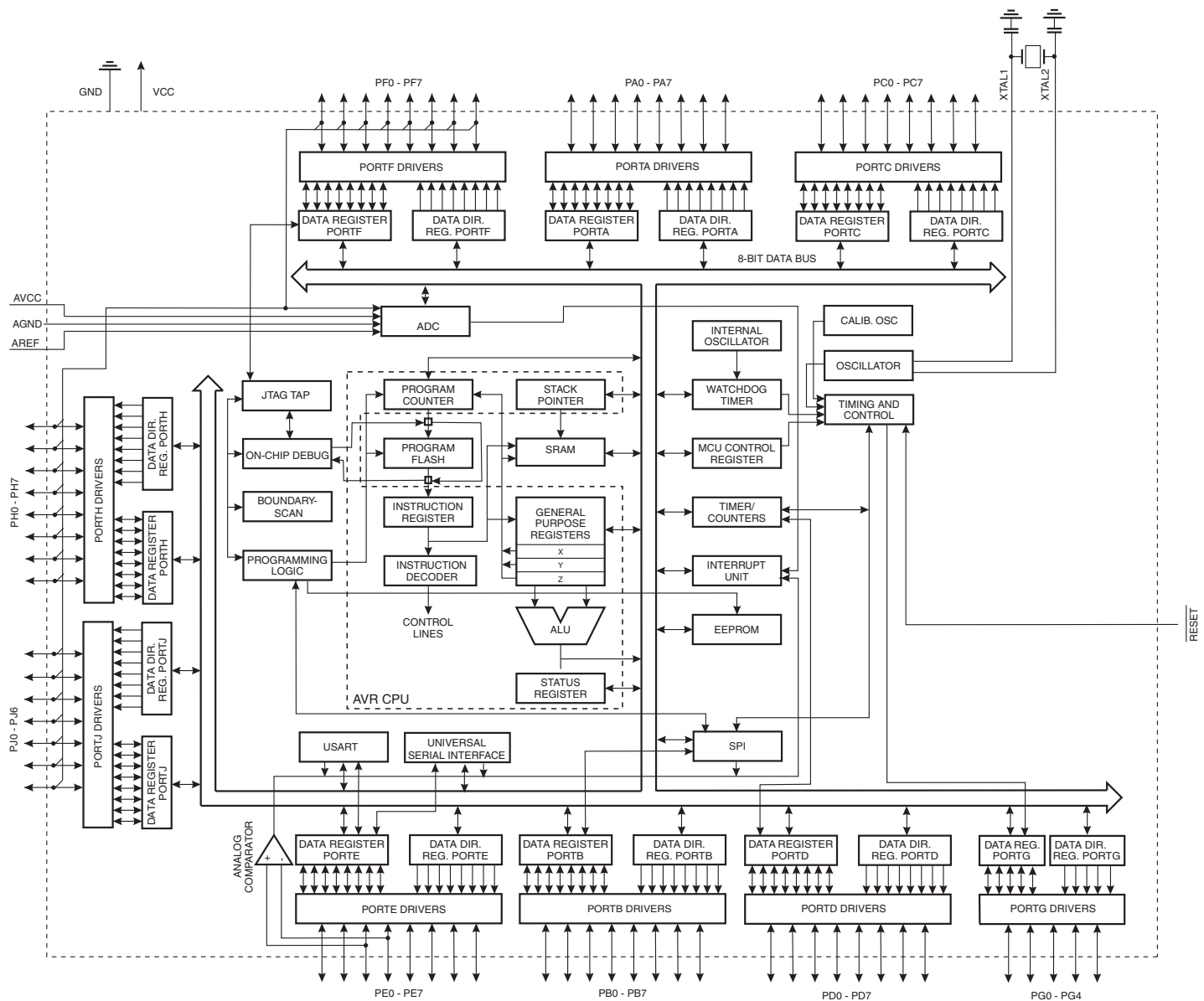


## 2. Overview

The Atmel ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P is a low-power CMOS 8-bit microcontroller based on the AVR enhanced RISC architecture. By executing powerful instructions in a single clock cycle, this microcontroller achieves throughputs approaching 1 MIPS per MHz allowing the system designer to optimize power consumption versus processing speed.

### 2.1 Block diagram

Figure 2-1. Block diagram.



The AVR core combines a rich instruction set with 32 general purpose working registers. All the 32 registers are directly connected to the Arithmetic Logic Unit (ALU), allowing two independent registers to be accessed in one single instruction executed in one clock cycle. The resulting architecture is more code efficient while achieving throughputs up to ten times faster than conventional CISC microcontrollers.

The Atmel ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P provides the following features: 16K/32K/64K bytes of In-System Programmable Flash with Read-While-Write capabilities, 512/1K/2K bytes EEPROM, 1K/2K/4K byte SRAM, 54/69 general purpose I/O lines, 32 general purpose working registers, a JTAG interface for Boundary-scan, On-chip Debugging support and programming, three flexible Timer/Counters with compare modes, internal and external interrupts, a serial programmable USART, Universal Serial Interface with Start Condition Detector, an 8-channel, 10-bit ADC, a programmable Watchdog Timer with internal Oscillator, an SPI serial port, and five software selectable power saving modes. The Idle mode stops the CPU while allowing the SRAM, Timer/Counters, SPI port, and interrupt system to continue functioning. The Power-down mode saves the register contents but freezes the Oscillator, disabling all other chip functions until the next interrupt or hardware reset. In Power-save mode, the asynchronous timer continues to run, allowing the user to maintain a timer base while the rest of the device is sleeping. The ADC Noise Reduction mode stops the CPU and all I/O modules except asynchronous timer and ADC, to minimize switching noise during ADC conversions. In Standby mode, the XTAL/resonator Oscillator is running while the rest of the device is sleeping. This allows very fast start-up combined with low-power consumption.

Atmel offers the QTouch® library for embedding capacitive touch buttons, sliders and wheels functionality into AVR microcontrollers. The patented charge-transfer signal acquisition offers robust sensing and includes fully debounced reporting of touch keys and includes Adjacent Key Suppression® (AKS®) technology for unambiguous detection of key events. The easy-to-use QTouch Suite toolchain allows you to explore, develop and debug your own touch applications.

The device is manufactured using Atmel's high density non-volatile memory technology. The On-chip ISP Flash allows the program memory to be reprogrammed In-System through an SPI serial interface, by a conventional non-volatile memory programmer, or by an On-chip Boot program running on the AVR core. The Boot program can use any interface to download the application program in the Application Flash memory. Software in the Boot Flash section will continue to run while the Application Flash section is updated, providing true Read-While-Write operation. By combining an 8-bit RISC CPU with In-System Self-Programmable Flash on a monolithic chip, the Atmel device is a powerful microcontroller that provides a highly flexible and cost effective solution to many embedded control applications.

The ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P AVR is supported with a full suite of program and system development tools including: C Compilers, Macro Assemblers, Program Debugger/Simulators, In-Circuit Emulators, and Evaluation kits.

## 2.2 Comparison between Atmel ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P

**Table 2-1.** Differences between: ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P.

| Device       | Flash    | EEPROM   | RAM    | MHz |
|--------------|----------|----------|--------|-----|
| ATmega165A   | 16Kbyte  | 512Bytes | 1Kbyte | 16  |
| ATmega165PA  | 16Kbyte  | 512Bytes | 1Kbyte | 16  |
| ATmega325A   | 32Kbyte  | 1Kbyte   | 2Kbyte | 20  |
| ATmega325PA  | 32Kbyte  | 1Kbyte   | 2Kbyte | 20  |
| ATmega3250A  | 32Kbytes | 1Kbyte   | 2Kbyte | 20  |
| ATmega3250PA | 32Kbyte  | 1Kbyte   | 2Kbyte | 20  |
| ATmega645A   | 64Kbyte  | 2Kbyte   | 4Kbyte | 16  |
| ATmega645P   | 64Kbyte  | 2Kbyte   | 4Kbyte | 16  |
| ATmega6450A  | 64Kbyte  | 2Kbyte   | 4Kbyte | 20  |
| ATmega6450P  | 64Kbyte  | 2Kbyte   | 4Kbyte | 20  |

## 2.3 Pin descriptions

### 2.3.1 VCC

Digital supply voltage.

### 2.3.2 GND

Ground.

### 2.3.3 Port A (PA7:PA0)

Port A is an 8-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port A output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port A pins that are externally pulled low will source current if the pull-up resistors are activated. The Port A pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port A also serves the functions of various special features of the ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P as listed on ["Alternate functions of Port B" on page 68](#).

### 2.3.4 Port B (PB7:PB0)

Port B is an 8-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port B output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port B pins that are externally pulled low will source current if the pull-up resistors are activated. The Port B pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port B has better driving capabilities than the other ports.

Port B also serves the functions of various special features of the ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P as listed on ["Alternate functions of Port B" on page 68](#).

### 2.3.5 Port C (PC7:PC0)

Port C is an 8-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port C output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port C pins that are

externally pulled low will source current if the pull-up resistors are activated. The Port C pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port C also serves the functions of special features of the Atmel ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P as listed on ["Alternate functions of Port D" on page 70](#).

### 2.3.6 Port D (PD7:PD0)

Port D is an 8-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port D output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port D pins that are externally pulled low will source current if the pull-up resistors are activated. The Port D pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port D also serves the functions of various special features of the ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P as listed on ["Alternate functions of Port D" on page 70](#).

### 2.3.7 Port E (PE7:PE0)

Port E is an 8-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port E output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port E pins that are externally pulled low will source current if the pull-up resistors are activated. The Port E pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port E also serves the functions of various special features of the ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P as listed on ["Alternate functions of Port E" on page 71](#).

### 2.3.8 Port F (PF7:PF0)

Port F serves as the analog inputs to the A/D Converter.

Port F also serves as an 8-bit bi-directional I/O port, if the A/D Converter is not used. Port pins can provide internal pull-up resistors (selected for each bit). The Port F output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port F pins that are externally pulled low will source current if the pull-up resistors are activated. The Port F pins are tri-stated when a reset condition becomes active, even if the clock is not running. If the JTAG interface is enabled, the pull-up resistors on pins PF7(TDI), PF5(TMS), and PF4(TCK) will be activated even if a reset occurs.

Port F also serves the functions of the JTAG interface, see ["Alternate functions of Port F" on page 73](#).

### 2.3.9 Port G (PG5:PG0)

Port G is a 6-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port G output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port G pins that are externally pulled low will source current if the pull-up resistors are activated. The Port G pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port G also serves the functions of various special features of the ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P as listed on [page 75](#).

### 2.3.10 Port H (PH7:PH0)

Port H is a 8-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port H output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port H pins that are externally pulled low will source current if the pull-up resistors are activated. The Port H pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port H also serves the functions of various special features of the ATmega3250A/3250PA/6450A/6450P as listed on [page 76](#).

### 2.3.11 Port J (PJ6:PJ0)

Port J is a 7-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port J output buffers have symmetrical drive characteristics with both high sink and source capability. As inputs, Port J pins that are externally pulled low will source current if the pull-up resistors are activated. The Port J pins are tri-stated when a reset condition becomes active, even if the clock is not running.

Port J also serves the functions of various special features of the Atmel ATmega3250A/3250PA/6450A/6450P as listed on [page 78](#).

### 2.3.12 $\overline{\text{RESET}}$

Reset input. A low level on this pin for longer than the minimum pulse length will generate a reset, even if the clock is not running. The minimum pulse length is given in [Table 28-13 on page 297](#). Shorter pulses are not guaranteed to generate a reset.

### 2.3.13 XTAL1

Input to the inverting Oscillator amplifier and input to the internal clock operating circuit.

### 2.3.14 XTAL2

Output from the inverting Oscillator amplifier.

### 2.3.15 AVCC

AVCC is the supply voltage pin for Port F and the A/D Converter. It should be externally connected to  $V_{CC}$ , even if the ADC is not used. If the ADC is used, it should be connected to  $V_{CC}$  through a low-pass filter.

### 2.3.16 AREF

This is the analog reference pin for the A/D Converter.

## 3. Ordering Information

### 3.1 ATmega165A

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                                                                                                                         | Package <sup>(1)</sup>                     | Operation Range                             |
|----------------------------|--------------|------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------|---------------------------------------------|
| 16                         | 1.8 - 5.5V   | ATmega165A-AU<br>ATmega165A-AUR <sup>(4)</sup><br>ATmega165A-MU<br>ATmega165A-MUR <sup>(4)</sup><br>ATmega165A-MCH<br>ATmega165A-MCHR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1<br>64MC<br>64MC | Industrial<br>(-40°C to 85°C)               |
|                            |              | ATmega165A-AN<br>ATmega165A-ANR <sup>(4)</sup><br>ATmega165A-MN<br>ATmega165A-MNR <sup>(4)</sup>                                                     | 64A<br>64A<br>64M1<br>64M1                 | Extended<br>(-40°C to 105°C) <sup>(5)</sup> |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel
  5. See characterization specifications at 105°C.

| Package Type |                                                                                                          |
|--------------|----------------------------------------------------------------------------------------------------------|
| <b>64A</b>   | 64-Lead, Thin (1.0mm) Plastic Gull Wing Quad Flat Package (TQFP)                                         |
| <b>64M1</b>  | 64-pad, 9 x 9 x 1.0mm body, lead pitch 0.50mm, Quad Flat No-Lead/Micro Lead Frame Package (QFN/MLF)      |
| <b>64MC</b>  | 64-lead (2-row Staggered), 7 x 7 x 1.0 mm body, 4.0 x 4.0mm Exposed Pad, Quad Flat No-Lead Package (QFN) |

## 3.2 ATmega165PA

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                                                                                                                               | Package <sup>(1)</sup>                     | Operation Range                             |
|----------------------------|--------------|------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------|---------------------------------------------|
| 16                         | 1.8 - 5.5V   | ATmega165PA-AU<br>ATmega165PA-AUR <sup>(4)</sup><br>ATmega165PA-MU<br>ATmega165PA-MUR <sup>(4)</sup><br>ATmega165PA-MCH<br>ATmega165PA-MCHR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1<br>64MC<br>64MC | Industrial<br>(-40°C to 85°C)               |
|                            |              | ATmega165PA-AN<br>ATmega165PA-ANR <sup>(4)</sup><br>ATmega165PA-MN<br>ATmega165PA-MNR <sup>(4)</sup>                                                       | 64A<br>64A<br>64M1<br>64M1                 | Extended<br>(-40°C to 105°C) <sup>(5)</sup> |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel.
  5. See characterization specifications at 105°C.

| Package Type |                                                                                                          |
|--------------|----------------------------------------------------------------------------------------------------------|
| <b>64A</b>   | 64-Lead, Thin (1.0mm) Plastic Gull Wing Quad Flat Package (TQFP)                                         |
| <b>64M1</b>  | 64-pad, 9 x 9 x 1.0mm body, lead pitch 0.50mm, Quad Flat No-Lead/Micro Lead Frame Package (QFN/MLF)      |
| <b>64MC</b>  | 64-lead (2-row Staggered), 7 x 7 x 1.0mm body, 4.0 x 4.0 mm Exposed Pad, Quad Flat No-Lead Package (QFN) |

### 3.3 ATmega325A

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                                                                     | Package <sup>(1)</sup>     | Operation Range                             |
|----------------------------|--------------|--------------------------------------------------------------------------------------------------|----------------------------|---------------------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega325A-AU<br>ATmega325A-AUR <sup>(4)</sup><br>ATmega325A-MU<br>ATmega325A-MUR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1 | Industrial<br>(-40°C to 85°C)               |
|                            |              | ATmega325A-AN<br>ATmega325A-ANR <sup>(4)</sup><br>ATmega325A-MN<br>ATmega325A-MNR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1 | Extended<br>(-40°C to 105°C) <sup>(5)</sup> |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel
  5. See characterizations specifications at 105°C.

| Package Type |                                                                                                     |
|--------------|-----------------------------------------------------------------------------------------------------|
| <b>64A</b>   | 64-Lead, Thin (1.0mm) Plastic Gull Wing Quad Flat Package (TQFP)                                    |
| <b>64M1</b>  | 64-pad, 9 x 9 x 1.0mm body, lead pitch 0.50mm, Quad Flat No-Lead/Micro Lead Frame Package (QFN/MLF) |

### 3.4 ATmega325PA

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                                                                         | Package <sup>(1)</sup>     | Operation Range                             |
|----------------------------|--------------|------------------------------------------------------------------------------------------------------|----------------------------|---------------------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega325PA-AU<br>ATmega325PA-AUR <sup>(4)</sup><br>ATmega325PA-MU<br>ATmega325PA-MUR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1 | Industrial<br>(-40°C to 85°C)               |
|                            |              | ATmega325PA-AN<br>ATmega325PA-ANR <sup>(4)</sup><br>ATmega325PA-MN<br>ATmega325PA-MNR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1 | Extended<br>(-40°C to 105°C) <sup>(5)</sup> |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel
  5. See characterization specifications at 105°C.

| Package Type |                                                                                                     |
|--------------|-----------------------------------------------------------------------------------------------------|
| <b>64A</b>   | 64-Lead, Thin (1.0mm) Plastic Gull Wing Quad Flat Package (TQFP)                                    |
| <b>64M1</b>  | 64-pad, 9 x 9 x 1.0mm body, lead pitch 0.50mm, Quad Flat No-Lead/Micro Lead Frame Package (QFN/MLF) |

### 3.5 ATmega3250A

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                     | Package <sup>(1)</sup> | Operation Range                             |
|----------------------------|--------------|--------------------------------------------------|------------------------|---------------------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega3250A-AU<br>ATmega3250A-AUR <sup>(4)</sup> | 100A<br>100A           | Industrial<br>(-40°C to 85°C)               |
|                            |              | ATmega3250A-AN<br>ATmega3250A-ANR <sup>(4)</sup> | 100A<br>100A           | Extended<br>(-40°C to 105°C) <sup>(5)</sup> |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel
  5. See characterization specifications at 105°C.

| Package Type |                                                                                            |
|--------------|--------------------------------------------------------------------------------------------|
| <b>100A</b>  | 100-lead, 14 x 14 x 1.0mm, 0.5mm Lead Pitch, Thin Profile Plastic Quad Flat Package (TQFP) |

### 3.6 ATmega3250PA

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>    | Package <sup>(1)</sup> | Operation Range                             |
|----------------------------|--------------|---------------------------------|------------------------|---------------------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega3250PA-AU                 | 100A                   | Industrial<br>(-40°C to 85°C)               |
|                            |              | ATmega3250PA-AUR <sup>(4)</sup> | 100A                   |                                             |
|                            |              | ATmega3250PA-AN                 | 100A                   | Extended<br>(-40°C to 105°C) <sup>(5)</sup> |
|                            |              | ATmega3250PA-ANR <sup>(4)</sup> | 100A                   |                                             |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel
  5. See characterization specifications at 105°C.

| Package Type |                                                                                            |
|--------------|--------------------------------------------------------------------------------------------|
| <b>100A</b>  | 100-lead, 14 x 14 x 1.0mm, 0.5mm Lead Pitch, Thin Profile Plastic Quad Flat Package (TQFP) |

### 3.7 ATmega645A

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                                                                     | Package <sup>(1)</sup>     | Operation Range               |
|----------------------------|--------------|--------------------------------------------------------------------------------------------------|----------------------------|-------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega645A-AU<br>ATmega645A-AUR <sup>(4)</sup><br>ATmega645A-MU<br>ATmega645A-MUR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1 | Industrial<br>(-40°C to 85°C) |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel

| Package Type |                                                                                                     |
|--------------|-----------------------------------------------------------------------------------------------------|
| <b>64A</b>   | 64-Lead, Thin (1.0mm) Plastic Gull Wing Quad Flat Package (TQFP)                                    |
| <b>64M1</b>  | 64-pad, 9 x 9 x 1.0mm body, lead pitch 0.50mm, Quad Flat No-Lead/Micro Lead Frame Package (QFN/MLF) |

### 3.8 ATmega645P

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                                                                     | Package <sup>(1)</sup>     | Operation Range               |
|----------------------------|--------------|--------------------------------------------------------------------------------------------------|----------------------------|-------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega645P-AU<br>ATmega645P-AUR <sup>(4)</sup><br>ATmega645P-MU<br>ATmega645P-MUR <sup>(4)</sup> | 64A<br>64A<br>64M1<br>64M1 | Industrial<br>(-40°C to 85°C) |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel

| Package Type |                                                                                                     |
|--------------|-----------------------------------------------------------------------------------------------------|
| <b>64A</b>   | 64-Lead, Thin (1.0mm) Plastic Gull Wing Quad Flat Package (TQFP)                                    |
| <b>64M1</b>  | 64-pad, 9 x 9 x 1.0mm body, lead pitch 0.50mm, Quad Flat No-Lead/Micro Lead Frame Package (QFN/MLF) |

### 3.9 ATmega6450A

| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                     | Package <sup>(1)</sup> | Operation Range               |
|----------------------------|--------------|--------------------------------------------------|------------------------|-------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega6450A-AU<br>ATmega6450A-AUR <sup>(4)</sup> | 100A<br>100A           | Industrial<br>(-40°C to 85°C) |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel

| Package Type |                                                                                            |
|--------------|--------------------------------------------------------------------------------------------|
| 100A         | 100-lead, 14 x 14 x 1.0mm, 0.5mm Lead Pitch, Thin Profile Plastic Quad Flat Package (TQFP) |

### 3.10 ATmega6450P

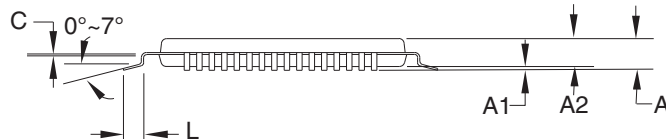
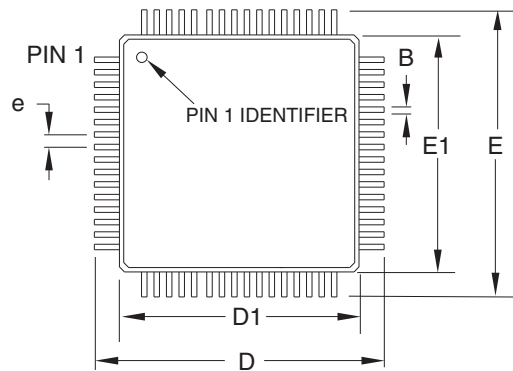
| Speed (MHz) <sup>(3)</sup> | Power Supply | Ordering Code <sup>(2)</sup>                     | Package <sup>(1)</sup> | Operation Range               |
|----------------------------|--------------|--------------------------------------------------|------------------------|-------------------------------|
| 20                         | 1.8 - 5.5V   | ATmega6450P-AU<br>ATmega6450P-AUR <sup>(4)</sup> | 100A<br>100A           | Industrial<br>(-40°C to 85°C) |

- Notes:
1. This device can also be supplied in wafer form. Please contact your local Atmel sales office for detailed ordering information and minimum quantities.
  2. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also Halide free and fully Green.
  3. For Speed vs.  $V_{CC}$ , see [Figure 28-1 on page 295](#).
  4. Tape & Reel

| Package Type |                                                                                            |
|--------------|--------------------------------------------------------------------------------------------|
| <b>100A</b>  | 100-lead, 14 x 14 x 1.0mm, 0.5mm Lead Pitch, Thin Profile Plastic Quad Flat Package (TQFP) |

## 4. Packaging Information

### 4.1 64A



**COMMON DIMENSIONS**  
(Unit of measure = mm)

| SYMBOL | MIN      | NOM   | MAX   | NOTE   |
|--------|----------|-------|-------|--------|
| A      | —        | —     | 1.20  |        |
| A1     | 0.05     | —     | 0.15  |        |
| A2     | 0.95     | 1.00  | 1.05  |        |
| D      | 15.75    | 16.00 | 16.25 |        |
| D1     | 13.90    | 14.00 | 14.10 | Note 2 |
| E      | 15.75    | 16.00 | 16.25 |        |
| E1     | 13.90    | 14.00 | 14.10 | Note 2 |
| B      | 0.30     | —     | 0.45  |        |
| C      | 0.09     | —     | 0.20  |        |
| L      | 0.45     | —     | 0.75  |        |
| e      | 0.80 TYP |       |       |        |

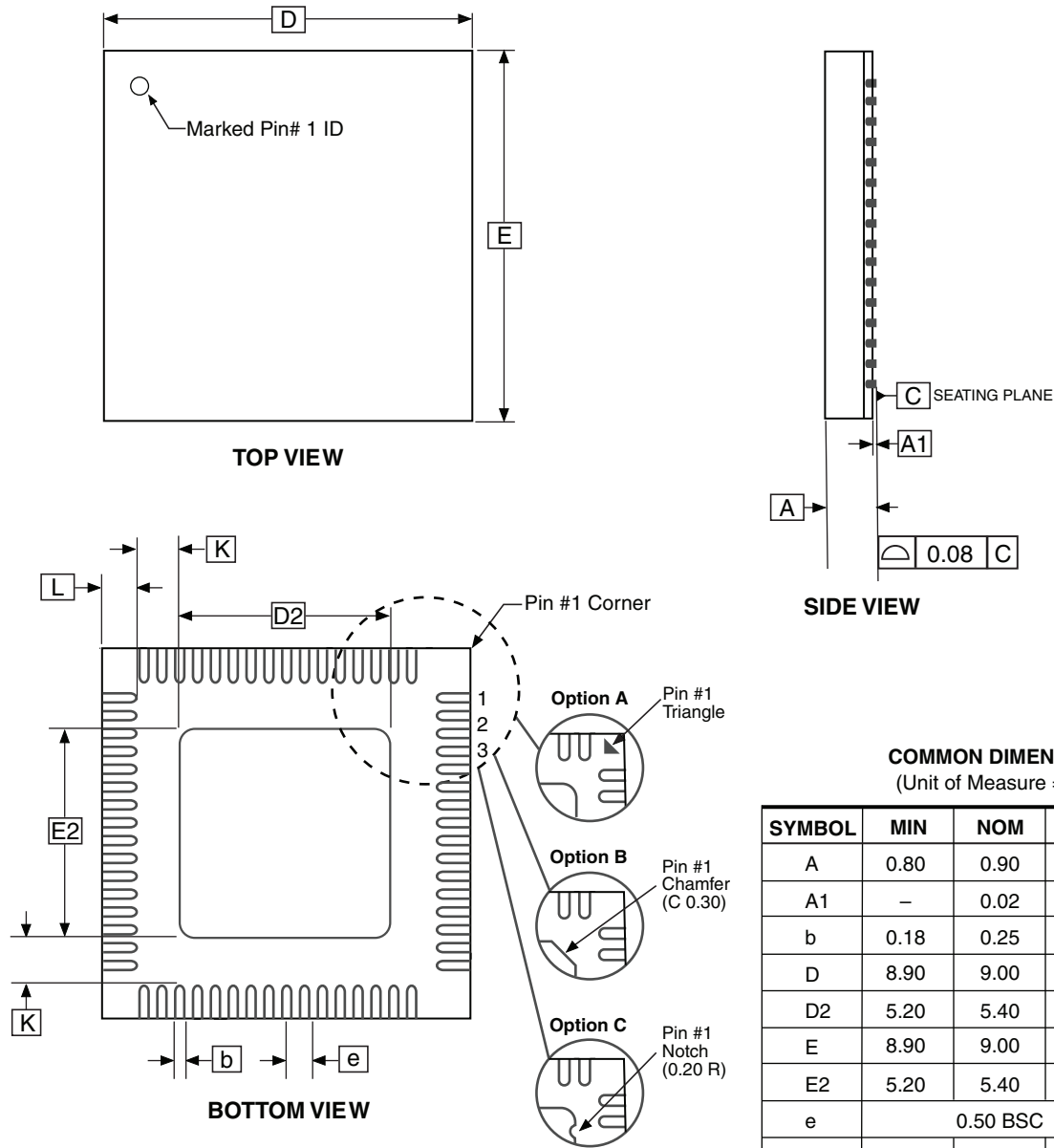
**Notes:**

1. This package conforms to JEDEC reference MS-026, Variation AEB.
2. Dimensions D1 and E1 do not include mold protrusion. Allowable protrusion is 0.25mm per side. Dimensions D1 and E1 are maximum plastic body size dimensions including mold mismatch.
3. Lead coplanarity is 0.10mm maximum.

2010-10-20

|                                                                                                                                |                                                                                                                                                     |                    |             |
|--------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|-------------|
|  2325 Orchard Parkway<br>San Jose, CA 95131 | <b>TITLE</b><br><b>64A</b> , 64-lead, 14 x 14mm Body Size, 1.0mm Body Thickness,<br>0.8mm Lead Pitch, Thin Profile Plastic Quad Flat Package (TQFP) | <b>DRAWING NO.</b> | <b>REV.</b> |
|                                                                                                                                |                                                                                                                                                     | 64A                | C           |

## 4.2 64M1



**Notes:**

1. JEDEC Standard MO-220, (SAW Singulation) Fig. 1, VMMD.
2. Dimension and tolerance conform to ASMEY14.5M-1994.

2010-10-19



2325 Orchard Parkway  
San Jose, CA 95131

**TITLE**

**64M1**, 64-pad, 9 x 9 x 1.0 mm Body, Lead Pitch 0.50 mm,  
5.40 mm Exposed Pad, Micro Lead Frame Package (MLF)

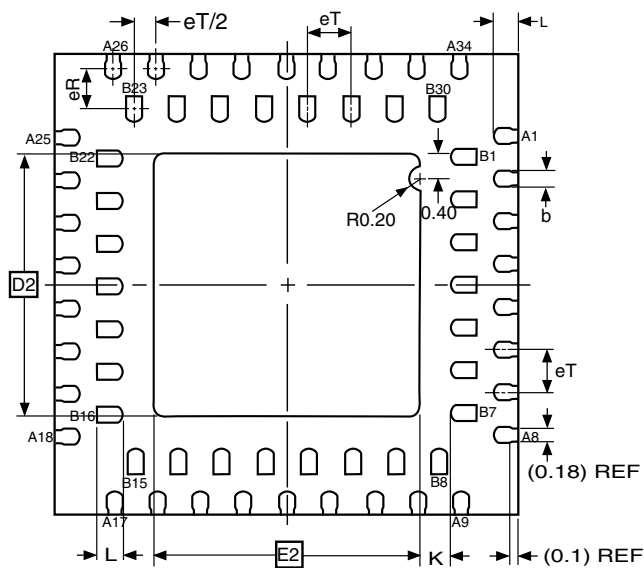
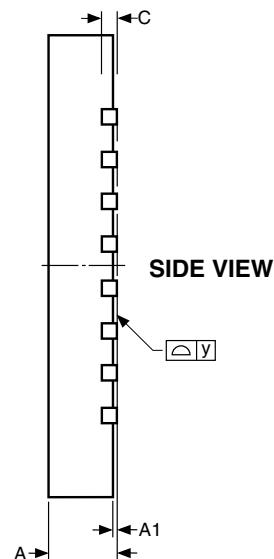
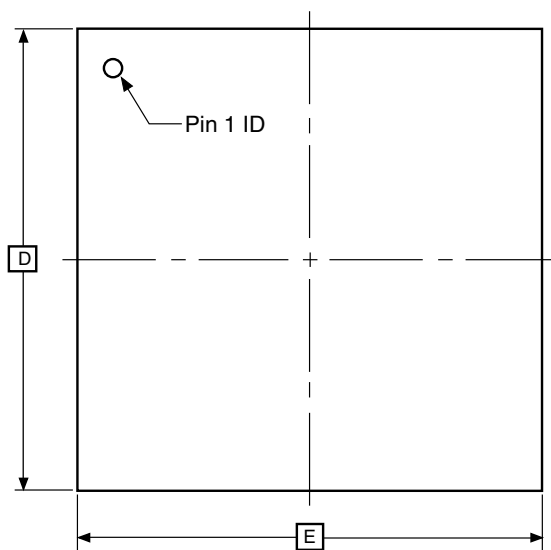
**DRAWING NO.**

64M1

**REV.**

H

## 4.3 64MC



**COMMON DIMENSIONS**  
(Unit of Measure = mm)

| SYMBOL | MIN      | NOM  | MAX   | NOTE  |
|--------|----------|------|-------|-------|
| A      | 0.80     | 0.90 | 1.00  |       |
| A1     | 0.00     | 0.02 | 0.05  |       |
| b      | 0.18     | 0.23 | 0.28  |       |
| C      | 0.20 REF |      |       |       |
| D      | 6.90     | 7.00 | 7.10  |       |
| D2     | 3.95     | 4.00 | 4.05  |       |
| E      | 6.90     | 7.00 | 7.10  |       |
| E2     | 3.95     | 4.00 | 4.05  |       |
| eT     | —        | 0.65 | —     |       |
| eR     | —        | 0.65 | —     |       |
| K      | 0.20     | —    | —     | (REF) |
| L      | 0.35     | 0.40 | 0.45  |       |
| y      | 0.00     | —    | 0.075 |       |

Note: 1. The terminal #1 ID is a Laser-marked Feature.

10/3/07



**Package Drawing Contact:**  
packagedrawings@atmel.com

**TITLE**  
**64MC**, 64QFN (2-Row Staggered),  
7 x 7 x 1.00 mm Body, 4.0 x 4.0 mm Exposed Pad,  
Quad Flat No Lead Package

**GPC**

ZXC

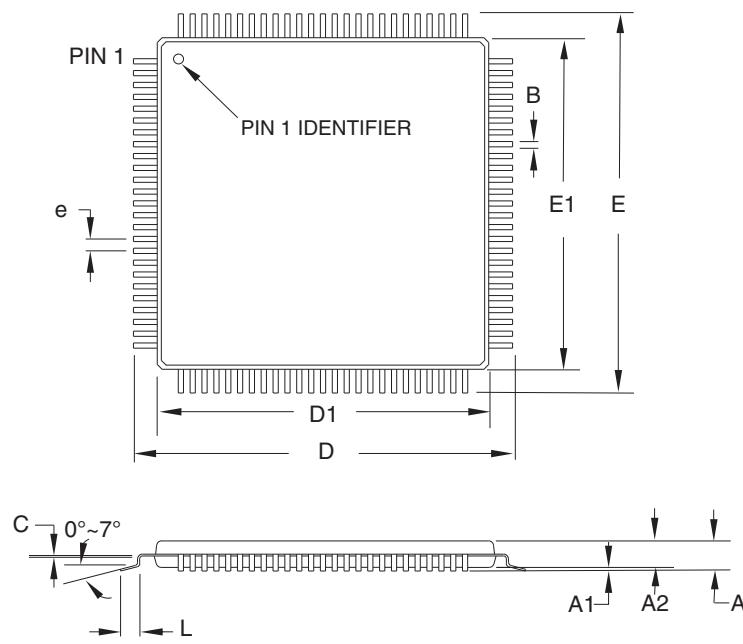
**DRAWING NO.**

64MC

**REV.**

A

4.4 100A



COMMON DIMENSIONS  
(Unit of Measure = mm)

| SYMBOL | MIN      | NOM   | MAX   | NOTE   |
|--------|----------|-------|-------|--------|
| A      | –        | –     | 1.20  |        |
| A1     | 0.05     | –     | 0.15  |        |
| A2     | 0.95     | 1.00  | 1.05  |        |
| D      | 15.75    | 16.00 | 16.25 |        |
| D1     | 13.90    | 14.00 | 14.10 | Note 2 |
| E      | 15.75    | 16.00 | 16.25 |        |
| E1     | 13.90    | 14.00 | 14.10 | Note 2 |
| B      | 0.17     | –     | 0.27  |        |
| C      | 0.09     | –     | 0.20  |        |
| L      | 0.45     | –     | 0.75  |        |
| e      | 0.50 TYP |       |       |        |

- Notes:
1. This package conforms to JEDEC reference MS-026, Variation AED.
  2. Dimensions D1 and E1 do not include mold protrusion. Allowable protrusion is 0.25mm per side. Dimensions D1 and E1 are maximum plastic body size dimensions including mold mismatch.
  3. Lead coplanarity is 0.08mm maximum.

2010-10-20

|                                                                                                                                |                                                                                                                                                       |                    |             |
|--------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|-------------|
|  2325 Orchard Parkway<br>San Jose, CA 95131 | <b>TITLE</b><br><b>100A</b> , 100-lead, 14 x 14mm Body Size, 1.0mm Body Thickness,<br>0.5mm Lead Pitch, Thin Profile Plastic Quad Flat Package (TQFP) | <b>DRAWING NO.</b> | <b>REV.</b> |
|                                                                                                                                |                                                                                                                                                       | 100A               | D           |

## 5. Errata

### 5.1 ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P Rev. G

No known errata.

### 5.2 ATmega165A/165PA/325A/325PA/3250A/3250PA/645A/645P/6450A/6450P Rev. A to F

Not sampled.



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